

# In order to load this configuration information in a pre-3.64.50  
 # version of IG-XL, it must be converted to the version 0 format.  
 # To convert this file from version 1 to version 0:  
 # 1. Make a backup of this file.  
 # 2. Copy the information between the tags <TEST\_HEAD TH 1> and </TEST\_HEAD TH 1>,  
 # paste it into a new file, and save that file as SimulatedConfig.txt in the current test program's  
 directory.  
 # 3. Copy the information between the tags <BACKPLANE-DIB\_CABLES TH 1> and </BACKPLANE-  
 DIB\_CABLES TH 1>,  
 # paste it into a new file, and save that file as DibSlotConfig.txt in the current test program's  
 directory.

<Version>  
 1  
 </Version>

<TEST\_HEAD TH 1>

#slot[.subslot]	Type	idprom (type serial rev company)
1.0	BBAC-15 aka: 805-004-02	805-004-50 0305B7F4 1041-E 5445
	BBAC-15 aka: 949-977-02	949-977-03 0305B142 0819-E 5445
	BBAC-15Rider PowerBoard aka: 949-984-00	949-987-02 03058D76 0739-D 5445 949-984-04 0305B05A 0949-E 5445
2.0	DC-90 aka: 805-229-00,805-229-06	805-229-50 0C156CAE 1225-C 5445
	DC-90 aka: 939-229-00	939-229-06 0C156AC2 1150-C 5445
	PowerBoard	939-228-00 0C17D17D 1225-D 5445
2.1	DC-90PowerSupply aka: 805-230-00	805-230-30 002B4735 0439-B 5445
	DC-90PowerSupply aka: 939-230-00	939-230-03 002AF137 0446-B 5445
3.0	DC-30 aka: 805-002-00,805-002-03,805-002-50	805-002-60 0600ECB3 1133-B 5445
	DC-30 aka: 949-901-00,949-901-05	949-901-60 0600EB86 0808-B 5445
	DC-30Rider aka: 949-904-00,949-904-01	949-904-60 0600EBE5 0808-B 5445
	PowerBoard aka: 949-955-00	949-955-02 0600D208 0531-E 5445
4.0	HVD-1 aka: 805-740-24	805-740-50 0C33DE1A 1735-C 5445
	HVD-1	939-273-00 0C34AEBB 1042-C 5445
4.1	HVDModuleBoard-0 aka: 805-740-24	805-740-50 0C33DE1A 1735-C 5445

	HVDMModuleBoard-0	939-274-10 0C34E2A2 1735-C 5445
	aka: 939-274-00	
4.2	HVDMModuleBoard-1	805-740-50 0C33DE1A 1735-C 5445
	aka: 805-740-24	
	HVDMModuleBoard-1	939-274-10 0C34E2AA 1735-C 5445
	aka: 939-274-00	
4.3	HVDMModuleBoard-2	805-740-50 0C33DE1A 1735-C 5445
	aka: 805-740-24	
	HVDMModuleBoard-2	939-274-10 0C34E294 1735-C 5445
	aka: 939-274-00	
5.0	DC-30	805-002-60 0C0178E8 1206-E 5445
	aka: 805-002-00,805-002-03,805-002-50	
	DC-30	949-901-60 0C0178A3 1205-E 5445
	aka: 949-901-00,949-901-05	
	DC-30Rider	949-904-60 0C01B89F 1205-E 5445
	aka: 949-904-00,949-904-01	
	PowerBoard	949-955-02 0C0177E3 0644-E 5445
	aka: 949-955-00	
6.0	VHFAC	805-245-50 03012B8E 0707-E 5445
	aka: 805-245-02,805-245-03	
	VHFAC	939-245-10 03012826 0707-E 5445
	aka: 939-245-00	
	VHFACCapture0	939-244-00 030127F8 0552-C 5445
	VHFACCapture1	939-244-00 030127F8 0552-C 5445
	VHFACSource4	939-247-10 0301BE08 0613-C 5445
	VHFACSource5	939-247-10 0301BE08 0613-C 5445
	VHFACTime	939-245-10 03012826 0707-E 5445
	aka: 939-245-00	
7.0	SupportBoard	805-003-50 0C0FA9AF 1223-B 5445
	aka: 805-003-00,805-003-20,805-003-30	
	SupportBoard	949-909-00 0C0F999F 1223-B 5445
	DCCalModule	949-938-12 0C0F97CA 0912-C 5445
	aka: 949-938-01	
	PowerBoard	949-910-30 0C0F96AA 1121-C 5445
	aka: 949-910-00,949-910-20	
7.1	DSP-1	810-503-00 03031F22 0423-B 5445
	DSP-1	949-939-00 03031F22 0423-B 5445
8.0	HSD-200	805-251-50 0C006192 1231-D 5445
	aka: 805-251-20	
	HSD-200	939-251-30 0C00458C 1231-D 5445
	aka: 939-251-20	
	PowerBoard	939-253-01 0C0048F7 1037-C 5445
	aka: 939-253-00	
9.0	DC-30	805-002-60 03014654 1206-B 5445
	aka: 805-002-00,805-002-03,806-002-50	
	DC-30	949-901-60 0301AD3A 0808-B 5445
	aka: 949-901-00,949-901-05	
	PowerBoard	949-955-02 0301AD58 0531-E 5445

aka: 949-955-00  
 DC-30Rider 949-904-60 03018EE6 0808-B 5445  
 aka: 949-904-00,949-904-01  
 10.0 DC-90 805-229-50 0C311FAE 1233-C 5445  
 aka: 805-229-06,805-229-00  
 DC-90 939-229-06 0C311EDE 1150-C 5445  
 aka: 939-229-00  
 PowerBoard 939-228-00 0C310B72 1225-D 5445  
 10.1 DC-90PowerSupply 805-230-06 0C1910C9 1115-B 5445  
 aka: 805-230-00  
 DC-90PowerSupply 939-230-06 0C191B48 1115-B 5445  
 aka: 939-230-00  
 11.0 DC-90 805-229-50 0C130F47 1225-C 5445  
 aka: 805-229-00,805-229-06  
 DC-90 939-229-06 0C125A72 1127-C 5445  
 aka: 939-229-00  
 PowerBoard 939-228-00 0C12CAF8 1225-D 5445  
 11.1 DC-90PowerSupply 805-230-30 002B4816 0439-B 5445  
 aka: 805-230-00  
 DC-90PowerSupply 939-230-03 002AF13E 0446-B 5445  
 aka: 939-230-00  
 12.0 DC-30 805-002-60 0C351261 1505-E 5445  
 aka: 805-002-00,805-002-03,805-002-50  
 DC-30 949-901-60 0C35D71B 1418-E 5445  
 aka: 949-901-00,949-901-05  
 PowerBoard 949-955-02 0C35D7DE 1505-E 5445  
 aka: 949-955-00  
 DC-30Rider 949-904-60 0C360002 1418-E 5445  
 aka: 949-904-00,949-904-01  
 13.0 BBAC-15 805-004-50 03028AE8 1041-E 5445  
 aka: 805-004-02  
 BBAC-15 949-977-03 0302695A 0819-E 5445  
 aka: 949-977-02  
 BBAC-15Rider 949-987-02 030289A7 0739-D 5445  
 PowerBoard 949-984-04 03026966 0633-E 5445  
 aka: 949-984-00  
 20.0 SupportBoard-USM 939-270-00 002AF110 0549-B 5445  
 aka: 805-003-02  
 SupportBoard-USM 939-270-00 002AF110 0549-B 5445

</TEST\_HEAD TH 1>

<BACKPLANE-DIB\_CABLES TH 1>

1	2
2	3
3	4
4	5
5	7
6	9

```

7          8, 20
8          17
9          18
10         21
11         23
12         24
13         25
14         50
15         49
16         48
17         42
18         40
19         38
20         45, 33
21         32
22         31
23         30
24         29
25         28
26         27

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</BACKPLANE-DIB\_CABLES TH 1>

<LICENSE\_ENABLERS>

#Off-line enabling levels

#FEATURE	ENABLED	LEVEL	LICENSE COUNT	LEGAL ENABLING POINTS
BBACCap	4	N/A		
48,46,44,42,40,38,36,34,32,30,28,26,24,22,20,18,16,14,12,10,8,6,4,2,1,0				
BBACCapBandwidth	15	N/A	15,3	
BBACSrc	4	N/A		
48,46,44,42,40,38,36,34,32,30,28,26,24,22,20,18,16,14,12,10,8,6,4,2,1,0				
BBACSrcBandwidth	15	N/A	15,3	
DSIO	1	N/A	24,3,2,1,0	
DSSC	1	N/A		
24,23,22,21,20,19,18,17,16,15,14,13,12,11,10,9,8,7,6,5,4,3,2,1,0				
EdgeSets	32	N/A	32,16,8,4,2,1	
Freq	100	N/A	200,150,125,100,66,50,33,25	
IG-XL_TesterServiceDaemon	N/A	N/A	N/A	N/A
LVMSize	16	N/A	64,48,32,16,12,8,4,2,1	
MainSlots	12	N/A	36,24,16,12,8,6,4	
TimeSets	256	N/A	256,128,64,32,16,8	
VHFACCapture	2	N/A		
48,46,44,42,40,38,36,34,32,30,28,26,24,22,20,18,16,14,12,10,8,6,4,2,1,0				
VHFACCaptureSamplingRate	125	N/A	125,80	
VHFACSource	2	N/A		
48,46,44,42,40,38,36,34,32,30,28,26,24,22,20,18,16,14,12,10,8,6,4,2,1,0				
VHFACSourceSamplingRate	400	N/A	400,100	

</LICENSE\_ENABLERS>

<SignalDeliveryTower>  
Config 420-483-40 810-115-07 1350  
</SignalDeliveryTower>

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DATE: 09/08/2022            TIME: 11:22:27  
TESTER: AGRMFLE  
DRIVE C TOTAL SPACE: 14,998 MB    DRIVE C UNUSED SPACE: 3,305 MB  
IG-XL VERSION: 5.10.20\_flx (P16)    IG-XL BUILD: 02.09.07.17.21  
TEST MODE: Full Check            LOOP: By System

\*\*\*\*\* SYSTEM CHECKS \*\*\*\*\*

Name	S/N	Part/All	Total	Pass	Fail	Invalid
ComputerIO		All	1	1	0	0
SMC		All	1	1	0	0

\*\*\*\*\* OPTION CHECKS \*\*\*\*\*

Slot	Option_Name	S/N	Part/All	Total	Pass	Fail	Invalid
1	BBAC-15	0305B7F4	All	1	0	1	0
2	DC-90	0C156CAE	All	1	1	0	0
3	DC-30	0600ECB3	All	1	1	0	0
4	HVD-1	0C33DE1A	All	1	1	0	0
5	DC-30	0C0178E8	All	1	1	0	0
6	VHFAC	03012B8E	All	1	1	0	0
7	SupportBoard-0	0C0FA9AF	All	1	1	0	0
8	HSD-200	0C006192	All	1	1	0	0
9	DC-30	03014654	All	1	1	0	0
10	DC-90	0C311FAE	All	1	1	0	0
11	DC-90	0C130F47	All	1	1	0	0
12	DC-30	0C351261	All	1	1	0	0
13	BBAC-15	03028AE8	All	1	0	1	0
20	SupportBoard-USM	002AF110	All	1	1	0	0

\*\*\*\*\* CONFIGURATION \*\*\*\*\*

Slot	Option_Name	Assembly	Part_Number	S/N	Rev_Date
1.0	BBAC-15	High-Level	805-004-50	0305B7F4	1041-E
	BBAC-15	Primary	949-977-03	0305B142	0819-E
	BBAC-15Rider	Primary	949-987-02	03058D76	0739-D
	PowerBoard	Primary	949-984-04	0305B05A	0949-E
2.0	DC-90	High-Level	805-229-50	0C156CAE	1225-C
	DC-90	Primary	939-229-06	0C156AC2	1150-C

	PowerBoard	Primary	939-228-00	0C17D17D	1225-D
	Sub		949-972-25	0C17DF4F	1206-E
2.1	DC-90PowerSupply	High-Level	805-230-30	002B4735	0439-B
	DC-90PowerSupply	Primary	939-230-03	002AF137	0446-B
	Sub		939-231-03	002AF186	0436-B
3.0	DC-30	High-Level	805-002-60	0600ECB3	1133-B
	DC-30	Primary	949-901-60	0600EB86	0808-B
	DC-30Rider	Primary	949-904-60	0600EBE5	0808-B
	PowerBoard	Primary	949-955-02	0600D208	0531-E
	Sub		949-972-00	0600FFEF	0313-C
4.0	HVD-1	High-Level	805-740-50	0C33DE1A	1735-C
	HVD-1	Primary	939-273-00	0C34AEBB	1042-C
4.1	HVDMModuleBoard-0	High-Level	805-740-50	0C33DE1A	1735-C
	HVDMModuleBoard-0	Primary	939-274-10	0C34E2A2	1735-C
4.2	HVDMModuleBoard-1	High-Level	805-740-50	0C33DE1A	1735-C
	HVDMModuleBoard-1	Primary	939-274-10	0C34E2AA	1735-C
4.3	HVDMModuleBoard-2	High-Level	805-740-50	0C33DE1A	1735-C
	HVDMModuleBoard-2	Primary	939-274-10	0C34E294	1735-C
5.0	DC-30	High-Level	805-002-60	0C0178E8	1206-E
	DC-30	Primary	949-901-60	0C0178A3	1205-E
	DC-30Rider	Primary	949-904-60	0C01B89F	1205-E
	PowerBoard	Primary	949-955-02	0C0177E3	0644-E
	Sub		949-972-25	0C01B6B6	1206-E
6.0	VHFAC	High-Level	805-245-50	03012B8E	0707-E
	VHFAC	Primary	939-245-10	03012826	0707-E
	VHFACCapture0	Primary	939-244-00	030127F8	0552-C
	VHFACCapture1	Primary	939-244-00	030127F8	0552-C
	VHFACSource4	Primary	939-247-10	0301BE08	0613-C
	VHFACSource5	Primary	939-247-10	0301BE08	0613-C
	VHFACTime	Primary	939-245-10	03012826	0707-E
7.0	SupportBoard	High-Level	805-003-50	0C0FA9AF	1223-B
	SupportBoard	Primary	949-909-00	0C0F999F	1223-B
	DCCalModule	Primary	949-938-12	0C0F97CA	0912-C
	PowerBoard	Primary	949-910-30	0C0F96AA	1121-C
	Sub		949-972-25	0C11351A	0544-E
7.1	DSP-1	High-Level	810-503-00	03031F22	0423-B
	DSP-1	Primary	949-939-00	03031F22	0423-B
8.0	HSD-200	High-Level	805-251-50	0C006192	1231-D
	HSD-200	Primary	939-251-30	0C00458C	1231-D
	PowerBoard	Primary	939-253-01	0C0048F7	1037-C
	Sub		949-972-25	0C0046B2	1206-E
9.0	DC-30	High-Level	805-002-60	03014654	1206-B
	DC-30	Primary	949-901-60	0301AD3A	0808-B
	PowerBoard	Primary	949-955-02	0301AD58	0531-E
	Sub		949-972-25	03014153	1206-E
	DC-30Rider	Primary	949-904-60	03018EE6	0808-B
10.0	DC-90	High-Level	805-229-50	0C311FAE	1233-C
	DC-90	Primary	939-229-06	0C311EDE	1150-C

PowerBoard Primary 939-228-00 OC310B72 1225-D  
     Sub 609-082-00 OC3109B1 1116-A  
 10.1 DC-90PowerSupply High-Level 805-230-06 OC1910C9 1115-B  
     DC-90PowerSupply Primary 939-230-06 OC191B48 1115-B  
     Sub 939-231-06 OC191B92 1115-B  
 11.0 DC-90 High-Level 805-229-50 OC130F47 1225-C  
     DC-90 Primary 939-229-06 OC125A72 1127-C  
     PowerBoard Primary 939-228-00 OC12CAF8 1225-D  
     Sub 949-972-25 OC13FE9F 1206-E  
 11.1 DC-90PowerSupply High-Level 805-230-30 002B4816 0439-B  
     DC-90PowerSupply Primary 939-230-03 002AF13E 0446-B  
     Sub 939-231-03 002ADA44 0436-B  
 12.0 DC-30 High-Level 805-002-60 OC351261 1505-E  
     DC-30 Primary 949-901-60 OC35D71B 1418-E  
     PowerBoard Primary 949-955-02 OC35D7DE 1505-E  
     Sub 609-082-02 OC35E2CC 1421-A  
     DC-30Rider Primary 949-904-60 OC360002 1418-E  
 13.0 BBAC-15 High-Level 805-004-50 03028AE8 1041-E  
     BBAC-15 Primary 949-977-03 0302695A 0819-E  
     BBAC-15Rider Primary 949-987-02 030289A7 0739-D  
     PowerBoard Primary 949-984-04 03026966 0633-E  
     Sub 949-972-25 03024856 0544-E  
 20.0 SupportBoard-USM High-Level 939-270-00 002AF110 0549-B  
     SupportBoard-USM Primary 939-270-00 002AF110 0549-B

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09/08/2022;11:22:27;Full Check;1;By System;Verbosity = ALL

1; ComputerIO; PCIT FreeTimer;; Pass; ; ; 09/08/2022 11:22:30  
 1; ComputerIO; PCIT RetrigTimer; ; Pass; ; ; 09/08/2022 11:22:30  
 1; ComputerIO; GPIB Comm; ; Pass; ; ; 09/08/2022 11:22:32  
 1; ComputerIO; PCIT DMA; ; Pass; ; ; 09/08/2022 11:22:33  
 1; SMC; Configuration; ; Pass; ; ; 09/08/2022 11:22:34  
 1; SMC; Status; SMC System Node; Pass; ; ; 09/08/2022 11:22:35  
 1; SMC; Status; SMC Support Cabinet; Pass; ; ; 09/08/2022 11:22:35  
 1; SMC; Status; 1:BBAC-15; Pass; ; ; 09/08/2022 11:22:35  
 1; SMC; Status; 2:DC-90; Pass; ; ; 09/08/2022 11:22:35  
 1; SMC; Status; 3:DC-30; Pass; ; ; 09/08/2022 11:22:35  
 1; SMC; Status; 4:HVD-1; Pass; ; ; 09/08/2022 11:22:35  
 1; SMC; Status; 5:DC-30; Pass; ; ; 09/08/2022 11:22:35  
 1; SMC; Status; 6:VHFAC; Pass; ; ; 09/08/2022 11:22:35  
 1; SMC; Status; 7:SupportBoard; Pass; ; ; 09/08/2022 11:22:35  
 1; SMC; Status; 8:HSD-200; Pass; ; ; 09/08/2022 11:22:35  
 1; SMC; Status; 9:DC-30; Pass; ; ; 09/08/2022 11:22:35  
 1; SMC; Status; 10:DC-90; Pass; ; ; 09/08/2022 11:22:35  
 1; SMC; Status; 11:DC-90; Pass; ; ; 09/08/2022 11:22:35  
 1; SMC; Status; 12:DC-30; Pass; ; ; 09/08/2022 11:22:35  
 1; SMC; Status; 13:BBAC-15; Pass; ; ; 09/08/2022 11:22:35  
 1; SMC; Status; 20:SupportBoard-USM; Pass; ; ; 09/08/2022 11:22:35

1;	SMC;	Echo;	SMC System Node;	Pass;	;	;	09/08/2022 11:22:40
1;	SMC;	Echo;	SMC Support Cabinet;	Pass;	;	;	09/08/2022 11:22:40
1;	SMC;	Echo;	1:BBAC-15;	Pass;	;	;	09/08/2022 11:22:40
1;	SMC;	Echo;	2:DC-90;	Pass;	;	;	09/08/2022 11:22:40
1;	SMC;	Echo;	3:DC-30;	Pass;	;	;	09/08/2022 11:22:40
1;	SMC;	Echo;	4:HVD-1;	Pass;	;	;	09/08/2022 11:22:40
1;	SMC;	Echo;	5:DC-30;	Pass;	;	;	09/08/2022 11:22:40
1;	SMC;	Echo;	6:VHFAC;	Pass;	;	;	09/08/2022 11:22:40
1;	SMC;	Echo;	7:SupportBoard;	Pass;	;	;	09/08/2022 11:22:40
1;	SMC;	Echo;	8:HSD-200;	Pass;	;	;	09/08/2022 11:22:40
1;	SMC;	Echo;	9:DC-30;	Pass;	;	;	09/08/2022 11:22:40
1;	SMC;	Echo;	10:DC-90;	Pass;	;	;	09/08/2022 11:22:40
1;	SMC;	Echo;	11:DC-90;	Pass;	;	;	09/08/2022 11:22:40
1;	SMC;	Echo;	12:DC-30;	Pass;	;	;	09/08/2022 11:22:40
1;	SMC;	Echo;	13:BBAC-15;	Pass;	;	;	09/08/2022 11:22:40
1;	SMC;	RAM;	SMC System Node;	Pass;	;	;	09/08/2022 11:22:45
1;	SMC;	RAM;	SMC Support Cabinet;	Pass;	;	;	09/08/2022 11:22:45
1;	SMC;	RAM;	1:BBAC-15;	Pass;	;	;	09/08/2022 11:22:45
1;	SMC;	RAM;	2:DC-90;	Pass;	;	;	09/08/2022 11:22:45
1;	SMC;	RAM;	3:DC-30;	Pass;	;	;	09/08/2022 11:22:45
1;	SMC;	RAM;	4:HVD-1;	Pass;	;	;	09/08/2022 11:22:45
1;	SMC;	RAM;	5:DC-30;	Pass;	;	;	09/08/2022 11:22:45
1;	SMC;	RAM;	6:VHFAC;	Pass;	;	;	09/08/2022 11:22:45
1;	SMC;	RAM;	7:SupportBoard;	Pass;	;	;	09/08/2022 11:22:45
1;	SMC;	RAM;	8:HSD-200;	Pass;	;	;	09/08/2022 11:22:45
1;	SMC;	RAM;	9:DC-30;	Pass;	;	;	09/08/2022 11:22:45
1;	SMC;	RAM;	10:DC-90;	Pass;	;	;	09/08/2022 11:22:45
1;	SMC;	RAM;	11:DC-90;	Pass;	;	;	09/08/2022 11:22:45
1;	SMC;	RAM;	12:DC-30;	Pass;	;	;	09/08/2022 11:22:45
1;	SMC;	RAM;	13:BBAC-15;	Pass;	;	;	09/08/2022 11:22:46
1;	SMC;	Calibrate;	SMC Support Cabinet;	Pass;	;	;	09/08/2022 11:22:50
1;	SMC;	Calibrate;	1:BBAC-15;	Pass;	;	;	09/08/2022 11:22:50
1;	SMC;	Calibrate;	2:DC-90;	Pass;	;	;	09/08/2022 11:22:50
1;	SMC;	Calibrate;	3:DC-30;	Pass;	;	;	09/08/2022 11:22:50
1;	SMC;	Calibrate;	4:HVD-1;	Pass;	;	;	09/08/2022 11:22:50
1;	SMC;	Calibrate;	5:DC-30;	Pass;	;	;	09/08/2022 11:22:50
1;	SMC;	Calibrate;	6:VHFAC;	Pass;	;	;	09/08/2022 11:22:50
1;	SMC;	Calibrate;	7:SupportBoard;	Pass;	;	;	09/08/2022 11:22:50
1;	SMC;	Calibrate;	8:HSD-200;	Pass;	;	;	09/08/2022 11:22:50
1;	SMC;	Calibrate;	9:DC-30;	Pass;	;	;	09/08/2022 11:22:50
1;	SMC;	Calibrate;	10:DC-90;	Pass;	;	;	09/08/2022 11:22:50
1;	SMC;	Calibrate;	11:DC-90;	Pass;	;	;	09/08/2022 11:22:50
1;	SMC;	Calibrate;	12:DC-30;	Pass;	;	;	09/08/2022 11:22:50
1;	SMC;	Calibrate;	13:BBAC-15;	Pass;	;	;	09/08/2022 11:22:50
1;	SMC;	Flash;	;	Pass;	;	;	09/08/2022 11:22:54
1;	7:SupportBoard-0;	RegisterTest;	;	Pass;	;	Slot 7 Pass;	09/08/2022

11:23:02



1;	7:SupportBoard-0;	DacAdcTest;	;	Pass;	;	Slot 7 Pass;	09/08/2022
11:23:02							
1;	7:SupportBoard-0;	DcCalTest;	;	Pass;	;	Slot 7 Pass;	09/08/2022
11:23:06							
1;	7:SupportBoard-0;	UtilityBitTest;	;	Pass;	;	Slot 7 Pass;	09/08/2022
11:23:07							
1;	7:SupportBoard-0;	User Power Test;	;	Pass;	;	Slot 7 Pass;	
09/08/2022	11:23:19						
1;	7:SupportBoard-0;	I6BypassTest;	;	Pass;	;	Slot 7 Pass;	09/08/2022
11:23:19							
1;	7:SupportBoard-0;	I6Test;	;	Pass;	;	Slot 7 Pass;	09/08/2022
11:23:19							
1;	7:SupportBoard-0;	Sync Bus Test;	;	Pass;	;	Slot 7 Pass;	09/08/2022
11:23:20							
1;	7:SupportBoard-0;	XPT Test;	;	Pass;	;	Slot 7 Pass;	09/08/2022
11:23:26							
1;	7:SupportBoard-0;	XPT To DSP Test;	;	Pass;	;	Slot 7 Pass;	
09/08/2022	11:23:26						
1;	7:SupportBoard-0;	G4Test;	;	Pass;	;	Slot 7 Pass;	09/08/2022
11:23:34							
1;	7:SupportBoard-0;	Move Bus Test;	;	Pass;	;	Slot 7 Pass;	09/08/2022
11:23:34							
1;	7:SupportBoard-0;	DSPMoveBusTest;	;	Pass;	;	Slot 7 Pass;	
09/08/2022	11:23:35						
1;	7:SupportBoard-0;	PicoClockTest;	;	Invalid;	;	Slot 7 Invalid;	09/08/2022
11:23:35							
1;	20:SupportBoard-USM;	RegisterTest;	;	Pass;	;	Slot 20 Pass;	09/08/2022
11:23:37							
1;	20:SupportBoard-USM;	DacAdcTest;	;	Pass;	;	Slot 20 Pass;	09/08/2022
11:23:38							
1;	20:SupportBoard-USM;	UtilityBitTest;	;	Pass;	;	Slot 20 Pass;	09/08/2022
11:23:38							
1;	20:SupportBoard-USM;	User Power Test;	;	Pass;	;	Slot 20 Pass;	
09/08/2022	11:23:50						
1;	1:BBAC-15;	IDProm;	;	Pass;	;	;	09/08/2022
11:24:02							
1;	13:BBAC-15;	IDProm;	;	Pass;	;	;	09/08/2022
11:24:02							
1;	1:BBAC-15;	Calibration Check;	Channel 1;	Pass;	;	;	09/08/2022
11:24:06							
1;	1:BBAC-15;	Calibration Check;	Channel 2;	Pass;	;	;	09/08/2022
11:24:06							
1;	1:BBAC-15;	Calibration Check;	PPMUs;Pass;	;	;	;	09/08/2022
11:24:06							
1;	13:BBAC-15;	Calibration Check;	Channel 1;	Pass;	;	;	09/08/2022
11:24:06							
1;	13:BBAC-15;	Calibration Check;	Channel 2;	Pass;	;	;	09/08/2022
11:24:06							
1;	13:BBAC-15;	Calibration Check;	PPMUs;Pass;	;	;	;	09/08/2022
11:24:06							
1;	1:BBAC-15;	Memory;	1;	Pass;	;	;	09/08/2022
11:25:06							
1;	1:BBAC-15;	Memory;	2;	Pass;	;	;	09/08/2022
11:25:06							
1;	13:BBAC-15;	Memory;	1;	Pass;	;	;	09/08/2022
11:25:06							
1;	13:BBAC-15;	Memory;	2;	Pass;	;	;	09/08/2022
11:25:06							

1;	1:BBAC-15;	Relay Test;	Channel 1;	Pass;	;	;	09/08/2022 11:25:32
1;	1:BBAC-15;	Relay Test;	Channel 2;	Pass;	;	;	09/08/2022 11:25:32
1;	13:BBAC-15;	Relay Test;	Channel 1;	Pass;	;	;	09/08/2022 11:25:32
1;	13:BBAC-15;	Relay Test;	Channel 2;	Pass;	;	;	09/08/2022 11:25:32
1;	1:BBAC-15;	SMC Voltage Check;	;	Pass;	;	;	09/08/2022 11:25:33
1;	13:BBAC-15;	SMC Voltage Check;	;	Pass;	;	;	09/08/2022 11:25:33
1;	1:BBAC-15;	Pattern Ucode Test;	1;	Pass;	;	;	09/08/2022 11:25:44
1;	1:BBAC-15;	Pattern Ucode Test;	2;	Pass;	;	;	09/08/2022 11:25:44
1;	13:BBAC-15;	Pattern Ucode Test;	1;	Pass;	;	;	09/08/2022 11:25:44
1;	13:BBAC-15;	Pattern Ucode Test;	2;	Pass;	;	;	09/08/2022 11:25:44
1;	1:BBAC-15;	Condition Bit;	Channel 1;	Pass;	;	;	09/08/2022 11:25:44
1;	1:BBAC-15;	Condition Bit;	Channel 2;	Pass;	;	;	09/08/2022 11:25:44
1;	13:BBAC-15;	Condition Bit;	Channel 1;	Pass;	;	;	09/08/2022 11:25:44
1;	13:BBAC-15;	Condition Bit;	Channel 2;	Pass;	;	;	09/08/2022 11:25:44
1;	1:BBAC-15;	Source segments;	Channel 1;	Pass;	;	;	09/08/2022 11:25:56
1;	1:BBAC-15;	Source segments;	Channel 2;	Pass;	;	;	09/08/2022 11:25:56
1;	13:BBAC-15;	Source segments;	Channel 1;	Pass;	;	;	09/08/2022 11:25:56
1;	13:BBAC-15;	Source segments;	Channel 2;	Pass;	;	;	09/08/2022 11:25:56
1;	1:BBAC-15;	MoveBus Test;	Channel 1;	Pass;	;	;	09/08/2022 11:26:03
1;	1:BBAC-15;	MoveBus Test;	Channel 2;	Pass;	;	;	09/08/2022 11:26:03
1;	13:BBAC-15;	MoveBus Test;	Channel 1;	Pass;	;	;	09/08/2022 11:26:03
1;	13:BBAC-15;	MoveBus Test;	Channel 2;	Pass;	;	;	09/08/2022 11:26:03
1;	1:BBAC-15;	PPMU FVMV;	1-SRC1P;	Pass;	;	;	09/08/2022 11:26:03
1;	1:BBAC-15;	PPMU FVMV;	2-SRC1N;	Pass;	;	;	09/08/2022 11:26:03
1;	1:BBAC-15;	PPMU FVMV;	3-SRC1R;	Pass;	;	;	09/08/2022 11:26:03
1;	1:BBAC-15;	PPMU FVMV;	4-CAP1P;	Pass;	;	;	09/08/2022 11:26:03
1;	1:BBAC-15;	PPMU FVMV;	5-CAP1N;	Pass;	;	;	09/08/2022 11:26:03
1;	1:BBAC-15;	PPMU FVMV;	6-SRC2P;	Pass;	;	;	09/08/2022 11:26:03
1;	1:BBAC-15;	PPMU FVMV;	7-SRC2N;	Pass;	;	;	09/08/2022 11:26:03
1;	1:BBAC-15;	PPMU FVMV;	8-SRC2R;	Pass;	;	;	09/08/2022 11:26:03
1;	1:BBAC-15;	PPMU FVMV;	9-CAP2P;	Pass;	;	;	09/08/2022 11:26:03
1;	1:BBAC-15;	PPMU FVMV;	10-CAP2N;	Pass;	;	;	09/08/2022 11:26:03
1;	13:BBAC-15;	PPMU FVMV;	1-SRC1P;	Pass;	;	;	09/08/2022 11:26:03
1;	13:BBAC-15;	PPMU FVMV;	2-SRC1N;	Pass;	;	;	09/08/2022 11:26:03
1;	13:BBAC-15;	PPMU FVMV;	3-SRC1R;	Pass;	;	;	09/08/2022 11:26:03
1;	13:BBAC-15;	PPMU FVMV;	4-CAP1P;	Pass;	;	;	09/08/2022 11:26:03
1;	13:BBAC-15;	PPMU FVMV;	5-CAP1N;	Pass;	;	;	09/08/2022 11:26:03
1;	13:BBAC-15;	PPMU FVMV;	6-SRC2P;	Pass;	;	;	09/08/2022 11:26:03
1;	13:BBAC-15;	PPMU FVMV;	7-SRC2N;	Pass;	;	;	09/08/2022 11:26:03
1;	13:BBAC-15;	PPMU FVMV;	8-SRC2R;	Pass;	;	;	09/08/2022 11:26:03
1;	13:BBAC-15;	PPMU FVMV;	9-CAP2P;	Pass;	;	;	09/08/2022 11:26:03
1;	13:BBAC-15;	PPMU FVMV;	10-CAP2N;	Pass;	;	;	09/08/2022 11:26:03
1;	1:BBAC-15;	PPMU FSysVMV;	1-SRC1P;	Pass;	;	;	09/08/2022 11:26:03

1;	1:BBAC-15;	PPMU FSysVMV;	2-SRC1N;	Pass;	;	;	09/08/2022
11:26:03							
1;	1:BBAC-15;	PPMU FSysVMV;	3-SRC1R;	Pass;	;	;	09/08/2022
11:26:03							
1;	1:BBAC-15;	PPMU FSysVMV;	4-CAP1P;	Pass;	;	;	09/08/2022
11:26:03							
1;	1:BBAC-15;	PPMU FSysVMV;	5-CAP1N;	Pass;	;	;	09/08/2022
11:26:03							
1;	1:BBAC-15;	PPMU FSysVMV;	6-SRC2P;	Pass;	;	;	09/08/2022
11:26:03							
1;	1:BBAC-15;	PPMU FSysVMV;	7-SRC2N;	Pass;	;	;	09/08/2022
11:26:03							
1;	1:BBAC-15;	PPMU FSysVMV;	8-SRC2R;	Pass;	;	;	09/08/2022
11:26:03							
1;	1:BBAC-15;	PPMU FSysVMV;	9-CAP2P;	Pass;	;	;	09/08/2022
11:26:03							
1;	1:BBAC-15;	PPMU FSysVMV;	10-CAP2N;	Pass;	;	;	09/08/2022
11:26:03							
1;	13:BBAC-15;	PPMU FSysVMV;	1-SRC1P;	Pass;	;	;	09/08/2022
11:26:03							
1;	13:BBAC-15;	PPMU FSysVMV;	2-SRC1N;	Pass;	;	;	09/08/2022
11:26:03							
1;	13:BBAC-15;	PPMU FSysVMV;	3-SRC1R;	Pass;	;	;	09/08/2022
11:26:03							
1;	13:BBAC-15;	PPMU FSysVMV;	4-CAP1P;	Pass;	;	;	09/08/2022
11:26:03							
1;	13:BBAC-15;	PPMU FSysVMV;	5-CAP1N;	Pass;	;	;	09/08/2022
11:26:03							
1;	13:BBAC-15;	PPMU FSysVMV;	6-SRC2P;	Pass;	;	;	09/08/2022
11:26:03							
1;	13:BBAC-15;	PPMU FSysVMV;	7-SRC2N;	Pass;	;	;	09/08/2022
11:26:03							
1;	13:BBAC-15;	PPMU FSysVMV;	8-SRC2R;	Pass;	;	;	09/08/2022
11:26:03							
1;	13:BBAC-15;	PPMU FSysVMV;	9-CAP2P;	Pass;	;	;	09/08/2022
11:26:03							
1;	13:BBAC-15;	PPMU FSysVMV;	10-CAP2N;	Pass;	;	;	09/08/2022
11:26:03							
1;	1:BBAC-15;	PPMU FIMI;	1-SRC1P;	Pass;	;	;	09/08/2022 11:26:04
1;	1:BBAC-15;	PPMU FIMI;	2-SRC1N;	Pass;	;	;	09/08/2022 11:26:04
1;	1:BBAC-15;	PPMU FIMI;	3-SRC1R;	Pass;	;	;	09/08/2022 11:26:04
1;	1:BBAC-15;	PPMU FIMI;	4-CAP1P;	Pass;	;	;	09/08/2022 11:26:04
1;	1:BBAC-15;	PPMU FIMI;	5-CAP1N;	Pass;	;	;	09/08/2022 11:26:04
1;	1:BBAC-15;	PPMU FIMI;	6-SRC2P;	Pass;	;	;	09/08/2022 11:26:04
1;	1:BBAC-15;	PPMU FIMI;	7-SRC2N;	Pass;	;	;	09/08/2022 11:26:04
1;	1:BBAC-15;	PPMU FIMI;	8-SRC2R;	Pass;	;	;	09/08/2022 11:26:04
1;	1:BBAC-15;	PPMU FIMI;	9-CAP2P;	Pass;	;	;	09/08/2022 11:26:04
1;	1:BBAC-15;	PPMU FIMI;	10-CAP2N;	Pass;	;	;	09/08/2022 11:26:04

1;	13:BBAC-15;	PPMU FIMI;	1-SRC1P;	Pass;	;	;	09/08/2022 11:26:04
1;	13:BBAC-15;	PPMU FIMI;	2-SRC1N;	Pass;	;	;	09/08/2022 11:26:04
1;	13:BBAC-15;	PPMU FIMI;	3-SRC1R;	Pass;	;	;	09/08/2022 11:26:04
1;	13:BBAC-15;	PPMU FIMI;	4-CAP1P;	Pass;	;	;	09/08/2022 11:26:04
1;	13:BBAC-15;	PPMU FIMI;	5-CAP1N;	Pass;	;	;	09/08/2022 11:26:04
1;	13:BBAC-15;	PPMU FIMI;	6-SRC2P;	Pass;	;	;	09/08/2022 11:26:04
1;	13:BBAC-15;	PPMU FIMI;	7-SRC2N;	Pass;	;	;	09/08/2022 11:26:04
1;	13:BBAC-15;	PPMU FIMI;	8-SRC2R;	Pass;	;	;	09/08/2022 11:26:04
1;	13:BBAC-15;	PPMU FIMI;	9-CAP2P;	Pass;	;	;	09/08/2022 11:26:04
1;	13:BBAC-15;	PPMU FIMI;	10-CAP2N;	Pass;	;	;	09/08/2022 11:26:04
1;	1:BBAC-15;	PPMU FIMV_FVMI;	1-SRC1P;	Pass;	;	;	09/08/2022 11:26:06
1;	1:BBAC-15;	PPMU FIMV_FVMI;	2-SRC1N;	Pass;	;	;	09/08/2022 11:26:06
1;	1:BBAC-15;	PPMU FIMV_FVMI;	3-SRC1R;	Pass;	;	;	09/08/2022 11:26:06
1;	1:BBAC-15;	PPMU FIMV_FVMI;	4-CAP1P;	Pass;	;	;	09/08/2022 11:26:06
1;	1:BBAC-15;	PPMU FIMV_FVMI;	5-CAP1N;	Pass;	;	;	09/08/2022 11:26:06
1;	1:BBAC-15;	PPMU FIMV_FVMI;	6-SRC2P;	Pass;	;	;	09/08/2022 11:26:06
1;	1:BBAC-15;	PPMU FIMV_FVMI;	7-SRC2N;	Pass;	;	;	09/08/2022 11:26:06
1;	1:BBAC-15;	PPMU FIMV_FVMI;	8-SRC2R;	Pass;	;	;	09/08/2022 11:26:06
1;	1:BBAC-15;	PPMU FIMV_FVMI;	9-CAP2P;	Pass;	;	;	09/08/2022 11:26:06
1;	1:BBAC-15;	PPMU FIMV_FVMI;	10-CAP2N;	Pass;	;	;	09/08/2022 11:26:06
1;	13:BBAC-15;	PPMU FIMV_FVMI;	1-SRC1P;	Pass;	;	;	09/08/2022 11:26:06
1;	13:BBAC-15;	PPMU FIMV_FVMI;	2-SRC1N;	Pass;	;	;	09/08/2022 11:26:06
1;	13:BBAC-15;	PPMU FIMV_FVMI;	3-SRC1R;	Pass;	;	;	09/08/2022 11:26:06
1;	13:BBAC-15;	PPMU FIMV_FVMI;	4-CAP1P;	Pass;	;	;	09/08/2022 11:26:06
1;	13:BBAC-15;	PPMU FIMV_FVMI;	5-CAP1N;	Pass;	;	;	09/08/2022 11:26:06
1;	13:BBAC-15;	PPMU FIMV_FVMI;	6-SRC2P;	Pass;	;	;	09/08/2022 11:26:06
1;	13:BBAC-15;	PPMU FIMV_FVMI;	7-SRC2N;	Pass;	;	;	09/08/2022 11:26:06
1;	13:BBAC-15;	PPMU FIMV_FVMI;	8-SRC2R;	Pass;	;	;	09/08/2022 11:26:06
1;	13:BBAC-15;	PPMU FIMV_FVMI;	9-CAP2P;	Pass;	;	;	09/08/2022 11:26:06

1;	13:BBAC-15;	PPMU FIMV_FVMI;	10-CAP2N;	Pass;	;	;	09/08/2022
11:26:06							
1;	1:BBAC-15;	PPMU DGS Test;	1-SRC1DGS;	Pass;	;	;	09/08/2022
11:26:06							
1;	1:BBAC-15;	PPMU DGS Test;	2-CAP1DGS;	Pass;	;	;	09/08/2022
11:26:06							
1;	1:BBAC-15;	PPMU DGS Test;	3-SRC2DGS;	Pass;	;	;	09/08/2022
11:26:06							
1;	1:BBAC-15;	PPMU DGS Test;	4-CAP2DGS;	Pass;	;	;	09/08/2022
11:26:06							
1;	13:BBAC-15;	PPMU DGS Test;	1-SRC1DGS;	Pass;	;	;	09/08/2022
11:26:06							
1;	13:BBAC-15;	PPMU DGS Test;	2-CAP1DGS;	Pass;	;	;	09/08/2022
11:26:06							
1;	13:BBAC-15;	PPMU DGS Test;	3-SRC2DGS;	Pass;	;	;	09/08/2022
11:26:06							
1;	13:BBAC-15;	PPMU DGS Test;	4-CAP2DGS;	Pass;	;	;	09/08/2022
11:26:06							
1;	1:BBAC-15;	PPMU VClamp; 1-SRC1P;		Pass;	;	;	09/08/2022 11:26:07
1;	1:BBAC-15;	PPMU VClamp; 2-SRC1N;		Pass;	;	;	09/08/2022 11:26:07
1;	1:BBAC-15;	PPMU VClamp; 3-SRC1R;		Pass;	;	;	09/08/2022 11:26:07
1;	1:BBAC-15;	PPMU VClamp; 4-CAP1P;		Pass;	;	;	09/08/2022 11:26:07
1;	1:BBAC-15;	PPMU VClamp; 5-CAP1N;		Pass;	;	;	09/08/2022 11:26:07
1;	1:BBAC-15;	PPMU VClamp; 6-SRC2P;		Pass;	;	;	09/08/2022 11:26:07
1;	1:BBAC-15;	PPMU VClamp; 7-SRC2N;		Pass;	;	;	09/08/2022 11:26:07
1;	1:BBAC-15;	PPMU VClamp; 8-SRC2R;		Pass;	;	;	09/08/2022 11:26:07
1;	1:BBAC-15;	PPMU VClamp; 9-CAP2P;		Pass;	;	;	09/08/2022 11:26:07
1;	1:BBAC-15;	PPMU VClamp; 10-CAP2N;		Pass;	;	;	09/08/2022 11:26:07
1;	13:BBAC-15;	PPMU VClamp; 1-SRC1P;		Pass;	;	;	09/08/2022 11:26:07
1;	13:BBAC-15;	PPMU VClamp; 2-SRC1N;		Pass;	;	;	09/08/2022 11:26:07
1;	13:BBAC-15;	PPMU VClamp; 3-SRC1R;		Pass;	;	;	09/08/2022 11:26:07
1;	13:BBAC-15;	PPMU VClamp; 4-CAP1P;		Pass;	;	;	09/08/2022 11:26:07
1;	13:BBAC-15;	PPMU VClamp; 5-CAP1N;		Pass;	;	;	09/08/2022 11:26:07
1;	13:BBAC-15;	PPMU VClamp; 6-SRC2P;		Pass;	;	;	09/08/2022 11:26:07
1;	13:BBAC-15;	PPMU VClamp; 7-SRC2N;		Pass;	;	;	09/08/2022 11:26:07
1;	13:BBAC-15;	PPMU VClamp; 8-SRC2R;		Pass;	;	;	09/08/2022 11:26:07
1;	13:BBAC-15;	PPMU VClamp; 9-CAP2P;		Pass;	;	;	09/08/2022 11:26:07
1;	13:BBAC-15;	PPMU VClamp; 10-CAP2N;		Pass;	;	;	09/08/2022 11:26:07
1;	1:BBAC-15;	PPMU VClmp ILkg;	1-SRC1P;	Pass;	;	;	09/08/2022
11:26:07							
1;	1:BBAC-15;	PPMU VClmp ILkg;	2-SRC1N;	Pass;	;	;	09/08/2022
11:26:07							
1;	1:BBAC-15;	PPMU VClmp ILkg;	3-SRC1R;	Pass;	;	;	09/08/2022
11:26:07							
1;	1:BBAC-15;	PPMU VClmp ILkg;	4-CAP1P;	Pass;	;	;	09/08/2022
11:26:07							
1;	1:BBAC-15;	PPMU VClmp ILkg;	5-CAP1N;	Pass;	;	;	09/08/2022
11:26:07							

1;	1:BBAC-15;	PPMU VClmp ILkg;	6-SRC2P;	Pass;	;	;	09/08/2022
11:26:07							
1;	1:BBAC-15;	PPMU VClmp ILkg;	7-SRC2N;	Pass;	;	;	09/08/2022
11:26:07							
1;	1:BBAC-15;	PPMU VClmp ILkg;	8-SRC2R;	Pass;	;	;	09/08/2022
11:26:07							
1;	1:BBAC-15;	PPMU VClmp ILkg;	9-CAP2P;	Pass;	;	;	09/08/2022
11:26:07							
1;	1:BBAC-15;	PPMU VClmp ILkg;	10-CAP2N;	Pass;	;	;	09/08/2022
11:26:07							
1;	13:BBAC-15;	PPMU VClmp ILkg;	1-SRC1P;	Pass;	;	;	09/08/2022
11:26:07							
1;	13:BBAC-15;	PPMU VClmp ILkg;	2-SRC1N;	Pass;	;	;	09/08/2022
11:26:07							
1;	13:BBAC-15;	PPMU VClmp ILkg;	3-SRC1R;	Pass;	;	;	09/08/2022
11:26:07							
1;	13:BBAC-15;	PPMU VClmp ILkg;	4-CAP1P;	Pass;	;	;	09/08/2022
11:26:07							
1;	13:BBAC-15;	PPMU VClmp ILkg;	5-CAP1N;	Pass;	;	;	09/08/2022
11:26:07							
1;	13:BBAC-15;	PPMU VClmp ILkg;	6-SRC2P;	Pass;	;	;	09/08/2022
11:26:07							
1;	13:BBAC-15;	PPMU VClmp ILkg;	7-SRC2N;	Pass;	;	;	09/08/2022
11:26:07							
1;	13:BBAC-15;	PPMU VClmp ILkg;	8-SRC2R;	Pass;	;	;	09/08/2022
11:26:07							
1;	13:BBAC-15;	PPMU VClmp ILkg;	9-CAP2P;	Pass;	;	;	09/08/2022
11:26:07							
1;	13:BBAC-15;	PPMU VClmp ILkg;	10-CAP2N;	Pass;	;	;	09/08/2022
11:26:07							
1;	1:BBAC-15;	DC Test;	Channel 1;	Pass;	;	;	09/08/2022 11:27:14
1;	1:BBAC-15;	DC Test;	Channel 2;	Pass;	;	;	09/08/2022 11:27:14
1;	13:BBAC-15;	DC Test;	Channel 1;	Fail;	BBAC-15 805-004-50 03028AE8;;		09/08/2022 11:27:14
1;	13:BBAC-15;	DC Test;	Channel 2;	Pass;	BBAC-15 805-004-50 03028AE8;;		09/08/2022 11:27:14
1;	1:BBAC-15;	Voltage Range Test;	Chan 1-Source and Digitizer ;	Pass;	;	;	09/08/2022 11:27:24
1;	1:BBAC-15;	Voltage Range Test;	Chan 2-Source and Digitizer ;	Pass;	;	;	09/08/2022 11:27:24
1;	13:BBAC-15;	Voltage Range Test;	Chan 1-Source and Digitizer ;	Pass;	;	;	09/08/2022 11:27:24
1;	13:BBAC-15;	Voltage Range Test;	Chan 2-Source and Digitizer ;	Pass;	;	;	09/08/2022 11:27:24
1;	1:BBAC-15;	INL Ramp;	Channel 1;	Pass;	;	;	09/08/2022 11:27:29
1;	1:BBAC-15;	INL Ramp;	Channel 2;	Pass;	;	;	09/08/2022 11:27:29
1;	13:BBAC-15;	INL Ramp;	Channel 1;	Pass;	;	;	09/08/2022 11:27:29
1;	13:BBAC-15;	INL Ramp;	Channel 2;	Pass;	;	;	09/08/2022 11:27:29

1;	1:BBAC-15;	Idle noise;	Channel 1;	Pass;	;	;	09/08/2022 11:32:22
1;	1:BBAC-15;	Idle noise;	Channel 2;	Pass;	;	;	09/08/2022 11:32:22
1;	13:BBAC-15;	Idle noise;	Channel 1;	Pass;	;	;	09/08/2022 11:32:22
1;	13:BBAC-15;	Idle noise;	Channel 2;	Pass;	;	;	09/08/2022 11:32:22
1;	1:BBAC-15;	CrossTalk;	Channel 1;	Fail;	BBAC-15 805-004-50 0305B7F4;	Replace	
Option; 09/08/2022 11:34:02							
1;	1:BBAC-15;	CrossTalk;	Channel 2;	Fail;	BBAC-15 805-004-50 0305B7F4;	Replace	
Option; 09/08/2022 11:34:02							
1;	13:BBAC-15;	CrossTalk;	Channel 1;	Fail;	BBAC-15 805-004-50 03028AE8;	Replace	
Option; 09/08/2022 11:34:02							
1;	13:BBAC-15;	CrossTalk;	Channel 2;	Fail;	BBAC-15 805-004-50 03028AE8;	Replace	
Option; 09/08/2022 11:34:02							
1;	1:BBAC-15;	SrcMxMn_ACCpling;	Channel 1;	Pass;	;	;	09/08/2022
11:34:19							
1;	1:BBAC-15;	SrcMxMn_ACCpling;	Channel 2;	Pass;	;	;	09/08/2022
11:34:19							
1;	13:BBAC-15;	SrcMxMn_ACCpling;	Channel 1;	Pass;	;	;	09/08/2022
11:34:19							
1;	13:BBAC-15;	SrcMxMn_ACCpling;	Channel 2;	Pass;	;	;	09/08/2022
11:34:19							
1;	1:BBAC-15;	Settling Time;	Channel 1;	Pass;	;	;	09/08/2022 11:36:14
1;	1:BBAC-15;	Settling Time;	Channel 2;	Pass;	;	;	09/08/2022 11:36:14
1;	13:BBAC-15;	Settling Time;	Channel 1;	Pass;	;	;	09/08/2022 11:36:14
1;	13:BBAC-15;	Settling Time;	Channel 2;	Pass;	;	;	09/08/2022 11:36:14
1;	1:BBAC-15;	Single Tone;	Channel 1;	Fail;	BBAC-15 805-004-50 0305B7F4;	Replace	
Option; 09/08/2022 11:44:52							
1;	1:BBAC-15;	Single Tone;	Channel 2;	Fail;	BBAC-15 805-004-50 0305B7F4;	Replace	
Option; 09/08/2022 11:44:52							
1;	13:BBAC-15;	Single Tone;	Channel 1;	Fail;	BBAC-15 805-004-50 03028AE8;	Replace	
Option; 09/08/2022 11:44:52							
1;	13:BBAC-15;	Single Tone;	Channel 2;	Fail;	BBAC-15 805-004-50 03028AE8;	Replace	
Option; 09/08/2022 11:44:52							
1;	1:BBAC-15;	Dual Tone Test;	Chan 1-Source and Digitizer ;	Pass;	;	;	
09/08/2022 11:45:35							
1;	1:BBAC-15;	Dual Tone Test;	Chan 2-Source and Digitizer ;	Pass;	;	;	
09/08/2022 11:45:35							
1;	13:BBAC-15;	Dual Tone Test;	Chan 1-Source and Digitizer ;	Pass;	;	;	
09/08/2022 11:45:35							
1;	13:BBAC-15;	Dual Tone Test;	Chan 2-Source and Digitizer ;	Pass;	;	;	
09/08/2022 11:45:35							
1;	1:BBAC-15;	Multi Tone Test;	Channel 1;	Pass;	;	;	09/08/2022
11:46:05							
1;	1:BBAC-15;	Multi Tone Test;	Channel 2;	Pass;	;	;	09/08/2022
11:46:05							
1;	13:BBAC-15;	Multi Tone Test;	Channel 1;	Pass;	;	;	09/08/2022
11:46:05							
1;	13:BBAC-15;	Multi Tone Test;	Channel 2;	Pass;	;	;	09/08/2022
11:46:05							































1;	9:DC-30;	PatGenTest;	17;	Pass;	;	;	09/08/2022 11:48:20
1;	9:DC-30;	PatGenTest;	18;	Pass;	;	;	09/08/2022 11:48:20
1;	9:DC-30;	PatGenTest;	19;	Pass;	;	;	09/08/2022 11:48:20
1;	9:DC-30;	PatGenTest;	20;	Pass;	;	;	09/08/2022 11:48:20
1;	12:DC-30;	PatGenTest;	1;	Pass;	;	;	09/08/2022 11:48:20
1;	12:DC-30;	PatGenTest;	2;	Pass;	;	;	09/08/2022 11:48:20
1;	12:DC-30;	PatGenTest;	3;	Pass;	;	;	09/08/2022 11:48:20
1;	12:DC-30;	PatGenTest;	4;	Pass;	;	;	09/08/2022 11:48:20
1;	12:DC-30;	PatGenTest;	5;	Pass;	;	;	09/08/2022 11:48:20
1;	12:DC-30;	PatGenTest;	6;	Pass;	;	;	09/08/2022 11:48:20
1;	12:DC-30;	PatGenTest;	7;	Pass;	;	;	09/08/2022 11:48:20
1;	12:DC-30;	PatGenTest;	8;	Pass;	;	;	09/08/2022 11:48:20
1;	12:DC-30;	PatGenTest;	9;	Pass;	;	;	09/08/2022 11:48:20
1;	12:DC-30;	PatGenTest;	10;	Pass;	;	;	09/08/2022 11:48:20
1;	12:DC-30;	PatGenTest;	11;	Pass;	;	;	09/08/2022 11:48:20
1;	12:DC-30;	PatGenTest;	12;	Pass;	;	;	09/08/2022 11:48:20
1;	12:DC-30;	PatGenTest;	13;	Pass;	;	;	09/08/2022 11:48:20
1;	12:DC-30;	PatGenTest;	14;	Pass;	;	;	09/08/2022 11:48:20
1;	12:DC-30;	PatGenTest;	15;	Pass;	;	;	09/08/2022 11:48:20
1;	12:DC-30;	PatGenTest;	16;	Pass;	;	;	09/08/2022 11:48:20
1;	12:DC-30;	PatGenTest;	17;	Pass;	;	;	09/08/2022 11:48:20
1;	12:DC-30;	PatGenTest;	18;	Pass;	;	;	09/08/2022 11:48:20
1;	12:DC-30;	PatGenTest;	19;	Pass;	;	;	09/08/2022 11:48:20
1;	12:DC-30;	PatGenTest;	20;	Pass;	;	;	09/08/2022 11:48:20
1;	3:DC-30;	DVMCommonMode;	1;	Pass;	;	;	09/08/2022 11:48:22
1;	3:DC-30;	DVMCommonMode;	2;	Pass;	;	;	09/08/2022 11:48:22
1;	5:DC-30;	DVMCommonMode;	1;	Pass;	;	;	09/08/2022 11:48:22
1;	5:DC-30;	DVMCommonMode;	2;	Pass;	;	;	09/08/2022 11:48:22
1;	9:DC-30;	DVMCommonMode;	1;	Pass;	;	;	09/08/2022 11:48:22
1;	9:DC-30;	DVMCommonMode;	2;	Pass;	;	;	09/08/2022 11:48:22
1;	12:DC-30;	DVMCommonMode;	1;	Pass;	;	;	09/08/2022 11:48:22
1;	12:DC-30;	DVMCommonMode;	2;	Pass;	;	;	09/08/2022 11:48:22
1;	3:DC-30;	Async&ThroughputTest;		VI 1;	Pass;	;	09/08/2022 11:48:24
1;	3:DC-30;	Async&ThroughputTest;		VI 2;	Pass;	;	09/08/2022 11:48:24
1;	3:DC-30;	Async&ThroughputTest;		VI 3;	Pass;	;	09/08/2022 11:48:24
1;	3:DC-30;	Async&ThroughputTest;		VI 4;	Pass;	;	09/08/2022 11:48:24
1;	3:DC-30;	Async&ThroughputTest;		VI 5;	Pass;	;	09/08/2022 11:48:24
1;	3:DC-30;	Async&ThroughputTest;		VI 6;	Pass;	;	09/08/2022 11:48:24
1;	3:DC-30;	Async&ThroughputTest;		VI 7;	Pass;	;	09/08/2022 11:48:24
1;	3:DC-30;	Async&ThroughputTest;		VI 8;	Pass;	;	09/08/2022 11:48:24

1; 11:48:24	3:DC-30;	Async&ThroughputTest;	VI 9;	Pass;	;	;	09/08/2022
1; 11:48:24	3:DC-30;	Async&ThroughputTest;	VI 10;	Pass;	;	;	09/08/2022
1; 11:48:24	3:DC-30;	Async&ThroughputTest;	VI 11;	Pass;	;	;	09/08/2022
1; 11:48:24	3:DC-30;	Async&ThroughputTest;	VI 12;	Pass;	;	;	09/08/2022
1; 11:48:24	3:DC-30;	Async&ThroughputTest;	VI 13;	Pass;	;	;	09/08/2022
1; 11:48:24	3:DC-30;	Async&ThroughputTest;	VI 14;	Pass;	;	;	09/08/2022
1; 11:48:24	3:DC-30;	Async&ThroughputTest;	VI 15;	Pass;	;	;	09/08/2022
1; 11:48:24	3:DC-30;	Async&ThroughputTest;	VI 16;	Pass;	;	;	09/08/2022
1; 11:48:24	3:DC-30;	Async&ThroughputTest;	VI 17;	Pass;	;	;	09/08/2022
1; 11:48:24	3:DC-30;	Async&ThroughputTest;	VI 18;	Pass;	;	;	09/08/2022
1; 11:48:24	3:DC-30;	Async&ThroughputTest;	VI 19;	Pass;	;	;	09/08/2022
1; 11:48:24	3:DC-30;	Async&ThroughputTest;	VI 20;	Pass;	;	;	09/08/2022
1; 11:48:24	3:DC-30;	Async&ThroughputTest;	DVM 1;	Pass;	;	;	09/08/2022
1; 11:48:24	3:DC-30;	Async&ThroughputTest;	DVM 2;	Pass;	;	;	09/08/2022
1; 11:48:24	5:DC-30;	Async&ThroughputTest;	VI 1;	Pass;	;	;	09/08/2022
1; 11:48:24	5:DC-30;	Async&ThroughputTest;	VI 2;	Pass;	;	;	09/08/2022
1; 11:48:24	5:DC-30;	Async&ThroughputTest;	VI 3;	Pass;	;	;	09/08/2022
1; 11:48:24	5:DC-30;	Async&ThroughputTest;	VI 4;	Pass;	;	;	09/08/2022
1; 11:48:24	5:DC-30;	Async&ThroughputTest;	VI 5;	Pass;	;	;	09/08/2022
1; 11:48:24	5:DC-30;	Async&ThroughputTest;	VI 6;	Pass;	;	;	09/08/2022
1; 11:48:24	5:DC-30;	Async&ThroughputTest;	VI 7;	Pass;	;	;	09/08/2022
1; 11:48:24	5:DC-30;	Async&ThroughputTest;	VI 8;	Pass;	;	;	09/08/2022
1; 11:48:24	5:DC-30;	Async&ThroughputTest;	VI 9;	Pass;	;	;	09/08/2022
1; 11:48:24	5:DC-30;	Async&ThroughputTest;	VI 10;	Pass;	;	;	09/08/2022

1; 11:48:24	5:DC-30;	Async&ThroughputTest;	VI 11;	Pass;	;	;	09/08/2022
1; 11:48:24	5:DC-30;	Async&ThroughputTest;	VI 12;	Pass;	;	;	09/08/2022
1; 11:48:24	5:DC-30;	Async&ThroughputTest;	VI 13;	Pass;	;	;	09/08/2022
1; 11:48:24	5:DC-30;	Async&ThroughputTest;	VI 14;	Pass;	;	;	09/08/2022
1; 11:48:24	5:DC-30;	Async&ThroughputTest;	VI 15;	Pass;	;	;	09/08/2022
1; 11:48:24	5:DC-30;	Async&ThroughputTest;	VI 16;	Pass;	;	;	09/08/2022
1; 11:48:24	5:DC-30;	Async&ThroughputTest;	VI 17;	Pass;	;	;	09/08/2022
1; 11:48:24	5:DC-30;	Async&ThroughputTest;	VI 18;	Pass;	;	;	09/08/2022
1; 11:48:24	5:DC-30;	Async&ThroughputTest;	VI 19;	Pass;	;	;	09/08/2022
1; 11:48:24	5:DC-30;	Async&ThroughputTest;	VI 20;	Pass;	;	;	09/08/2022
1; 11:48:24	5:DC-30;	Async&ThroughputTest;	DVM 1;	Pass;	;	;	09/08/2022
1; 11:48:24	5:DC-30;	Async&ThroughputTest;	DVM 2;	Pass;	;	;	09/08/2022
1; 11:48:24	9:DC-30;	Async&ThroughputTest;	VI 1;	Pass;	;	;	09/08/2022
1; 11:48:24	9:DC-30;	Async&ThroughputTest;	VI 2;	Pass;	;	;	09/08/2022
1; 11:48:24	9:DC-30;	Async&ThroughputTest;	VI 3;	Pass;	;	;	09/08/2022
1; 11:48:24	9:DC-30;	Async&ThroughputTest;	VI 4;	Pass;	;	;	09/08/2022
1; 11:48:24	9:DC-30;	Async&ThroughputTest;	VI 5;	Pass;	;	;	09/08/2022
1; 11:48:24	9:DC-30;	Async&ThroughputTest;	VI 6;	Pass;	;	;	09/08/2022
1; 11:48:24	9:DC-30;	Async&ThroughputTest;	VI 7;	Pass;	;	;	09/08/2022
1; 11:48:24	9:DC-30;	Async&ThroughputTest;	VI 8;	Pass;	;	;	09/08/2022
1; 11:48:24	9:DC-30;	Async&ThroughputTest;	VI 9;	Pass;	;	;	09/08/2022
1; 11:48:24	9:DC-30;	Async&ThroughputTest;	VI 10;	Pass;	;	;	09/08/2022
1; 11:48:24	9:DC-30;	Async&ThroughputTest;	VI 11;	Pass;	;	;	09/08/2022
1; 11:48:24	9:DC-30;	Async&ThroughputTest;	VI 12;	Pass;	;	;	09/08/2022

1; 11:48:24	9:DC-30;	Async&ThroughputTest;	VI 13;	Pass;	;	;	09/08/2022
1; 11:48:24	9:DC-30;	Async&ThroughputTest;	VI 14;	Pass;	;	;	09/08/2022
1; 11:48:24	9:DC-30;	Async&ThroughputTest;	VI 15;	Pass;	;	;	09/08/2022
1; 11:48:24	9:DC-30;	Async&ThroughputTest;	VI 16;	Pass;	;	;	09/08/2022
1; 11:48:24	9:DC-30;	Async&ThroughputTest;	VI 17;	Pass;	;	;	09/08/2022
1; 11:48:24	9:DC-30;	Async&ThroughputTest;	VI 18;	Pass;	;	;	09/08/2022
1; 11:48:24	9:DC-30;	Async&ThroughputTest;	VI 19;	Pass;	;	;	09/08/2022
1; 11:48:24	9:DC-30;	Async&ThroughputTest;	VI 20;	Pass;	;	;	09/08/2022
1; 11:48:24	9:DC-30;	Async&ThroughputTest;	DVM 1;	Pass;	;	;	09/08/2022
1; 11:48:24	9:DC-30;	Async&ThroughputTest;	DVM 2;	Pass;	;	;	09/08/2022
1; 11:48:24	12:DC-30;	Async&ThroughputTest;	VI 1;	Pass;	;	;	09/08/2022
1; 11:48:24	12:DC-30;	Async&ThroughputTest;	VI 2;	Pass;	;	;	09/08/2022
1; 11:48:24	12:DC-30;	Async&ThroughputTest;	VI 3;	Pass;	;	;	09/08/2022
1; 11:48:24	12:DC-30;	Async&ThroughputTest;	VI 4;	Pass;	;	;	09/08/2022
1; 11:48:24	12:DC-30;	Async&ThroughputTest;	VI 5;	Pass;	;	;	09/08/2022
1; 11:48:24	12:DC-30;	Async&ThroughputTest;	VI 6;	Pass;	;	;	09/08/2022
1; 11:48:24	12:DC-30;	Async&ThroughputTest;	VI 7;	Pass;	;	;	09/08/2022
1; 11:48:24	12:DC-30;	Async&ThroughputTest;	VI 8;	Pass;	;	;	09/08/2022
1; 11:48:24	12:DC-30;	Async&ThroughputTest;	VI 9;	Pass;	;	;	09/08/2022
1; 11:48:24	12:DC-30;	Async&ThroughputTest;	VI 10;	Pass;	;	;	09/08/2022
1; 11:48:24	12:DC-30;	Async&ThroughputTest;	VI 11;	Pass;	;	;	09/08/2022
1; 11:48:24	12:DC-30;	Async&ThroughputTest;	VI 12;	Pass;	;	;	09/08/2022
1; 11:48:24	12:DC-30;	Async&ThroughputTest;	VI 13;	Pass;	;	;	09/08/2022
1; 11:48:24	12:DC-30;	Async&ThroughputTest;	VI 14;	Pass;	;	;	09/08/2022



1;	12:DC-30;	Async&ThroughputTest;	VI 15;	Pass;	;	;	09/08/2022
11:48:24							
1;	12:DC-30;	Async&ThroughputTest;	VI 16;	Pass;	;	;	09/08/2022
11:48:24							
1;	12:DC-30;	Async&ThroughputTest;	VI 17;	Pass;	;	;	09/08/2022
11:48:24							
1;	12:DC-30;	Async&ThroughputTest;	VI 18;	Pass;	;	;	09/08/2022
11:48:24							
1;	12:DC-30;	Async&ThroughputTest;	VI 19;	Pass;	;	;	09/08/2022
11:48:24							
1;	12:DC-30;	Async&ThroughputTest;	VI 20;	Pass;	;	;	09/08/2022
11:48:24							
1;	12:DC-30;	Async&ThroughputTest;	DVM 1;	Pass;	;	;	09/08/2022
11:48:24							
1;	12:DC-30;	Async&ThroughputTest;	DVM 2;	Pass;	;	;	09/08/2022
11:48:24							
1;	3:DC-30;	VI_Filters;	1;	Pass;	;	;	09/08/2022 11:48:26
1;	3:DC-30;	VI_Filters;	2;	Pass;	;	;	09/08/2022 11:48:26
1;	3:DC-30;	VI_Filters;	3;	Pass;	;	;	09/08/2022 11:48:26
1;	3:DC-30;	VI_Filters;	4;	Pass;	;	;	09/08/2022 11:48:26
1;	3:DC-30;	VI_Filters;	5;	Pass;	;	;	09/08/2022 11:48:26
1;	3:DC-30;	VI_Filters;	6;	Pass;	;	;	09/08/2022 11:48:26
1;	3:DC-30;	VI_Filters;	7;	Pass;	;	;	09/08/2022 11:48:26
1;	3:DC-30;	VI_Filters;	8;	Pass;	;	;	09/08/2022 11:48:26
1;	3:DC-30;	VI_Filters;	9;	Pass;	;	;	09/08/2022 11:48:26
1;	3:DC-30;	VI_Filters;	10;	Pass;	;	;	09/08/2022 11:48:26
1;	3:DC-30;	VI_Filters;	11;	Pass;	;	;	09/08/2022 11:48:26
1;	3:DC-30;	VI_Filters;	12;	Pass;	;	;	09/08/2022 11:48:26
1;	3:DC-30;	VI_Filters;	13;	Pass;	;	;	09/08/2022 11:48:26
1;	3:DC-30;	VI_Filters;	14;	Pass;	;	;	09/08/2022 11:48:26
1;	3:DC-30;	VI_Filters;	15;	Pass;	;	;	09/08/2022 11:48:26
1;	3:DC-30;	VI_Filters;	16;	Pass;	;	;	09/08/2022 11:48:26
1;	3:DC-30;	VI_Filters;	17;	Pass;	;	;	09/08/2022 11:48:26
1;	3:DC-30;	VI_Filters;	18;	Pass;	;	;	09/08/2022 11:48:26
1;	3:DC-30;	VI_Filters;	19;	Pass;	;	;	09/08/2022 11:48:26
1;	3:DC-30;	VI_Filters;	20;	Pass;	;	;	09/08/2022 11:48:26
1;	5:DC-30;	VI_Filters;	1;	Pass;	;	;	09/08/2022 11:48:26
1;	5:DC-30;	VI_Filters;	2;	Pass;	;	;	09/08/2022 11:48:26
1;	5:DC-30;	VI_Filters;	3;	Pass;	;	;	09/08/2022 11:48:26
1;	5:DC-30;	VI_Filters;	4;	Pass;	;	;	09/08/2022 11:48:26
1;	5:DC-30;	VI_Filters;	5;	Pass;	;	;	09/08/2022 11:48:26
1;	5:DC-30;	VI_Filters;	6;	Pass;	;	;	09/08/2022 11:48:26
1;	5:DC-30;	VI_Filters;	7;	Pass;	;	;	09/08/2022 11:48:26
1;	5:DC-30;	VI_Filters;	8;	Pass;	;	;	09/08/2022 11:48:26
1;	5:DC-30;	VI_Filters;	9;	Pass;	;	;	09/08/2022 11:48:26
1;	5:DC-30;	VI_Filters;	10;	Pass;	;	;	09/08/2022 11:48:26
1;	5:DC-30;	VI_Filters;	11;	Pass;	;	;	09/08/2022 11:48:26
1;	5:DC-30;	VI_Filters;	12;	Pass;	;	;	09/08/2022 11:48:26







1; 11:48:52	3:DC-30;	VI_DgsAlarmsNconnection;	5;	Pass;	;	;	09/08/2022
1; 11:48:52	3:DC-30;	VI_DgsAlarmsNconnection;	6;	Pass;	;	;	09/08/2022
1; 11:48:52	3:DC-30;	VI_DgsAlarmsNconnection;	7;	Pass;	;	;	09/08/2022
1; 11:48:52	3:DC-30;	VI_DgsAlarmsNconnection;	8;	Pass;	;	;	09/08/2022
1; 11:48:52	3:DC-30;	VI_DgsAlarmsNconnection;	9;	Pass;	;	;	09/08/2022
1; 11:48:52	3:DC-30;	VI_DgsAlarmsNconnection;	10;	Pass;	;	;	09/08/2022
1; 11:48:52	3:DC-30;	VI_DgsAlarmsNconnection;	11;	Pass;	;	;	09/08/2022
1; 11:48:52	3:DC-30;	VI_DgsAlarmsNconnection;	12;	Pass;	;	;	09/08/2022
1; 11:48:52	3:DC-30;	VI_DgsAlarmsNconnection;	13;	Pass;	;	;	09/08/2022
1; 11:48:52	3:DC-30;	VI_DgsAlarmsNconnection;	14;	Pass;	;	;	09/08/2022
1; 11:48:52	3:DC-30;	VI_DgsAlarmsNconnection;	15;	Pass;	;	;	09/08/2022
1; 11:48:52	3:DC-30;	VI_DgsAlarmsNconnection;	16;	Pass;	;	;	09/08/2022
1; 11:48:52	3:DC-30;	VI_DgsAlarmsNconnection;	17;	Pass;	;	;	09/08/2022
1; 11:48:52	3:DC-30;	VI_DgsAlarmsNconnection;	18;	Pass;	;	;	09/08/2022
1; 11:48:52	3:DC-30;	VI_DgsAlarmsNconnection;	19;	Pass;	;	;	09/08/2022
1; 11:48:52	3:DC-30;	VI_DgsAlarmsNconnection;	20;	Pass;	;	;	09/08/2022
1; 11:48:52	5:DC-30;	VI_DgsAlarmsNconnection;	1;	Pass;	;	;	09/08/2022
1; 11:48:52	5:DC-30;	VI_DgsAlarmsNconnection;	2;	Pass;	;	;	09/08/2022
1; 11:48:52	5:DC-30;	VI_DgsAlarmsNconnection;	3;	Pass;	;	;	09/08/2022
1; 11:48:52	5:DC-30;	VI_DgsAlarmsNconnection;	4;	Pass;	;	;	09/08/2022
1; 11:48:52	5:DC-30;	VI_DgsAlarmsNconnection;	5;	Pass;	;	;	09/08/2022
1; 11:48:52	5:DC-30;	VI_DgsAlarmsNconnection;	6;	Pass;	;	;	09/08/2022
1; 11:48:52	5:DC-30;	VI_DgsAlarmsNconnection;	7;	Pass;	;	;	09/08/2022
1; 11:48:52	5:DC-30;	VI_DgsAlarmsNconnection;	8;	Pass;	;	;	09/08/2022

1; 11:48:52	5:DC-30;	VI_DgsAlarmsNconnection;	9;	Pass;	;	;	09/08/2022
1; 11:48:52	5:DC-30;	VI_DgsAlarmsNconnection;	10;	Pass;	;	;	09/08/2022
1; 11:48:52	5:DC-30;	VI_DgsAlarmsNconnection;	11;	Pass;	;	;	09/08/2022
1; 11:48:52	5:DC-30;	VI_DgsAlarmsNconnection;	12;	Pass;	;	;	09/08/2022
1; 11:48:52	5:DC-30;	VI_DgsAlarmsNconnection;	13;	Pass;	;	;	09/08/2022
1; 11:48:52	5:DC-30;	VI_DgsAlarmsNconnection;	14;	Pass;	;	;	09/08/2022
1; 11:48:52	5:DC-30;	VI_DgsAlarmsNconnection;	15;	Pass;	;	;	09/08/2022
1; 11:48:52	5:DC-30;	VI_DgsAlarmsNconnection;	16;	Pass;	;	;	09/08/2022
1; 11:48:52	5:DC-30;	VI_DgsAlarmsNconnection;	17;	Pass;	;	;	09/08/2022
1; 11:48:52	5:DC-30;	VI_DgsAlarmsNconnection;	18;	Pass;	;	;	09/08/2022
1; 11:48:52	5:DC-30;	VI_DgsAlarmsNconnection;	19;	Pass;	;	;	09/08/2022
1; 11:48:52	5:DC-30;	VI_DgsAlarmsNconnection;	20;	Pass;	;	;	09/08/2022
1; 11:48:52	9:DC-30;	VI_DgsAlarmsNconnection;	1;	Pass;	;	;	09/08/2022
1; 11:48:52	9:DC-30;	VI_DgsAlarmsNconnection;	2;	Pass;	;	;	09/08/2022
1; 11:48:52	9:DC-30;	VI_DgsAlarmsNconnection;	3;	Pass;	;	;	09/08/2022
1; 11:48:52	9:DC-30;	VI_DgsAlarmsNconnection;	4;	Pass;	;	;	09/08/2022
1; 11:48:52	9:DC-30;	VI_DgsAlarmsNconnection;	5;	Pass;	;	;	09/08/2022
1; 11:48:52	9:DC-30;	VI_DgsAlarmsNconnection;	6;	Pass;	;	;	09/08/2022
1; 11:48:52	9:DC-30;	VI_DgsAlarmsNconnection;	7;	Pass;	;	;	09/08/2022
1; 11:48:52	9:DC-30;	VI_DgsAlarmsNconnection;	8;	Pass;	;	;	09/08/2022
1; 11:48:52	9:DC-30;	VI_DgsAlarmsNconnection;	9;	Pass;	;	;	09/08/2022
1; 11:48:52	9:DC-30;	VI_DgsAlarmsNconnection;	10;	Pass;	;	;	09/08/2022
1; 11:48:52	9:DC-30;	VI_DgsAlarmsNconnection;	11;	Pass;	;	;	09/08/2022
1; 11:48:52	9:DC-30;	VI_DgsAlarmsNconnection;	12;	Pass;	;	;	09/08/2022

1; 11:48:52	9:DC-30;	VI_DgsAlarmsNconnection;	13;	Pass;	;	;	09/08/2022
1; 11:48:52	9:DC-30;	VI_DgsAlarmsNconnection;	14;	Pass;	;	;	09/08/2022
1; 11:48:52	9:DC-30;	VI_DgsAlarmsNconnection;	15;	Pass;	;	;	09/08/2022
1; 11:48:52	9:DC-30;	VI_DgsAlarmsNconnection;	16;	Pass;	;	;	09/08/2022
1; 11:48:52	9:DC-30;	VI_DgsAlarmsNconnection;	17;	Pass;	;	;	09/08/2022
1; 11:48:52	9:DC-30;	VI_DgsAlarmsNconnection;	18;	Pass;	;	;	09/08/2022
1; 11:48:52	9:DC-30;	VI_DgsAlarmsNconnection;	19;	Pass;	;	;	09/08/2022
1; 11:48:52	9:DC-30;	VI_DgsAlarmsNconnection;	20;	Pass;	;	;	09/08/2022
1; 11:48:52	12:DC-30;	VI_DgsAlarmsNconnection;	1;	Pass;	;	;	09/08/2022
1; 11:48:52	12:DC-30;	VI_DgsAlarmsNconnection;	2;	Pass;	;	;	09/08/2022
1; 11:48:52	12:DC-30;	VI_DgsAlarmsNconnection;	3;	Pass;	;	;	09/08/2022
1; 11:48:52	12:DC-30;	VI_DgsAlarmsNconnection;	4;	Pass;	;	;	09/08/2022
1; 11:48:52	12:DC-30;	VI_DgsAlarmsNconnection;	5;	Pass;	;	;	09/08/2022
1; 11:48:52	12:DC-30;	VI_DgsAlarmsNconnection;	6;	Pass;	;	;	09/08/2022
1; 11:48:52	12:DC-30;	VI_DgsAlarmsNconnection;	7;	Pass;	;	;	09/08/2022
1; 11:48:52	12:DC-30;	VI_DgsAlarmsNconnection;	8;	Pass;	;	;	09/08/2022
1; 11:48:52	12:DC-30;	VI_DgsAlarmsNconnection;	9;	Pass;	;	;	09/08/2022
1; 11:48:52	12:DC-30;	VI_DgsAlarmsNconnection;	10;	Pass;	;	;	09/08/2022
1; 11:48:52	12:DC-30;	VI_DgsAlarmsNconnection;	11;	Pass;	;	;	09/08/2022
1; 11:48:52	12:DC-30;	VI_DgsAlarmsNconnection;	12;	Pass;	;	;	09/08/2022
1; 11:48:52	12:DC-30;	VI_DgsAlarmsNconnection;	13;	Pass;	;	;	09/08/2022
1; 11:48:52	12:DC-30;	VI_DgsAlarmsNconnection;	14;	Pass;	;	;	09/08/2022
1; 11:48:52	12:DC-30;	VI_DgsAlarmsNconnection;	15;	Pass;	;	;	09/08/2022
1; 11:48:52	12:DC-30;	VI_DgsAlarmsNconnection;	16;	Pass;	;	;	09/08/2022

1;	12:DC-30;	VI_DgsAlarmsNconnection;	17;	Pass;	;	;	09/08/2022
11:48:52							
1;	12:DC-30;	VI_DgsAlarmsNconnection;	18;	Pass;	;	;	09/08/2022
11:48:52							
1;	12:DC-30;	VI_DgsAlarmsNconnection;	19;	Pass;	;	;	09/08/2022
11:48:52							
1;	12:DC-30;	VI_DgsAlarmsNconnection;	20;	Pass;	;	;	09/08/2022
11:48:52							
1;	2:DC-90;	CalibrationTest_90;	;	Pass;	;	;	09/08/2022 11:48:54
1;	10:DC-90;	CalibrationTest_90;	;	Pass;	;	;	09/08/2022 11:48:54
1;	11:DC-90;	CalibrationTest_90;	;	Pass;	;	;	09/08/2022 11:48:54
1;	2:DC-90;	FPGA_Version;	;	Pass;	;	;	09/08/2022 11:48:55
1;	10:DC-90;	FPGA_Version;	;	Pass;	;	;	09/08/2022 11:48:55
1;	11:DC-90;	FPGA_Version;	;	Pass;	;	;	09/08/2022 11:48:55
1;	2:DC-90;	VI_Voltage;	1;	Pass;	;	;	09/08/2022 11:49:00
1;	2:DC-90;	VI_Voltage;	2;	Pass;	;	;	09/08/2022 11:49:00
1;	2:DC-90;	VI_Voltage;	3;	Pass;	;	;	09/08/2022 11:49:00
1;	2:DC-90;	VI_Voltage;	4;	Pass;	;	;	09/08/2022 11:49:00
1;	10:DC-90;	VI_Voltage;	1;	Pass;	;	;	09/08/2022 11:49:00
1;	10:DC-90;	VI_Voltage;	2;	Pass;	;	;	09/08/2022 11:49:00
1;	10:DC-90;	VI_Voltage;	3;	Pass;	;	;	09/08/2022 11:49:00
1;	10:DC-90;	VI_Voltage;	4;	Pass;	;	;	09/08/2022 11:49:00
1;	11:DC-90;	VI_Voltage;	1;	Pass;	;	;	09/08/2022 11:49:00
1;	11:DC-90;	VI_Voltage;	2;	Pass;	;	;	09/08/2022 11:49:00
1;	11:DC-90;	VI_Voltage;	3;	Pass;	;	;	09/08/2022 11:49:00
1;	11:DC-90;	VI_Voltage;	4;	Pass;	;	;	09/08/2022 11:49:00
1;	2:DC-90;	VI_Source&Capture;	1;	Pass;	;	;	09/08/2022 11:49:08
1;	2:DC-90;	VI_Source&Capture;	2;	Pass;	;	;	09/08/2022 11:49:08
1;	2:DC-90;	VI_Source&Capture;	3;	Pass;	;	;	09/08/2022 11:49:08
1;	2:DC-90;	VI_Source&Capture;	4;	Pass;	;	;	09/08/2022 11:49:08
1;	10:DC-90;	VI_Source&Capture;	1;	Pass;	;	;	09/08/2022 11:49:08
1;	10:DC-90;	VI_Source&Capture;	2;	Pass;	;	;	09/08/2022 11:49:08
1;	10:DC-90;	VI_Source&Capture;	3;	Pass;	;	;	09/08/2022 11:49:08
1;	10:DC-90;	VI_Source&Capture;	4;	Pass;	;	;	09/08/2022 11:49:08
1;	11:DC-90;	VI_Source&Capture;	1;	Pass;	;	;	09/08/2022 11:49:08
1;	11:DC-90;	VI_Source&Capture;	2;	Pass;	;	;	09/08/2022 11:49:08
1;	11:DC-90;	VI_Source&Capture;	3;	Pass;	;	;	09/08/2022 11:49:08
1;	11:DC-90;	VI_Source&Capture;	4;	Pass;	;	;	09/08/2022 11:49:08
1;	2:DC-90;	VI_PatGenTest;	1;	Pass;	;	;	09/08/2022 11:49:11
1;	2:DC-90;	VI_PatGenTest;	2;	Pass;	;	;	09/08/2022 11:49:11
1;	2:DC-90;	VI_PatGenTest;	3;	Pass;	;	;	09/08/2022 11:49:11
1;	2:DC-90;	VI_PatGenTest;	4;	Pass;	;	;	09/08/2022 11:49:11
1;	10:DC-90;	VI_PatGenTest;	1;	Pass;	;	;	09/08/2022 11:49:11
1;	10:DC-90;	VI_PatGenTest;	2;	Pass;	;	;	09/08/2022 11:49:11
1;	10:DC-90;	VI_PatGenTest;	3;	Pass;	;	;	09/08/2022 11:49:11
1;	10:DC-90;	VI_PatGenTest;	4;	Pass;	;	;	09/08/2022 11:49:11
1;	11:DC-90;	VI_PatGenTest;	1;	Pass;	;	;	09/08/2022 11:49:11
1;	11:DC-90;	VI_PatGenTest;	2;	Pass;	;	;	09/08/2022 11:49:11



1;	11:DC-90;	VI_PatGenTest; 3;	Pass;	;	;	09/08/2022 11:49:11
1;	11:DC-90;	VI_PatGenTest; 4;	Pass;	;	;	09/08/2022 11:49:11
1;	2:DC-90;	VI_MeterFilters;	1;	Pass;	;	09/08/2022 11:49:12
1;	2:DC-90;	VI_MeterFilters;	2;	Pass;	;	09/08/2022 11:49:12
1;	2:DC-90;	VI_MeterFilters;	3;	Pass;	;	09/08/2022 11:49:12
1;	2:DC-90;	VI_MeterFilters;	4;	Pass;	;	09/08/2022 11:49:12
1;	10:DC-90;	VI_MeterFilters;	1;	Pass;	;	09/08/2022 11:49:12
1;	10:DC-90;	VI_MeterFilters;	2;	Pass;	;	09/08/2022 11:49:12
1;	10:DC-90;	VI_MeterFilters;	3;	Pass;	;	09/08/2022 11:49:12
1;	10:DC-90;	VI_MeterFilters;	4;	Pass;	;	09/08/2022 11:49:12
1;	11:DC-90;	VI_MeterFilters;	1;	Pass;	;	09/08/2022 11:49:12
1;	11:DC-90;	VI_MeterFilters;	2;	Pass;	;	09/08/2022 11:49:12
1;	11:DC-90;	VI_MeterFilters;	3;	Pass;	;	09/08/2022 11:49:12
1;	11:DC-90;	VI_MeterFilters;	4;	Pass;	;	09/08/2022 11:49:12
1;	2:DC-90;	VI_MeterCurrent;	1;	Pass;	;	09/08/2022 11:49:53
1;	2:DC-90;	VI_MeterCurrent;	2;	Pass;	;	09/08/2022 11:49:53
1;	2:DC-90;	VI_MeterCurrent;	3;	Pass;	;	09/08/2022 11:49:53
1;	2:DC-90;	VI_MeterCurrent;	4;	Pass;	;	09/08/2022 11:49:53
1;	10:DC-90;	VI_MeterCurrent;	1;	Pass;	;	09/08/2022 11:49:53
1;	10:DC-90;	VI_MeterCurrent;	2;	Pass;	;	09/08/2022 11:49:53
1;	10:DC-90;	VI_MeterCurrent;	3;	Pass;	;	09/08/2022 11:49:53
1;	10:DC-90;	VI_MeterCurrent;	4;	Pass;	;	09/08/2022 11:49:53
1;	11:DC-90;	VI_MeterCurrent;	1;	Pass;	;	09/08/2022 11:49:53
1;	11:DC-90;	VI_MeterCurrent;	2;	Pass;	;	09/08/2022 11:49:53
1;	11:DC-90;	VI_MeterCurrent;	3;	Pass;	;	09/08/2022 11:49:53
1;	11:DC-90;	VI_MeterCurrent;	4;	Pass;	;	09/08/2022 11:49:53
1;	2:DC-90;	VI_CalBusConnect;	1;	Pass;	;	09/08/2022 11:50:03
1;	2:DC-90;	VI_CalBusConnect;	2;	Pass;	;	09/08/2022 11:50:03
1;	2:DC-90;	VI_CalBusConnect;	3;	Pass;	;	09/08/2022 11:50:03
1;	2:DC-90;	VI_CalBusConnect;	4;	Pass;	;	09/08/2022 11:50:03
1;	10:DC-90;	VI_CalBusConnect;	1;	Pass;	;	09/08/2022 11:50:03
1;	10:DC-90;	VI_CalBusConnect;	2;	Pass;	;	09/08/2022 11:50:03
1;	10:DC-90;	VI_CalBusConnect;	3;	Pass;	;	09/08/2022 11:50:03
1;	10:DC-90;	VI_CalBusConnect;	4;	Pass;	;	09/08/2022 11:50:03
1;	11:DC-90;	VI_CalBusConnect;	1;	Pass;	;	09/08/2022 11:50:03
1;	11:DC-90;	VI_CalBusConnect;	2;	Pass;	;	09/08/2022 11:50:03
1;	11:DC-90;	VI_CalBusConnect;	3;	Pass;	;	09/08/2022 11:50:03
1;	11:DC-90;	VI_CalBusConnect;	4;	Pass;	;	09/08/2022 11:50:03
1;	2:DC-90;	InternalCalBus; 1;	Pass;	;	;	09/08/2022 11:50:05
1;	10:DC-90;	InternalCalBus; 1;	Pass;	;	;	09/08/2022 11:50:05
1;	11:DC-90;	InternalCalBus; 1;	Pass;	;	;	09/08/2022 11:50:05
1;	2:DC-90;	VI_HiZBiPolar; 1;	Pass;	;	;	09/08/2022 11:50:13
1;	2:DC-90;	VI_HiZBiPolar; 2;	Pass;	;	;	09/08/2022 11:50:13
1;	2:DC-90;	VI_HiZBiPolar; 3;	Pass;	;	;	09/08/2022 11:50:13
1;	2:DC-90;	VI_HiZBiPolar; 4;	Pass;	;	;	09/08/2022 11:50:13
1;	10:DC-90;	VI_HiZBiPolar; 1;	Pass;	;	;	09/08/2022 11:50:13
1;	10:DC-90;	VI_HiZBiPolar; 2;	Pass;	;	;	09/08/2022 11:50:13
1;	10:DC-90;	VI_HiZBiPolar; 3;	Pass;	;	;	09/08/2022 11:50:13

1;	10:DC-90;	VI_HiZBiPolar; 4;	Pass; ; ;	09/08/2022 11:50:13
1;	11:DC-90;	VI_HiZBiPolar; 1;	Pass; ; ;	09/08/2022 11:50:13
1;	11:DC-90;	VI_HiZBiPolar; 2;	Pass; ; ;	09/08/2022 11:50:13
1;	11:DC-90;	VI_HiZBiPolar; 3;	Pass; ; ;	09/08/2022 11:50:13
1;	11:DC-90;	VI_HiZBiPolar; 4;	Pass; ; ;	09/08/2022 11:50:13
1;	2:DC-90;	VI_Unipolar; 1;	Pass; ; ;	09/08/2022 11:50:15
1;	2:DC-90;	VI_Unipolar; 2;	Pass; ; ;	09/08/2022 11:50:15
1;	2:DC-90;	VI_Unipolar; 3;	Pass; ; ;	09/08/2022 11:50:15
1;	2:DC-90;	VI_Unipolar; 4;	Pass; ; ;	09/08/2022 11:50:15
1;	10:DC-90;	VI_Unipolar; 1;	Pass; ; ;	09/08/2022 11:50:15
1;	10:DC-90;	VI_Unipolar; 2;	Pass; ; ;	09/08/2022 11:50:15
1;	10:DC-90;	VI_Unipolar; 3;	Pass; ; ;	09/08/2022 11:50:15
1;	10:DC-90;	VI_Unipolar; 4;	Pass; ; ;	09/08/2022 11:50:15
1;	11:DC-90;	VI_Unipolar; 1;	Pass; ; ;	09/08/2022 11:50:15
1;	11:DC-90;	VI_Unipolar; 2;	Pass; ; ;	09/08/2022 11:50:15
1;	11:DC-90;	VI_Unipolar; 3;	Pass; ; ;	09/08/2022 11:50:15
1;	11:DC-90;	VI_Unipolar; 4;	Pass; ; ;	09/08/2022 11:50:15
1;	2:DC-90;	VI_HiZUniPolar; 1;	Pass; ; ;	09/08/2022 11:50:26
1;	2:DC-90;	VI_HiZUniPolar; 2;	Pass; ; ;	09/08/2022 11:50:26
1;	2:DC-90;	VI_HiZUniPolar; 3;	Pass; ; ;	09/08/2022 11:50:26
1;	2:DC-90;	VI_HiZUniPolar; 4;	Pass; ; ;	09/08/2022 11:50:26
1;	10:DC-90;	VI_HiZUniPolar; 1;	Pass; ; ;	09/08/2022 11:50:26
1;	10:DC-90;	VI_HiZUniPolar; 2;	Pass; ; ;	09/08/2022 11:50:26
1;	10:DC-90;	VI_HiZUniPolar; 3;	Pass; ; ;	09/08/2022 11:50:26
1;	10:DC-90;	VI_HiZUniPolar; 4;	Pass; ; ;	09/08/2022 11:50:26
1;	11:DC-90;	VI_HiZUniPolar; 1;	Pass; ; ;	09/08/2022 11:50:26
1;	11:DC-90;	VI_HiZUniPolar; 2;	Pass; ; ;	09/08/2022 11:50:26
1;	11:DC-90;	VI_HiZUniPolar; 3;	Pass; ; ;	09/08/2022 11:50:26
1;	11:DC-90;	VI_HiZUniPolar; 4;	Pass; ; ;	09/08/2022 11:50:26
1;	2:DC-90;	VI_Current; 1;	Pass; ; ;	09/08/2022 11:50:49
1;	2:DC-90;	VI_Current; 2;	Pass; ; ;	09/08/2022 11:50:49
1;	2:DC-90;	VI_Current; 3;	Pass; ; ;	09/08/2022 11:50:49
1;	2:DC-90;	VI_Current; 4;	Pass; ; ;	09/08/2022 11:50:49
1;	10:DC-90;	VI_Current; 1;	Pass; ; ;	09/08/2022 11:50:49
1;	10:DC-90;	VI_Current; 2;	Pass; ; ;	09/08/2022 11:50:49
1;	10:DC-90;	VI_Current; 3;	Pass; ; ;	09/08/2022 11:50:49
1;	10:DC-90;	VI_Current; 4;	Pass; ; ;	09/08/2022 11:50:49
1;	11:DC-90;	VI_Current; 1;	Pass; ; ;	09/08/2022 11:50:49
1;	11:DC-90;	VI_Current; 2;	Pass; ; ;	09/08/2022 11:50:49
1;	11:DC-90;	VI_Current; 3;	Pass; ; ;	09/08/2022 11:50:49
1;	11:DC-90;	VI_Current; 4;	Pass; ; ;	09/08/2022 11:50:49
1;	2:DC-90;	VI_PulseCurrent; 1;	Pass; ; ;	09/08/2022 11:51:05
1;	2:DC-90;	VI_PulseCurrent; 2;	Pass; ; ;	09/08/2022 11:51:05
1;	2:DC-90;	VI_PulseCurrent; 3;	Pass; ; ;	09/08/2022 11:51:05
1;	2:DC-90;	VI_PulseCurrent; 4;	Pass; ; ;	09/08/2022 11:51:05
1;	10:DC-90;	VI_PulseCurrent; 1;	Pass; ; ;	09/08/2022 11:51:05
1;	10:DC-90;	VI_PulseCurrent; 2;	Pass; ; ;	09/08/2022 11:51:05
1;	10:DC-90;	VI_PulseCurrent; 3;	Pass; ; ;	09/08/2022 11:51:05

1;	10:DC-90;	VI_PulseCurrent;	4;	Pass;	;	;	09/08/2022 11:51:05
1;	11:DC-90;	VI_PulseCurrent;	1;	Pass;	;	;	09/08/2022 11:51:05
1;	11:DC-90;	VI_PulseCurrent;	2;	Pass;	;	;	09/08/2022 11:51:05
1;	11:DC-90;	VI_PulseCurrent;	3;	Pass;	;	;	09/08/2022 11:51:05
1;	11:DC-90;	VI_PulseCurrent;	4;	Pass;	;	;	09/08/2022 11:51:05
1;	2:DC-90;	VI_Merge2Ch; 1;	Pass;	;	;	;	09/08/2022 11:51:10
1;	2:DC-90;	VI_Merge2Ch; 2;	Pass;	;	;	;	09/08/2022 11:51:10
1;	10:DC-90;	VI_Merge2Ch; 1;	Pass;	;	;	;	09/08/2022 11:51:10
1;	10:DC-90;	VI_Merge2Ch; 2;	Pass;	;	;	;	09/08/2022 11:51:10
1;	11:DC-90;	VI_Merge2Ch; 1;	Pass;	;	;	;	09/08/2022 11:51:10
1;	11:DC-90;	VI_Merge2Ch; 2;	Pass;	;	;	;	09/08/2022 11:51:10
1;	2:DC-90;	VI_Merge3Ch; 1;	Pass;	;	;	;	09/08/2022 11:51:12
1;	10:DC-90;	VI_Merge3Ch; 1;	Pass;	;	;	;	09/08/2022 11:51:12
1;	11:DC-90;	VI_Merge3Ch; 1;	Pass;	;	;	;	09/08/2022 11:51:12
1;	2:DC-90;	VI_Merge4Ch; 1;	Pass;	;	;	;	09/08/2022 11:51:15
1;	10:DC-90;	VI_Merge4Ch; 1;	Pass;	;	;	;	09/08/2022 11:51:15
1;	11:DC-90;	VI_Merge4Ch; 1;	Pass;	;	;	;	09/08/2022 11:51:15
1;	2:DC-90;	VI_Alarms; 1;	Pass;	;	;	;	09/08/2022 11:51:18
1;	2:DC-90;	VI_Alarms; 2;	Pass;	;	;	;	09/08/2022 11:51:18
1;	2:DC-90;	VI_Alarms; 3;	Pass;	;	;	;	09/08/2022 11:51:18
1;	2:DC-90;	VI_Alarms; 4;	Pass;	;	;	;	09/08/2022 11:51:18
1;	10:DC-90;	VI_Alarms; 1;	Pass;	;	;	;	09/08/2022 11:51:18
1;	10:DC-90;	VI_Alarms; 2;	Pass;	;	;	;	09/08/2022 11:51:18
1;	10:DC-90;	VI_Alarms; 3;	Pass;	;	;	;	09/08/2022 11:51:18
1;	10:DC-90;	VI_Alarms; 4;	Pass;	;	;	;	09/08/2022 11:51:18
1;	11:DC-90;	VI_Alarms; 1;	Pass;	;	;	;	09/08/2022 11:51:18
1;	11:DC-90;	VI_Alarms; 2;	Pass;	;	;	;	09/08/2022 11:51:18
1;	11:DC-90;	VI_Alarms; 3;	Pass;	;	;	;	09/08/2022 11:51:18
1;	11:DC-90;	VI_Alarms; 4;	Pass;	;	;	;	09/08/2022 11:51:18
1;	2:DC-90;	VI_MergeVoltage;	1;	Pass;	;	;	09/08/2022 11:51:25
1;	2:DC-90;	VI_MergeVoltage;	2;	Pass;	;	;	09/08/2022 11:51:25
1;	2:DC-90;	VI_MergeVoltage;	3;	Pass;	;	;	09/08/2022 11:51:25
1;	2:DC-90;	VI_MergeVoltage;	4;	Pass;	;	;	09/08/2022 11:51:25
1;	10:DC-90;	VI_MergeVoltage;	1;	Pass;	;	;	09/08/2022 11:51:25
1;	10:DC-90;	VI_MergeVoltage;	2;	Pass;	;	;	09/08/2022 11:51:25
1;	10:DC-90;	VI_MergeVoltage;	3;	Pass;	;	;	09/08/2022 11:51:25
1;	10:DC-90;	VI_MergeVoltage;	4;	Pass;	;	;	09/08/2022 11:51:25
1;	11:DC-90;	VI_MergeVoltage;	1;	Pass;	;	;	09/08/2022 11:51:25
1;	11:DC-90;	VI_MergeVoltage;	2;	Pass;	;	;	09/08/2022 11:51:25
1;	11:DC-90;	VI_MergeVoltage;	3;	Pass;	;	;	09/08/2022 11:51:25
1;	11:DC-90;	VI_MergeVoltage;	4;	Pass;	;	;	09/08/2022 11:51:25
1;	2:DC-90;	VI_MergePulse2Ch;	1;	Pass;	;	;	09/08/2022 11:51:31
1;	2:DC-90;	VI_MergePulse2Ch;	2;	Pass;	;	;	09/08/2022 11:51:31
1;	10:DC-90;	VI_MergePulse2Ch;	1;	Pass;	;	;	09/08/2022 11:51:31
1;	10:DC-90;	VI_MergePulse2Ch;	2;	Pass;	;	;	09/08/2022 11:51:31
1;	11:DC-90;	VI_MergePulse2Ch;	1;	Pass;	;	;	09/08/2022 11:51:31
1;	11:DC-90;	VI_MergePulse2Ch;	2;	Pass;	;	;	09/08/2022 11:51:31
1;	2:DC-90;	VI_MergePulse3Ch;	1;	Pass;	;	;	09/08/2022 11:51:34

1;	10:DC-90;	VI_MergePulse3Ch;	1;	Pass;	;	;	09/08/2022 11:51:34
1;	11:DC-90;	VI_MergePulse3Ch;	1;	Pass;	;	;	09/08/2022 11:51:34
1;	2:DC-90;	VI_MergePulse4Ch;	1;	Pass;	;	;	09/08/2022 11:51:38
1;	10:DC-90;	VI_MergePulse4Ch;	1;	Pass;	;	;	09/08/2022 11:51:38
1;	11:DC-90;	VI_MergePulse4Ch;	1;	Pass;	;	;	09/08/2022 11:51:38
1;	2:DC-90;	Tmu_StamperAcc;	1;	Pass;	;	;	09/08/2022 11:52:26
1;	2:DC-90;	Tmu_StamperAcc;	2;	Pass;	;	;	09/08/2022 11:52:26
1;	10:DC-90;	Tmu_StamperAcc;	1;	Pass;	;	;	09/08/2022 11:52:26
1;	10:DC-90;	Tmu_StamperAcc;	2;	Pass;	;	;	09/08/2022 11:52:26
1;	11:DC-90;	Tmu_StamperAcc;	1;	Pass;	;	;	09/08/2022 11:52:26
1;	11:DC-90;	Tmu_StamperAcc;	2;	Pass;	;	;	09/08/2022 11:52:26
1;	2:DC-90;	Tmu_FrontEnd; 1;	Pass;	;	;	;	09/08/2022 11:52:57
1;	2:DC-90;	Tmu_FrontEnd; 2;	Pass;	;	;	;	09/08/2022 11:52:57
1;	10:DC-90;	Tmu_FrontEnd; 1;	Pass;	;	;	;	09/08/2022 11:52:57
1;	10:DC-90;	Tmu_FrontEnd; 2;	Pass;	;	;	;	09/08/2022 11:52:57
1;	11:DC-90;	Tmu_FrontEnd; 1;	Pass;	;	;	;	09/08/2022 11:52:57
1;	11:DC-90;	Tmu_FrontEnd; 2;	Pass;	;	;	;	09/08/2022 11:52:57
1;	2:DC-90;	VI_BandWidth; 1;	Pass;	;	;	;	09/08/2022 11:53:20
1;	2:DC-90;	VI_BandWidth; 2;	Pass;	;	;	;	09/08/2022 11:53:20
1;	2:DC-90;	VI_BandWidth; 3;	Pass;	;	;	;	09/08/2022 11:53:20
1;	2:DC-90;	VI_BandWidth; 4;	Pass;	;	;	;	09/08/2022 11:53:20
1;	10:DC-90;	VI_BandWidth; 1;	Pass;	;	;	;	09/08/2022 11:53:20
1;	10:DC-90;	VI_BandWidth; 2;	Pass;	;	;	;	09/08/2022 11:53:20
1;	10:DC-90;	VI_BandWidth; 3;	Pass;	;	;	;	09/08/2022 11:53:20
1;	10:DC-90;	VI_BandWidth; 4;	Pass;	;	;	;	09/08/2022 11:53:20
1;	11:DC-90;	VI_BandWidth; 1;	Pass;	;	;	;	09/08/2022 11:53:20
1;	11:DC-90;	VI_BandWidth; 2;	Pass;	;	;	;	09/08/2022 11:53:20
1;	11:DC-90;	VI_BandWidth; 3;	Pass;	;	;	;	09/08/2022 11:53:20
1;	11:DC-90;	VI_BandWidth; 4;	Pass;	;	;	;	09/08/2022 11:53:20
1;	2:DC-90;	Tmu_TLE&Hysteresis;	1;	Pass;	;	;	09/08/2022 11:53:39
1;	2:DC-90;	Tmu_TLE&Hysteresis;	2;	Pass;	;	;	09/08/2022 11:53:39
1;	10:DC-90;	Tmu_TLE&Hysteresis;	1;	Pass;	;	;	09/08/2022 11:53:39
1;	10:DC-90;	Tmu_TLE&Hysteresis;	2;	Pass;	;	;	09/08/2022 11:53:39
1;	11:DC-90;	Tmu_TLE&Hysteresis;	1;	Pass;	;	;	09/08/2022 11:53:39
1;	11:DC-90;	Tmu_TLE&Hysteresis;	2;	Pass;	;	;	09/08/2022 11:53:39
1;	2:DC-90;	Async&Throughput;	1;	Pass;	;	;	09/08/2022 11:53:42
1;	2:DC-90;	Async&Throughput;	2;	Pass;	;	;	09/08/2022 11:53:42
1;	2:DC-90;	Async&Throughput;	3;	Pass;	;	;	09/08/2022 11:53:42
1;	2:DC-90;	Async&Throughput;	4;	Pass;	;	;	09/08/2022 11:53:42
1;	10:DC-90;	Async&Throughput;	1;	Pass;	;	;	09/08/2022 11:53:42
1;	10:DC-90;	Async&Throughput;	2;	Pass;	;	;	09/08/2022 11:53:42
1;	10:DC-90;	Async&Throughput;	3;	Pass;	;	;	09/08/2022 11:53:42
1;	10:DC-90;	Async&Throughput;	4;	Pass;	;	;	09/08/2022 11:53:42
1;	11:DC-90;	Async&Throughput;	1;	Pass;	;	;	09/08/2022 11:53:42
1;	11:DC-90;	Async&Throughput;	2;	Pass;	;	;	09/08/2022 11:53:42
1;	11:DC-90;	Async&Throughput;	3;	Pass;	;	;	09/08/2022 11:53:42
1;	11:DC-90;	Async&Throughput;	4;	Pass;	;	;	09/08/2022 11:53:42
1;	2:DC-90;	Tmu_Alarms;	1;	Pass;	;	;	09/08/2022 11:53:43

























1;	8:HSD-200;	ActiveLoad;	41;	Pass;	;	;	09/08/2022 11:54:40
1;	8:HSD-200;	ActiveLoad;	42;	Pass;	;	;	09/08/2022 11:54:40
1;	8:HSD-200;	ActiveLoad;	43;	Pass;	;	;	09/08/2022 11:54:40
1;	8:HSD-200;	ActiveLoad;	44;	Pass;	;	;	09/08/2022 11:54:40
1;	8:HSD-200;	ActiveLoad;	45;	Pass;	;	;	09/08/2022 11:54:40
1;	8:HSD-200;	ActiveLoad;	46;	Pass;	;	;	09/08/2022 11:54:40
1;	8:HSD-200;	ActiveLoad;	47;	Pass;	;	;	09/08/2022 11:54:40
1;	8:HSD-200;	ActiveLoad;	48;	Pass;	;	;	09/08/2022 11:54:40
1;	8:HSD-200;	DSMTO-Memories;	;	Pass;	;	;	09/08/2022 11:54:41
1;	8:HSD-200;	DSMTO-SCMEM Dnld;	;	Pass;	;	;	09/08/2022 11:54:41
1;	8:HSD-200;	DSMTO-MTO UtilCntr;	;	Pass;	;	;	09/08/2022 11:54:42
1;	8:HSD-200;	DSMTO-MTO XYCntr;	;	Pass;	;	;	09/08/2022 11:54:42
1;	8:HSD-200;	DSMTO-DSSC Basic;	;	Pass;	;	;	09/08/2022 11:54:42
1;	8:HSD-200;	DSMTO-DSSC Loopback;	;	Pass;	;	;	09/08/2022 11:54:42
1;	8:HSD-200;	FrequencyCounter;	1;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	2;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	3;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	4;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	5;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	6;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	7;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	8;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	9;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	10;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	11;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	12;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	13;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	14;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	15;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	16;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	17;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	18;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	19;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	20;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	21;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	22;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	23;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	24;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	25;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	26;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	27;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	28;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	29;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	30;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	31;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	32;	Pass;	;	;	09/08/2022 11:54:44
1;	8:HSD-200;	FrequencyCounter;	33;	Pass;	;	;	09/08/2022 11:54:44

















1; 11:54:49	8:HSD-200;	CommonModeComparator;	9;	Pass;	;	;	09/08/2022
1; 11:54:49	8:HSD-200;	CommonModeComparator;	10;	Pass;	;	;	09/08/2022
1; 11:54:49	8:HSD-200;	CommonModeComparator;	11;	Pass;	;	;	09/08/2022
1; 11:54:49	8:HSD-200;	CommonModeComparator;	12;	Pass;	;	;	09/08/2022
1; 11:54:49	8:HSD-200;	CommonModeComparator;	13;	Pass;	;	;	09/08/2022
1; 11:54:49	8:HSD-200;	CommonModeComparator;	14;	Pass;	;	;	09/08/2022
1; 11:54:49	8:HSD-200;	CommonModeComparator;	15;	Pass;	;	;	09/08/2022
1; 11:54:49	8:HSD-200;	CommonModeComparator;	16;	Pass;	;	;	09/08/2022
1; 11:54:49	8:HSD-200;	CommonModeComparator;	17;	Pass;	;	;	09/08/2022
1; 11:54:49	8:HSD-200;	CommonModeComparator;	18;	Pass;	;	;	09/08/2022
1; 11:54:49	8:HSD-200;	CommonModeComparator;	19;	Pass;	;	;	09/08/2022
1; 11:54:49	8:HSD-200;	CommonModeComparator;	20;	Pass;	;	;	09/08/2022
1; 11:54:49	8:HSD-200;	CommonModeComparator;	21;	Pass;	;	;	09/08/2022
1; 11:54:49	8:HSD-200;	CommonModeComparator;	22;	Pass;	;	;	09/08/2022
1; 11:54:49	8:HSD-200;	CommonModeComparator;	23;	Pass;	;	;	09/08/2022
1; 11:54:49	8:HSD-200;	CommonModeComparator;	24;	Pass;	;	;	09/08/2022
1; 11:54:49	8:HSD-200;	CommonModeComparator;	25;	Pass;	;	;	09/08/2022
1; 11:54:49	8:HSD-200;	CommonModeComparator;	26;	Pass;	;	;	09/08/2022
1; 11:54:49	8:HSD-200;	CommonModeComparator;	27;	Pass;	;	;	09/08/2022
1; 11:54:49	8:HSD-200;	CommonModeComparator;	28;	Pass;	;	;	09/08/2022
1; 11:54:49	8:HSD-200;	CommonModeComparator;	29;	Pass;	;	;	09/08/2022
1; 11:54:49	8:HSD-200;	CommonModeComparator;	30;	Pass;	;	;	09/08/2022
1; 11:54:49	8:HSD-200;	CommonModeComparator;	31;	Pass;	;	;	09/08/2022
1; 11:54:49	8:HSD-200;	CommonModeComparator;	32;	Pass;	;	;	09/08/2022



1;	8:HSD-200;	CommonModeComparator;	33;	Pass;	;	;	09/08/2022
11:54:49							
1;	8:HSD-200;	CommonModeComparator;	34;	Pass;	;	;	09/08/2022
11:54:49							
1;	8:HSD-200;	CommonModeComparator;	35;	Pass;	;	;	09/08/2022
11:54:49							
1;	8:HSD-200;	CommonModeComparator;	36;	Pass;	;	;	09/08/2022
11:54:49							
1;	8:HSD-200;	CommonModeComparator;	37;	Pass;	;	;	09/08/2022
11:54:49							
1;	8:HSD-200;	CommonModeComparator;	38;	Pass;	;	;	09/08/2022
11:54:49							
1;	8:HSD-200;	CommonModeComparator;	39;	Pass;	;	;	09/08/2022
11:54:49							
1;	8:HSD-200;	CommonModeComparator;	40;	Pass;	;	;	09/08/2022
11:54:49							
1;	8:HSD-200;	CommonModeComparator;	41;	Pass;	;	;	09/08/2022
11:54:49							
1;	8:HSD-200;	CommonModeComparator;	42;	Pass;	;	;	09/08/2022
11:54:49							
1;	8:HSD-200;	CommonModeComparator;	43;	Pass;	;	;	09/08/2022
11:54:49							
1;	8:HSD-200;	CommonModeComparator;	44;	Pass;	;	;	09/08/2022
11:54:49							
1;	8:HSD-200;	CommonModeComparator;	45;	Pass;	;	;	09/08/2022
11:54:49							
1;	8:HSD-200;	CommonModeComparator;	46;	Pass;	;	;	09/08/2022
11:54:49							
1;	8:HSD-200;	CommonModeComparator;	47;	Pass;	;	;	09/08/2022
11:54:49							
1;	8:HSD-200;	CommonModeComparator;	48;	Pass;	;	;	09/08/2022
11:54:49							
1;	8:HSD-200;	DifferentialComparator;1;	Pass;	;	;	;	09/08/2022 11:54:49
1;	8:HSD-200;	DifferentialComparator;2;	Pass;	;	;	;	09/08/2022 11:54:49
1;	8:HSD-200;	DifferentialComparator;3;	Pass;	;	;	;	09/08/2022 11:54:49
1;	8:HSD-200;	DifferentialComparator;4;	Pass;	;	;	;	09/08/2022 11:54:49
1;	8:HSD-200;	DifferentialComparator;5;	Pass;	;	;	;	09/08/2022 11:54:49
1;	8:HSD-200;	DifferentialComparator;6;	Pass;	;	;	;	09/08/2022 11:54:49
1;	8:HSD-200;	DifferentialComparator;7;	Pass;	;	;	;	09/08/2022 11:54:49
1;	8:HSD-200;	DifferentialComparator;8;	Pass;	;	;	;	09/08/2022 11:54:49
1;	8:HSD-200;	DifferentialComparator;9;	Pass;	;	;	;	09/08/2022 11:54:49
1;	8:HSD-200;	DifferentialComparator;10;	Pass;	;	;	;	09/08/2022 11:54:49
1;	8:HSD-200;	DifferentialComparator;11;	Pass;	;	;	;	09/08/2022 11:54:49
1;	8:HSD-200;	DifferentialComparator;12;	Pass;	;	;	;	09/08/2022 11:54:49
1;	8:HSD-200;	DifferentialComparator;13;	Pass;	;	;	;	09/08/2022 11:54:49
1;	8:HSD-200;	DifferentialComparator;14;	Pass;	;	;	;	09/08/2022 11:54:49
1;	8:HSD-200;	DifferentialComparator;15;	Pass;	;	;	;	09/08/2022 11:54:49
1;	8:HSD-200;	DifferentialComparator;16;	Pass;	;	;	;	09/08/2022 11:54:49



1;	8:HSD-200;	Alarms; 17;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 18;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 19;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 20;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 21;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 22;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 23;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 24;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 25;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 26;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 27;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 28;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 29;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 30;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 31;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 32;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 33;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 34;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 35;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 36;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 37;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 38;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 39;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 40;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 41;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 42;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 43;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 44;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 45;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 46;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 47;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	Alarms; 48;	Pass;	;	;	09/08/2022 11:54:55
1;	8:HSD-200;	ScanTest;	;	Pass;	;	09/08/2022 11:54:56
1;	8:HSD-200;	HV-Levels;	5;	Pass;	;	09/08/2022 11:55:01
1;	8:HSD-200;	HV-Levels;	18;	Pass;	;	09/08/2022 11:55:01
1;	4:HVD-1;	SMC Test;	;	Pass;	;	Option Passed; 09/08/2022 11:55:04
1;	4:HVD-1;	CkHvdRegisters;	;	Pass;	;	Option Passed; 09/08/2022
11:55:07						
1;	4:HVD-1;	DrvCmpLevels; 1;	Pass;	;	;	Option Passed; 09/08/2022 11:55:07
1;	4:HVD-1;	DrvCmpLevels; 2;	Pass;	;	;	Option Passed; 09/08/2022 11:55:07
1;	4:HVD-1;	DrvCmpLevels; 3;	Pass;	;	;	Option Passed; 09/08/2022 11:55:07
1;	4:HVD-1;	DrvCmpLevels; 4;	Pass;	;	;	Option Passed; 09/08/2022 11:55:07
1;	4:HVD-1;	DrvCmpLevels; 5;	Pass;	;	;	Option Passed; 09/08/2022 11:55:07
1;	4:HVD-1;	DrvCmpLevels; 6;	Pass;	;	;	Option Passed; 09/08/2022 11:55:07
1;	4:HVD-1;	DrvCmpLevels; 7;	Pass;	;	;	Option Passed; 09/08/2022 11:55:07
1;	4:HVD-1;	DrvCmpLevels; 8;	Pass;	;	;	Option Passed; 09/08/2022 11:55:07
1;	4:HVD-1;	DrvCmpLevels; 9;	Pass;	;	;	Option Passed; 09/08/2022 11:55:07
1;	4:HVD-1;	DrvCmpLevels; 10;	Pass;	;	;	Option Passed; 09/08/2022 11:55:07

1;	4:HVD-1;	DrvCmpLevels; 11;	Pass;	;	Option Passed; 09/08/2022 11:55:07
1;	4:HVD-1;	DrvCmpLevels; 12;	Pass;	;	Option Passed; 09/08/2022 11:55:07
1;	4:HVD-1;	DrvCmpLevels; 13;	Pass;	;	Option Passed; 09/08/2022 11:55:07
1;	4:HVD-1;	DrvCmpLevels; 14;	Pass;	;	Option Passed; 09/08/2022 11:55:07
1;	4:HVD-1;	DrvCmpLevels; 15;	Pass;	;	Option Passed; 09/08/2022 11:55:07
1;	4:HVD-1;	DrvCmpLevels; 16;	Pass;	;	Option Passed; 09/08/2022 11:55:07
1;	4:HVD-1;	DrvCmpLevels; 17;	Pass;	;	Option Passed; 09/08/2022 11:55:07
1;	4:HVD-1;	DrvCmpLevels; 18;	Pass;	;	Option Passed; 09/08/2022 11:55:07
1;	4:HVD-1;	DrvCmpLevels; 19;	Pass;	;	Option Passed; 09/08/2022 11:55:07
1;	4:HVD-1;	DrvCmpLevels; 20;	Pass;	;	Option Passed; 09/08/2022 11:55:07
1;	4:HVD-1;	DrvCmpLevels; 21;	Pass;	;	Option Passed; 09/08/2022 11:55:07
1;	4:HVD-1;	DrvCmpLevels; 22;	Pass;	;	Option Passed; 09/08/2022 11:55:07
1;	4:HVD-1;	DrvCmpLevels; 23;	Pass;	;	Option Passed; 09/08/2022 11:55:07
1;	4:HVD-1;	DrvCmpLevels; 24;	Pass;	;	Option Passed; 09/08/2022 11:55:07
1;	4:HVD-1;	DrvCmpFormats;	1;	Pass;	Option Passed; 09/08/2022 11:55:19
1;	4:HVD-1;	DrvCmpFormats;	2;	Pass;	Option Passed; 09/08/2022 11:55:19
1;	4:HVD-1;	DrvCmpFormats;	3;	Pass;	Option Passed; 09/08/2022 11:55:19
1;	4:HVD-1;	DrvCmpFormats;	4;	Pass;	Option Passed; 09/08/2022 11:55:19
1;	4:HVD-1;	DrvCmpFormats;	5;	Pass;	Option Passed; 09/08/2022 11:55:19
1;	4:HVD-1;	DrvCmpFormats;	6;	Pass;	Option Passed; 09/08/2022 11:55:19
1;	4:HVD-1;	DrvCmpFormats;	7;	Pass;	Option Passed; 09/08/2022 11:55:19
1;	4:HVD-1;	DrvCmpFormats;	8;	Pass;	Option Passed; 09/08/2022 11:55:19
1;	4:HVD-1;	DrvCmpFormats;	9;	Pass;	Option Passed; 09/08/2022 11:55:19
1;	4:HVD-1;	DrvCmpFormats;	10;	Pass;	Option Passed; 09/08/2022 11:55:19
1;	4:HVD-1;	DrvCmpFormats;	11;	Pass;	Option Passed; 09/08/2022 11:55:19
1;	4:HVD-1;	DrvCmpFormats;	12;	Pass;	Option Passed; 09/08/2022 11:55:19
1;	4:HVD-1;	DrvCmpFormats;	13;	Pass;	Option Passed; 09/08/2022 11:55:19
1;	4:HVD-1;	DrvCmpFormats;	14;	Pass;	Option Passed; 09/08/2022 11:55:19
1;	4:HVD-1;	DrvCmpFormats;	15;	Pass;	Option Passed; 09/08/2022 11:55:19
1;	4:HVD-1;	DrvCmpFormats;	16;	Pass;	Option Passed; 09/08/2022 11:55:19
1;	4:HVD-1;	DrvCmpFormats;	17;	Pass;	Option Passed; 09/08/2022 11:55:19

1;	4:HVD-1;	DrvCmpFormats;	18;	Pass;	;	Option Passed;	09/08/2022
11:55:19							
1;	4:HVD-1;	DrvCmpFormats;	19;	Pass;	;	Option Passed;	09/08/2022
11:55:19							
1;	4:HVD-1;	DrvCmpFormats;	20;	Pass;	;	Option Passed;	09/08/2022
11:55:19							
1;	4:HVD-1;	DrvCmpFormats;	21;	Pass;	;	Option Passed;	09/08/2022
11:55:19							
1;	4:HVD-1;	DrvCmpFormats;	22;	Pass;	;	Option Passed;	09/08/2022
11:55:19							
1;	4:HVD-1;	DrvCmpFormats;	23;	Pass;	;	Option Passed;	09/08/2022
11:55:19							
1;	4:HVD-1;	DrvCmpFormats;	24;	Pass;	;	Option Passed;	09/08/2022
11:55:19							
1;	4:HVD-1;	TG LVM BIST; 1;	Pass;	;		Option Passed;	09/08/2022 11:55:19
1;	4:HVD-1;	TG LVM BIST; 2;	Pass;	;		Option Passed;	09/08/2022 11:55:19
1;	4:HVD-1;	TG LVM BIST; 3;	Pass;	;		Option Passed;	09/08/2022 11:55:19
1;	4:HVD-1;	TG LVM BIST; 4;	Pass;	;		Option Passed;	09/08/2022 11:55:19
1;	4:HVD-1;	TG LVM BIST; 5;	Pass;	;		Option Passed;	09/08/2022 11:55:19
1;	4:HVD-1;	TG LVM BIST; 6;	Pass;	;		Option Passed;	09/08/2022 11:55:19
1;	4:HVD-1;	TG LVM BIST; 7;	Pass;	;		Option Passed;	09/08/2022 11:55:19
1;	4:HVD-1;	TG LVM BIST; 8;	Pass;	;		Option Passed;	09/08/2022 11:55:19
1;	4:HVD-1;	TG LVM BIST; 9;	Pass;	;		Option Passed;	09/08/2022 11:55:19
1;	4:HVD-1;	TG LVM BIST; 10;	Pass;	;		Option Passed;	09/08/2022 11:55:19
1;	4:HVD-1;	TG LVM BIST; 11;	Pass;	;		Option Passed;	09/08/2022 11:55:19
1;	4:HVD-1;	TG LVM BIST; 12;	Pass;	;		Option Passed;	09/08/2022 11:55:19
1;	4:HVD-1;	TG LVM BIST; 13;	Pass;	;		Option Passed;	09/08/2022 11:55:19
1;	4:HVD-1;	TG LVM BIST; 14;	Pass;	;		Option Passed;	09/08/2022 11:55:19
1;	4:HVD-1;	TG LVM BIST; 15;	Pass;	;		Option Passed;	09/08/2022 11:55:19
1;	4:HVD-1;	TG LVM BIST; 16;	Pass;	;		Option Passed;	09/08/2022 11:55:19
1;	4:HVD-1;	TG LVM BIST; 17;	Pass;	;		Option Passed;	09/08/2022 11:55:19
1;	4:HVD-1;	TG LVM BIST; 18;	Pass;	;		Option Passed;	09/08/2022 11:55:19
1;	4:HVD-1;	TG LVM BIST; 19;	Pass;	;		Option Passed;	09/08/2022 11:55:19
1;	4:HVD-1;	TG LVM BIST; 20;	Pass;	;		Option Passed;	09/08/2022 11:55:19
1;	4:HVD-1;	TG LVM BIST; 21;	Pass;	;		Option Passed;	09/08/2022 11:55:19
1;	4:HVD-1;	TG LVM BIST; 22;	Pass;	;		Option Passed;	09/08/2022 11:55:19
1;	4:HVD-1;	TG LVM BIST; 23;	Pass;	;		Option Passed;	09/08/2022 11:55:19
1;	4:HVD-1;	TG LVM BIST; 24;	Pass;	;		Option Passed;	09/08/2022 11:55:19
1;	4:HVD-1;	PG LVM BIST; ;	Pass;	;		Option Passed;	09/08/2022 11:55:19
1;	4:HVD-1;	CkCtrlRamBistTest;	;	Pass;	;	Option Passed;	09/08/2022
11:55:19							
1;	4:HVD-1;	PG Download; ;	Pass;	;		Option Passed;	09/08/2022 11:55:20
1;	4:HVD-1;	PG RAM Test; ;	Pass;	;		Option Passed;	09/08/2022 11:55:20
1;	4:HVD-1;	TG RAM Test; 1;	Pass;	;		Option Passed;	09/08/2022 11:55:22
1;	4:HVD-1;	TG RAM Test; 5;	Pass;	;		Option Passed;	09/08/2022 11:55:22
1;	4:HVD-1;	TG RAM Test; 9;	Pass;	;		Option Passed;	09/08/2022 11:55:22
1;	4:HVD-1;	TG RAM Test; 13;	Pass;	;		Option Passed;	09/08/2022 11:55:22
1;	4:HVD-1;	TG RAM Test; 17;	Pass;	;		Option Passed;	09/08/2022 11:55:22

1;	4:HVD-1;	TG RAM Test;	21;	Pass;	;	Option Passed;	09/08/2022 11:55:22
1;	4:HVD-1;	PG Opcodes;	;	Pass;	;	Option Passed;	09/08/2022 11:55:24
1;	4:HVD-1;	EventStamper;	1;	Pass;	;	Option Passed;	09/08/2022 11:55:24
1;	4:HVD-1;	EventStamper;	2;	Pass;	;	Option Passed;	09/08/2022 11:55:24
1;	4:HVD-1;	EventStamper;	3;	Pass;	;	Option Passed;	09/08/2022 11:55:24
1;	4:HVD-1;	EventStamper;	4;	Pass;	;	Option Passed;	09/08/2022 11:55:24
1;	4:HVD-1;	EventStamper;	5;	Pass;	;	Option Passed;	09/08/2022 11:55:24
1;	4:HVD-1;	EventStamper;	6;	Pass;	;	Option Passed;	09/08/2022 11:55:24
1;	4:HVD-1;	EventStamper;	7;	Pass;	;	Option Passed;	09/08/2022 11:55:24
1;	4:HVD-1;	EventStamper;	8;	Pass;	;	Option Passed;	09/08/2022 11:55:24
1;	4:HVD-1;	EventStamper;	9;	Pass;	;	Option Passed;	09/08/2022 11:55:24
1;	4:HVD-1;	EventStamper;	10;	Pass;	;	Option Passed;	09/08/2022 11:55:24
1;	4:HVD-1;	EventStamper;	11;	Pass;	;	Option Passed;	09/08/2022 11:55:24
1;	4:HVD-1;	EventStamper;	12;	Pass;	;	Option Passed;	09/08/2022 11:55:24
1;	4:HVD-1;	EventStamper;	13;	Pass;	;	Option Passed;	09/08/2022 11:55:24
1;	4:HVD-1;	EventStamper;	14;	Pass;	;	Option Passed;	09/08/2022 11:55:24
1;	4:HVD-1;	EventStamper;	15;	Pass;	;	Option Passed;	09/08/2022 11:55:24
1;	4:HVD-1;	EventStamper;	16;	Pass;	;	Option Passed;	09/08/2022 11:55:24
1;	4:HVD-1;	EventStamper;	17;	Pass;	;	Option Passed;	09/08/2022 11:55:24
1;	4:HVD-1;	EventStamper;	18;	Pass;	;	Option Passed;	09/08/2022 11:55:24
1;	4:HVD-1;	EventStamper;	19;	Pass;	;	Option Passed;	09/08/2022 11:55:24
1;	4:HVD-1;	EventStamper;	20;	Pass;	;	Option Passed;	09/08/2022 11:55:24
1;	4:HVD-1;	EventStamper;	21;	Pass;	;	Option Passed;	09/08/2022 11:55:24
1;	4:HVD-1;	EventStamper;	22;	Pass;	;	Option Passed;	09/08/2022 11:55:24
1;	4:HVD-1;	EventStamper;	23;	Pass;	;	Option Passed;	09/08/2022 11:55:24
1;	4:HVD-1;	EventStamper;	24;	Pass;	;	Option Passed;	09/08/2022 11:55:24
1;	4:HVD-1;	CkCcuLowSense;	;	Pass;	;	Option Passed;	09/08/2022 11:55:25
1;	4:HVD-1;	CkCcuHiSense;	;	Pass;	;	Option Passed;	09/08/2022 11:55:26
1;	4:HVD-1;	CkCcuLoadResistorTest;	;	Pass;	;	Option Passed;	09/08/2022 11:55:28
1;	4:HVD-1;	PmuForceVoltage;	1;	Pass;	;	Option Passed;	09/08/2022 11:55:39
1;	4:HVD-1;	PmuForceVoltage;	2;	Pass;	;	Option Passed;	09/08/2022 11:55:39
1;	4:HVD-1;	PmuForceVoltage;	3;	Pass;	;	Option Passed;	09/08/2022 11:55:39
1;	4:HVD-1;	PmuForceVoltage;	4;	Pass;	;	Option Passed;	09/08/2022 11:55:39
1;	4:HVD-1;	PmuForceVoltage;	5;	Pass;	;	Option Passed;	09/08/2022 11:55:39
1;	4:HVD-1;	PmuForceVoltage;	6;	Pass;	;	Option Passed;	09/08/2022 11:55:39
1;	4:HVD-1;	PmuForceVoltage;	7;	Pass;	;	Option Passed;	09/08/2022 11:55:39
1;	4:HVD-1;	PmuForceVoltage;	8;	Pass;	;	Option Passed;	09/08/2022 11:55:39

1; 11:55:39	4:HVD-1;	PmuForceVoltage;	9;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:39	4:HVD-1;	PmuForceVoltage;	10;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:39	4:HVD-1;	PmuForceVoltage;	11;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:39	4:HVD-1;	PmuForceVoltage;	12;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:39	4:HVD-1;	PmuForceVoltage;	13;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:39	4:HVD-1;	PmuForceVoltage;	14;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:39	4:HVD-1;	PmuForceVoltage;	15;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:39	4:HVD-1;	PmuForceVoltage;	16;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:39	4:HVD-1;	PmuForceVoltage;	17;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:39	4:HVD-1;	PmuForceVoltage;	18;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:39	4:HVD-1;	PmuForceVoltage;	19;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:39	4:HVD-1;	PmuForceVoltage;	20;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:39	4:HVD-1;	PmuForceVoltage;	21;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:39	4:HVD-1;	PmuForceVoltage;	22;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:39	4:HVD-1;	PmuForceVoltage;	23;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:39	4:HVD-1;	PmuForceVoltage;	24;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:47	4:HVD-1;	PmuForceCurrent;	1;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:47	4:HVD-1;	PmuForceCurrent;	2;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:47	4:HVD-1;	PmuForceCurrent;	3;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:47	4:HVD-1;	PmuForceCurrent;	4;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:47	4:HVD-1;	PmuForceCurrent;	5;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:47	4:HVD-1;	PmuForceCurrent;	6;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:47	4:HVD-1;	PmuForceCurrent;	7;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:47	4:HVD-1;	PmuForceCurrent;	8;	Pass; ;	Option Passed; 09/08/2022

1; 11:55:47	4:HVD-1;	PmuForceCurrent;	9;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:47	4:HVD-1;	PmuForceCurrent;	10;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:47	4:HVD-1;	PmuForceCurrent;	11;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:47	4:HVD-1;	PmuForceCurrent;	12;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:47	4:HVD-1;	PmuForceCurrent;	13;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:47	4:HVD-1;	PmuForceCurrent;	14;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:47	4:HVD-1;	PmuForceCurrent;	15;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:47	4:HVD-1;	PmuForceCurrent;	16;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:47	4:HVD-1;	PmuForceCurrent;	17;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:47	4:HVD-1;	PmuForceCurrent;	18;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:47	4:HVD-1;	PmuForceCurrent;	19;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:47	4:HVD-1;	PmuForceCurrent;	20;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:47	4:HVD-1;	PmuForceCurrent;	21;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:47	4:HVD-1;	PmuForceCurrent;	22;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:47	4:HVD-1;	PmuForceCurrent;	23;	Pass; ;	Option Passed; 09/08/2022
1; 11:55:47	4:HVD-1;	PmuForceCurrent;	24;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:00	4:HVD-1;	PmuForceIDualMode;	1;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:00	4:HVD-1;	PmuForceIDualMode;	3;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:00	4:HVD-1;	PmuForceIDualMode;	5;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:00	4:HVD-1;	PmuForceIDualMode;	7;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:00	4:HVD-1;	PmuForceIDualMode;	9;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:00	4:HVD-1;	PmuForceIDualMode;	11;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:00	4:HVD-1;	PmuForceIDualMode;	13;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:00	4:HVD-1;	PmuForceIDualMode;	15;	Pass; ;	Option Passed; 09/08/2022



1;	4:HVD-1;	PmuForcelDualMode;	17;	Pass;	;	Option Passed;	09/08/2022
11:56:00							
1;	4:HVD-1;	PmuForcelDualMode;	19;	Pass;	;	Option Passed;	09/08/2022
11:56:00							
1;	4:HVD-1;	PmuForcelDualMode;	21;	Pass;	;	Option Passed;	09/08/2022
11:56:00							
1;	4:HVD-1;	PmuForcelDualMode;	23;	Pass;	;	Option Passed;	09/08/2022
11:56:00							
1;	4:HVD-1;	PmuForcelQuadMode;	1;	Pass;	;	Option Passed;	09/08/2022
11:56:11							
1;	4:HVD-1;	PmuForcelQuadMode;	5;	Pass;	;	Option Passed;	09/08/2022
11:56:11							
1;	4:HVD-1;	PmuForcelQuadMode;	9;	Pass;	;	Option Passed;	09/08/2022
11:56:11							
1;	4:HVD-1;	PmuForcelQuadMode;	13;	Pass;	;	Option Passed;	09/08/2022
11:56:11							
1;	4:HVD-1;	PmuForcelQuadMode;	17;	Pass;	;	Option Passed;	09/08/2022
11:56:11							
1;	4:HVD-1;	PmuForcelQuadMode;	21;	Pass;	;	Option Passed;	09/08/2022
11:56:11							
1;	4:HVD-1;	PmuCurrentClamp;	1;	Pass;	;	Option Passed;	09/08/2022
11:56:35							
1;	4:HVD-1;	PmuCurrentClamp;	2;	Pass;	;	Option Passed;	09/08/2022
11:56:35							
1;	4:HVD-1;	PmuCurrentClamp;	3;	Pass;	;	Option Passed;	09/08/2022
11:56:35							
1;	4:HVD-1;	PmuCurrentClamp;	4;	Pass;	;	Option Passed;	09/08/2022
11:56:35							
1;	4:HVD-1;	PmuCurrentClamp;	5;	Pass;	;	Option Passed;	09/08/2022
11:56:35							
1;	4:HVD-1;	PmuCurrentClamp;	6;	Pass;	;	Option Passed;	09/08/2022
11:56:35							
1;	4:HVD-1;	PmuCurrentClamp;	7;	Pass;	;	Option Passed;	09/08/2022
11:56:35							
1;	4:HVD-1;	PmuCurrentClamp;	8;	Pass;	;	Option Passed;	09/08/2022
11:56:35							
1;	4:HVD-1;	PmuCurrentClamp;	9;	Pass;	;	Option Passed;	09/08/2022
11:56:35							
1;	4:HVD-1;	PmuCurrentClamp;	10;	Pass;	;	Option Passed;	09/08/2022
11:56:35							
1;	4:HVD-1;	PmuCurrentClamp;	11;	Pass;	;	Option Passed;	09/08/2022
11:56:35							
1;	4:HVD-1;	PmuCurrentClamp;	12;	Pass;	;	Option Passed;	09/08/2022
11:56:35							
1;	4:HVD-1;	PmuCurrentClamp;	13;	Pass;	;	Option Passed;	09/08/2022
11:56:35							
1;	4:HVD-1;	PmuCurrentClamp;	14;	Pass;	;	Option Passed;	09/08/2022
11:56:35							

1; 11:56:35	4:HVD-1;	PmuCurrentClamp;	15;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:35	4:HVD-1;	PmuCurrentClamp;	16;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:35	4:HVD-1;	PmuCurrentClamp;	17;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:35	4:HVD-1;	PmuCurrentClamp;	18;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:35	4:HVD-1;	PmuCurrentClamp;	19;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:35	4:HVD-1;	PmuCurrentClamp;	20;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:35	4:HVD-1;	PmuCurrentClamp;	21;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:35	4:HVD-1;	PmuCurrentClamp;	22;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:35	4:HVD-1;	PmuCurrentClamp;	23;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:35	4:HVD-1;	PmuCurrentClamp;	24;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:52	4:HVD-1;	PmuVoltageClamp;	1;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:52	4:HVD-1;	PmuVoltageClamp;	2;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:52	4:HVD-1;	PmuVoltageClamp;	3;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:52	4:HVD-1;	PmuVoltageClamp;	4;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:52	4:HVD-1;	PmuVoltageClamp;	5;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:52	4:HVD-1;	PmuVoltageClamp;	6;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:52	4:HVD-1;	PmuVoltageClamp;	7;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:52	4:HVD-1;	PmuVoltageClamp;	8;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:52	4:HVD-1;	PmuVoltageClamp;	9;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:52	4:HVD-1;	PmuVoltageClamp;	10;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:52	4:HVD-1;	PmuVoltageClamp;	11;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:52	4:HVD-1;	PmuVoltageClamp;	12;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:52	4:HVD-1;	PmuVoltageClamp;	13;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:52	4:HVD-1;	PmuVoltageClamp;	14;	Pass; ;	Option Passed; 09/08/2022

1; 11:56:52	4:HVD-1;	PmuVoltageClamp;	15;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:52	4:HVD-1;	PmuVoltageClamp;	16;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:52	4:HVD-1;	PmuVoltageClamp;	17;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:52	4:HVD-1;	PmuVoltageClamp;	18;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:52	4:HVD-1;	PmuVoltageClamp;	19;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:52	4:HVD-1;	PmuVoltageClamp;	20;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:52	4:HVD-1;	PmuVoltageClamp;	21;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:52	4:HVD-1;	PmuVoltageClamp;	22;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:52	4:HVD-1;	PmuVoltageClamp;	23;	Pass; ;	Option Passed; 09/08/2022
1; 11:56:52	4:HVD-1;	PmuVoltageClamp;	24;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:10	4:HVD-1;	PmuSettlingTime;	1;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:10	4:HVD-1;	PmuSettlingTime;	2;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:10	4:HVD-1;	PmuSettlingTime;	3;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:10	4:HVD-1;	PmuSettlingTime;	4;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:10	4:HVD-1;	PmuSettlingTime;	5;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:10	4:HVD-1;	PmuSettlingTime;	6;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:10	4:HVD-1;	PmuSettlingTime;	7;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:10	4:HVD-1;	PmuSettlingTime;	8;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:10	4:HVD-1;	PmuSettlingTime;	9;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:10	4:HVD-1;	PmuSettlingTime;	10;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:10	4:HVD-1;	PmuSettlingTime;	11;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:10	4:HVD-1;	PmuSettlingTime;	12;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:10	4:HVD-1;	PmuSettlingTime;	13;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:10	4:HVD-1;	PmuSettlingTime;	14;	Pass; ;	Option Passed; 09/08/2022

1; 11:57:10	4:HVD-1;	PmuSettlingTime;	15;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:10	4:HVD-1;	PmuSettlingTime;	16;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:10	4:HVD-1;	PmuSettlingTime;	17;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:10	4:HVD-1;	PmuSettlingTime;	18;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:10	4:HVD-1;	PmuSettlingTime;	19;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:10	4:HVD-1;	PmuSettlingTime;	20;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:10	4:HVD-1;	PmuSettlingTime;	21;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:11	4:HVD-1;	PmuSettlingTime;	22;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:11	4:HVD-1;	PmuSettlingTime;	23;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:11	4:HVD-1;	PmuSettlingTime;	24;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:18	4:HVD-1;	CkPmuSinadTest;	1;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:18	4:HVD-1;	CkPmuSinadTest;	2;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:18	4:HVD-1;	CkPmuSinadTest;	3;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:18	4:HVD-1;	CkPmuSinadTest;	4;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:18	4:HVD-1;	CkPmuSinadTest;	5;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:18	4:HVD-1;	CkPmuSinadTest;	6;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:18	4:HVD-1;	CkPmuSinadTest;	7;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:18	4:HVD-1;	CkPmuSinadTest;	8;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:18	4:HVD-1;	CkPmuSinadTest;	9;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:18	4:HVD-1;	CkPmuSinadTest;	10;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:18	4:HVD-1;	CkPmuSinadTest;	11;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:18	4:HVD-1;	CkPmuSinadTest;	12;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:18	4:HVD-1;	CkPmuSinadTest;	13;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:18	4:HVD-1;	CkPmuSinadTest;	14;	Pass; ;	Option Passed; 09/08/2022

1;	4:HVD-1;	CkPmuSinadTest;	15;	Pass;	;	Option Passed;	09/08/2022
11:57:18							
1;	4:HVD-1;	CkPmuSinadTest;	16;	Pass;	;	Option Passed;	09/08/2022
11:57:18							
1;	4:HVD-1;	CkPmuSinadTest;	17;	Pass;	;	Option Passed;	09/08/2022
11:57:18							
1;	4:HVD-1;	CkPmuSinadTest;	18;	Pass;	;	Option Passed;	09/08/2022
11:57:18							
1;	4:HVD-1;	CkPmuSinadTest;	19;	Pass;	;	Option Passed;	09/08/2022
11:57:18							
1;	4:HVD-1;	CkPmuSinadTest;	20;	Pass;	;	Option Passed;	09/08/2022
11:57:18							
1;	4:HVD-1;	CkPmuSinadTest;	21;	Pass;	;	Option Passed;	09/08/2022
11:57:18							
1;	4:HVD-1;	CkPmuSinadTest;	22;	Pass;	;	Option Passed;	09/08/2022
11:57:18							
1;	4:HVD-1;	CkPmuSinadTest;	23;	Pass;	;	Option Passed;	09/08/2022
11:57:18							
1;	4:HVD-1;	CkPmuSinadTest;	24;	Pass;	;	Option Passed;	09/08/2022
11:57:18							
1;	4:HVD-1;	CkPmuPatternBurstTest;		1;	Pass;	;	Option Passed;
							09/08/2022 11:57:30
1;	4:HVD-1;	CkPmuPatternBurstTest;		2;	Pass;	;	Option Passed;
							09/08/2022 11:57:30
1;	4:HVD-1;	CkPmuPatternBurstTest;		3;	Pass;	;	Option Passed;
							09/08/2022 11:57:30
1;	4:HVD-1;	CkPmuPatternBurstTest;		4;	Pass;	;	Option Passed;
							09/08/2022 11:57:30
1;	4:HVD-1;	CkPmuPatternBurstTest;		5;	Pass;	;	Option Passed;
							09/08/2022 11:57:30
1;	4:HVD-1;	CkPmuPatternBurstTest;		6;	Pass;	;	Option Passed;
							09/08/2022 11:57:30
1;	4:HVD-1;	CkPmuPatternBurstTest;		7;	Pass;	;	Option Passed;
							09/08/2022 11:57:30
1;	4:HVD-1;	CkPmuPatternBurstTest;		8;	Pass;	;	Option Passed;
							09/08/2022 11:57:30
1;	4:HVD-1;	CkPmuPatternBurstTest;		9;	Pass;	;	Option Passed;
							09/08/2022 11:57:30
1;	4:HVD-1;	CkPmuPatternBurstTest;		10;	Pass;	;	Option Passed;
							09/08/2022 11:57:30
1;	4:HVD-1;	CkPmuPatternBurstTest;		11;	Pass;	;	Option Passed;
							09/08/2022 11:57:30
1;	4:HVD-1;	CkPmuPatternBurstTest;		12;	Pass;	;	Option Passed;
							09/08/2022 11:57:30
1;	4:HVD-1;	CkPmuPatternBurstTest;		13;	Pass;	;	Option Passed;
							09/08/2022 11:57:30
1;	4:HVD-1;	CkPmuPatternBurstTest;		14;	Pass;	;	Option Passed;
							09/08/2022 11:57:30

1;	4:HVD-1;	CkPmuPatternBurstTest;	15;	Pass;	;	Option Passed;
	09/08/2022	11:57:30				
1;	4:HVD-1;	CkPmuPatternBurstTest;	16;	Pass;	;	Option Passed;
	09/08/2022	11:57:30				
1;	4:HVD-1;	CkPmuPatternBurstTest;	17;	Pass;	;	Option Passed;
	09/08/2022	11:57:30				
1;	4:HVD-1;	CkPmuPatternBurstTest;	18;	Pass;	;	Option Passed;
	09/08/2022	11:57:30				
1;	4:HVD-1;	CkPmuPatternBurstTest;	19;	Pass;	;	Option Passed;
	09/08/2022	11:57:30				
1;	4:HVD-1;	CkPmuPatternBurstTest;	20;	Pass;	;	Option Passed;
	09/08/2022	11:57:30				
1;	4:HVD-1;	CkPmuPatternBurstTest;	21;	Pass;	;	Option Passed;
	09/08/2022	11:57:30				
1;	4:HVD-1;	CkPmuPatternBurstTest;	22;	Pass;	;	Option Passed;
	09/08/2022	11:57:30				
1;	4:HVD-1;	CkPmuPatternBurstTest;	23;	Pass;	;	Option Passed;
	09/08/2022	11:57:30				
1;	4:HVD-1;	CkPmuPatternBurstTest;	24;	Pass;	;	Option Passed;
	09/08/2022	11:57:30				
1;	4:HVD-1;	CkPmuHiSenseTest;	1;	Pass;	;	Option Passed; 09/08/2022
	11:57:34					
1;	4:HVD-1;	CkPmuHiSenseTest;	2;	Pass;	;	Option Passed; 09/08/2022
	11:57:34					
1;	4:HVD-1;	CkPmuHiSenseTest;	3;	Pass;	;	Option Passed; 09/08/2022
	11:57:34					
1;	4:HVD-1;	CkPmuHiSenseTest;	4;	Pass;	;	Option Passed; 09/08/2022
	11:57:34					
1;	4:HVD-1;	CkPmuHiSenseTest;	5;	Pass;	;	Option Passed; 09/08/2022
	11:57:34					
1;	4:HVD-1;	CkPmuHiSenseTest;	6;	Pass;	;	Option Passed; 09/08/2022
	11:57:34					
1;	4:HVD-1;	CkPmuHiSenseTest;	7;	Pass;	;	Option Passed; 09/08/2022
	11:57:34					
1;	4:HVD-1;	CkPmuHiSenseTest;	8;	Pass;	;	Option Passed; 09/08/2022
	11:57:34					
1;	4:HVD-1;	CkPmuHiSenseTest;	9;	Pass;	;	Option Passed; 09/08/2022
	11:57:34					
1;	4:HVD-1;	CkPmuHiSenseTest;	10;	Pass;	;	Option Passed; 09/08/2022
	11:57:34					
1;	4:HVD-1;	CkPmuHiSenseTest;	11;	Pass;	;	Option Passed; 09/08/2022
	11:57:34					
1;	4:HVD-1;	CkPmuHiSenseTest;	12;	Pass;	;	Option Passed; 09/08/2022
	11:57:34					
1;	4:HVD-1;	CkPmuHiSenseTest;	13;	Pass;	;	Option Passed; 09/08/2022
	11:57:34					
1;	4:HVD-1;	CkPmuHiSenseTest;	14;	Pass;	;	Option Passed; 09/08/2022
	11:57:34					

1; 11:57:34	4:HVD-1;	CkPmuHiSenseTest;	15;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:34	4:HVD-1;	CkPmuHiSenseTest;	16;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:34	4:HVD-1;	CkPmuHiSenseTest;	17;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:34	4:HVD-1;	CkPmuHiSenseTest;	18;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:34	4:HVD-1;	CkPmuHiSenseTest;	19;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:34	4:HVD-1;	CkPmuHiSenseTest;	20;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:34	4:HVD-1;	CkPmuHiSenseTest;	21;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:34	4:HVD-1;	CkPmuHiSenseTest;	22;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:34	4:HVD-1;	CkPmuHiSenseTest;	23;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:34	4:HVD-1;	CkPmuHiSenseTest;	24;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:38	4:HVD-1;	CkPmuLoSenseTest;	1;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:38	4:HVD-1;	CkPmuLoSenseTest;	2;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:38	4:HVD-1;	CkPmuLoSenseTest;	3;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:38	4:HVD-1;	CkPmuLoSenseTest;	4;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:38	4:HVD-1;	CkPmuLoSenseTest;	5;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:38	4:HVD-1;	CkPmuLoSenseTest;	6;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:38	4:HVD-1;	CkPmuLoSenseTest;	7;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:38	4:HVD-1;	CkPmuLoSenseTest;	8;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:38	4:HVD-1;	CkPmuLoSenseTest;	9;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:38	4:HVD-1;	CkPmuLoSenseTest;	10;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:38	4:HVD-1;	CkPmuLoSenseTest;	11;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:38	4:HVD-1;	CkPmuLoSenseTest;	12;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:38	4:HVD-1;	CkPmuLoSenseTest;	13;	Pass; ;	Option Passed; 09/08/2022
1; 11:57:38	4:HVD-1;	CkPmuLoSenseTest;	14;	Pass; ;	Option Passed; 09/08/2022

1;	4:HVD-1;	CkPmuLoSenseTest;	15;	Pass;	;	Option Passed;	09/08/2022
11:57:38							
1;	4:HVD-1;	CkPmuLoSenseTest;	16;	Pass;	;	Option Passed;	09/08/2022
11:57:38							
1;	4:HVD-1;	CkPmuLoSenseTest;	17;	Pass;	;	Option Passed;	09/08/2022
11:57:38							
1;	4:HVD-1;	CkPmuLoSenseTest;	18;	Pass;	;	Option Passed;	09/08/2022
11:57:38							
1;	4:HVD-1;	CkPmuLoSenseTest;	19;	Pass;	;	Option Passed;	09/08/2022
11:57:38							
1;	4:HVD-1;	CkPmuLoSenseTest;	20;	Pass;	;	Option Passed;	09/08/2022
11:57:38							
1;	4:HVD-1;	CkPmuLoSenseTest;	21;	Pass;	;	Option Passed;	09/08/2022
11:57:38							
1;	4:HVD-1;	CkPmuLoSenseTest;	22;	Pass;	;	Option Passed;	09/08/2022
11:57:38							
1;	4:HVD-1;	CkPmuLoSenseTest;	23;	Pass;	;	Option Passed;	09/08/2022
11:57:38							
1;	4:HVD-1;	CkPmuLoSenseTest;	24;	Pass;	;	Option Passed;	09/08/2022
11:57:38							
1;	6:VHFAC;	CalStateCheck;	;	Pass;	;	;	09/08/2022 11:57:41
1;	6:VHFAC;	VHF_PGMemoryTest;	;	Pass;	;	;	09/08/2022 11:58:39
1;	6:VHFAC;	BistMemory;	SRC0;	Pass;	;	;	09/08/2022 11:58:40
1;	6:VHFAC;	BistMemory;	SRC1;	Pass;	;	;	09/08/2022 11:58:40
1;	6:VHFAC;	BistMemory;	CAP0;	Pass;	;	;	09/08/2022 11:58:40
1;	6:VHFAC;	BistMemory;	CAP1;	Pass;	;	;	09/08/2022 11:58:40
1;	6:VHFAC;	BistMemory;	TMU0;	Pass;	;	;	09/08/2022 11:58:40
1;	6:VHFAC;	SrcWavemem;	SRC0;	Pass;	;	;	09/08/2022 11:58:41
1;	6:VHFAC;	SrcWavemem;	SRC1;	Pass;	;	;	09/08/2022 11:58:41
1;	6:VHFAC;	SrcBulkmem;	SRC0;	Pass;	;	;	09/08/2022 11:58:43
1;	6:VHFAC;	SrcBulkmem;	SRC1;	Pass;	;	;	09/08/2022 11:58:43
1;	6:VHFAC;	SrcEventmem;	SRC0;	Pass;	;	;	09/08/2022 11:58:43
1;	6:VHFAC;	SrcEventmem;	SRC1;	Pass;	;	;	09/08/2022 11:58:43
1;	6:VHFAC;	SrcCondmem;	SRC0;	Pass;	;	;	09/08/2022 11:58:44
1;	6:VHFAC;	SrcCondmem;	SRC1;	Pass;	;	;	09/08/2022 11:58:44
1;	6:VHFAC;	SrcUcodemem;	SRC0;	Pass;	;	;	09/08/2022 11:58:45
1;	6:VHFAC;	SrcUcodemem;	SRC1;	Pass;	;	;	09/08/2022 11:58:45
1;	6:VHFAC;	SrcWfmIdLut;	SRC0;	Pass;	;	;	09/08/2022 11:58:47
1;	6:VHFAC;	SrcWfmIdLut;	SRC1;	Pass;	;	;	09/08/2022 11:58:47
1;	6:VHFAC;	SrcPsetLut;	SRC0;	Pass;	;	;	09/08/2022 11:58:48
1;	6:VHFAC;	SrcPsetLut;	SRC1;	Pass;	;	;	09/08/2022 11:58:48
1;	6:VHFAC;	SrcExTrgLut;	SRC0;	Pass;	;	;	09/08/2022 11:58:50
1;	6:VHFAC;	SrcExTrgLut;	SRC1;	Pass;	;	;	09/08/2022 11:58:50
1;	6:VHFAC;	SrcSVM;	SRC0;	Pass;	;	;	09/08/2022 11:58:52
1;	6:VHFAC;	SrcSVM;	SRC1;	Pass;	;	;	09/08/2022 11:58:52
1;	6:VHFAC;	SrcVrefDAC;	SRC0;	Pass;	;	;	09/08/2022 11:58:58
1;	6:VHFAC;	SrcVrefDAC;	SRC1;	Pass;	;	;	09/08/2022 11:58:58
1;	6:VHFAC;	SrcDCBaseLine;	SRC0;	Pass;	;	;	09/08/2022 11:59:24



1;	6:VHFAC;	SrcDCBaseLine;	SRC1;	Pass;	;	;	09/08/2022 11:59:24
1;	6:VHFAC;	SrcALRO;	SRC0;	Pass;	;	;	09/08/2022 11:59:26
1;	6:VHFAC;	SrcALRO;	SRC1;	Pass;	;	;	09/08/2022 11:59:26
1;	6:VHFAC;	SrcStepAtten;	SRC0;	Pass;	;	;	09/08/2022 11:59:35
1;	6:VHFAC;	SrcStepAtten;	SRC1;	Pass;	;	;	09/08/2022 11:59:35
1;	6:VHFAC;	SrcBands;	SRC0;	Pass;	;	;	09/08/2022 11:59:52
1;	6:VHFAC;	SrcBands;	SRC1;	Pass;	;	;	09/08/2022 11:59:52
1;	6:VHFAC;	SrcCalBus;	SRC0;	Pass;	;	;	09/08/2022 11:59:54
1;	6:VHFAC;	SrcCalBus;	SRC1;	Pass;	;	;	09/08/2022 11:59:54
1;	6:VHFAC;	CapCapMemA;	CAP0;	Pass;	;	;	09/08/2022 11:59:55
1;	6:VHFAC;	CapCapMemA;	CAP1;	Pass;	;	;	09/08/2022 11:59:55
1;	6:VHFAC;	CapCapMemB;	CAP0;	Pass;	;	;	09/08/2022 11:59:56
1;	6:VHFAC;	CapCapMemB;	CAP1;	Pass;	;	;	09/08/2022 11:59:56
1;	6:VHFAC;	CapBulkmem;	CAP0;	Pass;	;	;	09/08/2022 11:59:57
1;	6:VHFAC;	CapBulkmem;	CAP1;	Pass;	;	;	09/08/2022 11:59:57
1;	6:VHFAC;	CapSVM;	CAP0;	Pass;	;	;	09/08/2022 11:59:59
1;	6:VHFAC;	CapSVM;	CAP1;	Pass;	;	;	09/08/2022 11:59:59
1;	6:VHFAC;	CapPsetLut;	CAP0;	Pass;	;	;	09/08/2022 12:00:00
1;	6:VHFAC;	CapPsetLut;	CAP1;	Pass;	;	;	09/08/2022 12:00:00
1;	6:VHFAC;	CapPeakCap;	CAP0;	Pass;	;	;	09/08/2022 12:00:02
1;	6:VHFAC;	CapPeakCap;	CAP1;	Pass;	;	;	09/08/2022 12:00:02
1;	6:VHFAC;	CapCapLut;	CAP0;	Pass;	;	;	09/08/2022 12:00:04
1;	6:VHFAC;	CapCapLut;	CAP1;	Pass;	;	;	09/08/2022 12:00:04
1;	6:VHFAC;	CapExTrgLut;	CAP0;	Pass;	;	;	09/08/2022 12:00:06
1;	6:VHFAC;	CapExTrgLut;	CAP1;	Pass;	;	;	09/08/2022 12:00:06
1;	6:VHFAC;	CapResistance;	CAP0;	Pass;	;	;	09/08/2022 12:00:06
1;	6:VHFAC;	CapResistance;	CAP1;	Pass;	;	;	09/08/2022 12:00:06
1;	6:VHFAC;	CapGND;	CAP0;	Pass;	;	;	09/08/2022 12:00:08
1;	6:VHFAC;	CapGND;	CAP1;	Pass;	;	;	09/08/2022 12:00:08
1;	6:VHFAC;	CapDCRange;	CAP0;	Pass;	;	;	09/08/2022 12:00:18
1;	6:VHFAC;	CapDCRange;	CAP1;	Pass;	;	;	09/08/2022 12:00:18
1;	6:VHFAC;	CapDCpeakIn;	CAP0;	Pass;	;	;	09/08/2022 12:00:19
1;	6:VHFAC;	CapDCpeakIn;	CAP1;	Pass;	;	;	09/08/2022 12:00:19
1;	6:VHFAC;	CapFilter;	CAP0;	Pass;	;	;	09/08/2022 12:00:53
1;	6:VHFAC;	CapFilter;	CAP1;	Pass;	;	;	09/08/2022 12:00:53
1;	6:VHFAC;	CapDCBase;	CAP0;	Pass;	;	;	09/08/2022 12:00:58
1;	6:VHFAC;	CapDCBase;	CAP1;	Pass;	;	;	09/08/2022 12:00:58
1;	6:VHFAC;	CapAlarms;	CAP0;	Pass;	;	;	09/08/2022 12:00:58
1;	6:VHFAC;	CapAlarms;	CAP1;	Pass;	;	;	09/08/2022 12:00:58
1;	6:VHFAC;	SrcCapAmpAcur;		SrcAndCap0_SE;	Pass;	;	;
							09/08/2022 12:01:15
1;	6:VHFAC;	SrcCapAmpAcur;		SrcAndCap0_DIFF;	Pass;	;	;
							09/08/2022 12:01:15
1;	6:VHFAC;	SrcCapAmpAcur;		SrcAndCap1_SE;	Pass;	;	;
							09/08/2022 12:01:15
1;	6:VHFAC;	SrcCapAmpAcur;		SrcAndCap1_DIFF;	Pass;	;	;
							09/08/2022 12:01:15

1;	6:VHFAC;	SrcCapSpecPur; SrcAndCap0_SE;	Pass;	;	;	09/08/2022
12:02:19						
1;	6:VHFAC;	SrcCapSpecPur; SrcAndCap0_DIFF;	Pass;	;	;	09/08/2022
12:02:19						
1;	6:VHFAC;	SrcCapSpecPur; SrcAndCap1_SE;	Pass;	;	;	09/08/2022
12:02:19						
1;	6:VHFAC;	SrcCapSpecPur; SrcAndCap1_DIFF;	Pass;	;	;	09/08/2022
12:02:19						
1;	6:VHFAC;	SrcTmuStepResp; Src0AndTmu;	Pass;	;	;	09/08/2022
12:02:24						
1;	6:VHFAC;	SrcTmuStepResp; Src1AndTmu;	Pass;	;	;	09/08/2022
12:02:24						
1;	6:VHFAC;	SrcCapDCBaseLine; SrcAndCap0;	Pass;	;	;	09/08/2022
12:02:33						
1;	6:VHFAC;	SrcCapDCBaseLine; SrcAndCap1;	Pass;	;	;	09/08/2022
12:02:33						
1;	6:VHFAC;	SrcCapFsMaxMin; SRC0 & CAP0;	Pass;	;	;	09/08/2022
12:02:59						
1;	6:VHFAC;	SrcCapFsMaxMin; SRC1 & CAP1;	Pass;	;	;	09/08/2022
12:02:59						
1;	6:VHFAC;	SrcCapPeakDetect; SRC0 & CAP0;	Pass;	;	;	09/08/2022
12:03:15						
1;	6:VHFAC;	SrcCapPeakDetect; SRC1 & CAP1;	Pass;	;	;	09/08/2022
12:03:15						
1;	6:VHFAC;	SrcCapVideoWave; SRC0 & CAP0;	Pass;	;	;	09/08/2022
12:03:16						
1;	6:VHFAC;	SrcCapVideoWave; SRC1 & CAP1;	Pass;	;	;	09/08/2022
12:03:16						
1;	6:VHFAC;	SrcCapHarmWithDC; SRC0 & CAP0;	Pass;	;	;	09/08/2022
12:03:23						
1;	6:VHFAC;	SrcCapHarmWithDC; SRC1 & CAP1;	Pass;	;	;	09/08/2022
12:03:23						
1;	6:VHFAC;	SrcCapPsetTest;SrcAndCap0_DIFF;	Pass;	;	;	09/08/2022
12:03:28						
1;	6:VHFAC;	SrcCapPsetTest;SrcAndCap1_DIFF;	Pass;	;	;	09/08/2022
12:03:28						
1;	6:VHFAC;	TmuBulkmem; TMU0;	Pass;	;	;	09/08/2022 12:03:28
1;	6:VHFAC;	TmuCapmem; TMU0;	Pass;	;	;	09/08/2022 12:03:29
1;	6:VHFAC;	TmuSVM; TMU0;	Pass;	;	;	09/08/2022 12:03:31
1;	6:VHFAC;	TmuPsetLut; TMU0;	Pass;	;	;	09/08/2022 12:03:31
1;	6:VHFAC;	TmuCalLut; TMU0;	Pass;	;	;	09/08/2022 12:03:32
1;	6:VHFAC;	TmuFrequencyMode; SE;	Pass;	;	;	09/08/2022 12:03:33
1;	6:VHFAC;	TmuFrequencyMode; DIF;	Pass;	;	;	09/08/2022 12:03:33
1;	6:VHFAC;	TmuFrequencyMode; EXT1;	Pass;	;	;	09/08/2022 12:03:33
1;	6:VHFAC;	TmuFrequencyMode; EXT2;	Pass;	;	;	09/08/2022 12:03:33
1;	6:VHFAC;	TmuPerodMode; SE;	Pass;	;	;	09/08/2022 12:03:35
1;	6:VHFAC;	TmuPerodMode; DIF;	Pass;	;	;	09/08/2022 12:03:35
1;	6:VHFAC;	TmuPerodMode; EXT1;	Pass;	;	;	09/08/2022 12:03:35

1;	6:VHFAC;	TmuPerodMode;	EXT2;	Pass;	;	;	09/08/2022 12:03:35
1;	6:VHFAC;	TmuPulseWidthMd;	SE;	Pass;	;	;	09/08/2022 12:03:36
1;	6:VHFAC;	TmuPulseWidthMd;	DIF;	Pass;	;	;	09/08/2022 12:03:36
1;	6:VHFAC;	TmuPulseWidthMd;	EXT1;	Pass;	;	;	09/08/2022 12:03:36
1;	6:VHFAC;	TmuPulseWidthMd;	EXT2;	Pass;	;	;	09/08/2022 12:03:36
1;	6:VHFAC;	TmuDutyCyclMode;	SE;	Pass;	;	;	09/08/2022 12:03:37
1;	6:VHFAC;	TmuDutyCyclMode;	DIF;	Pass;	;	;	09/08/2022 12:03:37
1;	6:VHFAC;	TmuDutyCyclMode;	EXT1;	Pass;	;	;	09/08/2022 12:03:37
1;	6:VHFAC;	TmuDutyCyclMode;	EXT2;	Pass;	;	;	09/08/2022 12:03:37
1;	6:VHFAC;	TmuRiseTimeMode;	SE;	Pass;	;	;	09/08/2022 12:03:38
1;	6:VHFAC;	TmuRiseTimeMode;	DIF;	Pass;	;	;	09/08/2022 12:03:38
1;	6:VHFAC;	TMUPropDelay;	SE;	Pass;	;	;	09/08/2022 12:03:39
1;	6:VHFAC;	TMUPropDelay;	DIF;	Pass;	;	;	09/08/2022 12:03:39
1;	6:VHFAC;	TMUPropDelay;	EXT;	Pass;	;	;	09/08/2022 12:03:39
1;	6:VHFAC;	TmuEdgeJitter;	CH1;	Pass;	;	;	09/08/2022 12:03:43
1;	6:VHFAC;	TmuEdgeJitter;	CH2;	Pass;	;	;	09/08/2022 12:03:43
1;	6:VHFAC;	TmuPeriodJitter;	CH1;	Pass;	;	;	09/08/2022 12:03:44
1;	6:VHFAC;	TmuPeriodJitter;	CH2;	Pass;	;	;	09/08/2022 12:03:44
1;	6:VHFAC;	TmuArmCtrl;	TCIO;	Pass;	;	;	09/08/2022 12:03:49
1;	6:VHFAC;	TmuArmCtrl;	CH1;	Pass;	;	;	09/08/2022 12:03:49
1;	6:VHFAC;	TmuArmCtrl;	CH2;	Pass;	;	;	09/08/2022 12:03:49
1;	6:VHFAC;	TmuArmCtrl;	Ext1;	Pass;	;	;	09/08/2022 12:03:49
1;	6:VHFAC;	TmuArmCtrl;	Ext2;	Pass;	;	;	09/08/2022 12:03:49
1;	6:VHFAC;	TmuArmCtrl;	Pattern;	Pass;	;	;	09/08/2022 12:03:49
1;	6:VHFAC;	TmuPrecount;	CH1;	Pass;	;	;	09/08/2022 12:03:51
1;	6:VHFAC;	TmuPrecount;	CH2;	Pass;	;	;	09/08/2022 12:03:51
1;	6:VHFAC;	TmuPrecount;	Ext1;	Pass;	;	;	09/08/2022 12:03:51
1;	6:VHFAC;	TmuPrecount;	Ext2;	Pass;	;	;	09/08/2022 12:03:51
1;	6:VHFAC;	TmuBeforeEach;	CH1;	Pass;	;	;	09/08/2022 12:03:53
1;	6:VHFAC;	TmuBeforeEach;	CH2;	Pass;	;	;	09/08/2022 12:03:53
1;	6:VHFAC;	TmuBeforeEach;	Ext1;	Pass;	;	;	09/08/2022 12:03:53
1;	6:VHFAC;	TmuBeforeEach;	Ext2;	Pass;	;	;	09/08/2022 12:03:53
1;	6:VHFAC;	TmuEnableTrigger;	CH1;	Pass;	;	;	09/08/2022 12:03:54
1;	6:VHFAC;	TmuEnableTrigger;	CH2;	Pass;	;	;	09/08/2022 12:03:54
1;	6:VHFAC;	TmuEnableTrigger;	Ext1;	Pass;	;	;	09/08/2022 12:03:54
1;	6:VHFAC;	TmuEnableTrigger;	Ext2;	Pass;	;	;	09/08/2022 12:03:54
1;	6:VHFAC;	TmuEnableGate;	CH1;	Pass;	;	;	09/08/2022 12:03:55
1;	6:VHFAC;	TmuEnableGate;	CH2;	Pass;	;	;	09/08/2022 12:03:55
1;	6:VHFAC;	TmuEnableGate;	Ext1;	Pass;	;	;	09/08/2022 12:03:55
1;	6:VHFAC;	TmuEnableGate;	Ext2;	Pass;	;	;	09/08/2022 12:03:55
1;	6:VHFAC;	TMUInterLeave;	CH1;	Pass;	;	;	09/08/2022 12:03:56
1;	6:VHFAC;	TMUInterLeave;	Ext;	Pass;	;	;	09/08/2022 12:03:56
1;	6:VHFAC;	TMUSlopeSelect;	CH1;	Pass;	;	;	09/08/2022 12:03:57
1;	6:VHFAC;	TMUSlopeSelect;	Ext;	Pass;	;	;	09/08/2022 12:03:57
1;	6:VHFAC;	TmuTimeRange;	CH1;	Pass;	;	;	09/08/2022 12:04:19
1;	6:VHFAC;	TmuTB_Error;	TMU0;	Pass;	;	;	09/08/2022 12:04:20
1;	6:VHFAC;	TmuTB_Ofset;	TMU0;	Pass;	;	;	09/08/2022 12:04:20
1;	6:VHFAC;	TmuBetween;	CH1;	Pass;	;	;	09/08/2022 12:04:22

1;	6:VHFAC;	TmuStampJitter;	CH1;	Pass;	;	;	09/08/2022 12:04:22
1;	6:VHFAC;	TmuStampAccuracy;	TMU0;	Pass;	;	;	09/08/2022 12:04:23
1;	6:VHFAC;	TmuStampOffsetEr;	SE;	Pass;	;	;	09/08/2022 12:04:24
1;	6:VHFAC;	TmuStampOffsetEr;	DIF;	Pass;	;	;	09/08/2022 12:04:24
1;	6:VHFAC;	TmuStampMemDepth;	CH1;	Pass;	;	;	09/08/2022 12:04:25
1;	6:VHFAC;	TmuMaxInputVolt;	CH1;	Pass;	;	;	09/08/2022 12:04:26
1;	6:VHFAC;	TmuMaxInputVolt;	CH2;	Pass;	;	;	09/08/2022 12:04:26
1;	6:VHFAC;	TmuInplImpedanc;	CH1;	Pass;	;	;	09/08/2022 12:04:31
1;	6:VHFAC;	TmuInplImpedanc;	CH2;	Pass;	;	;	09/08/2022 12:04:31
1;	6:VHFAC;	TmuThreshold;	CH1;	Pass;	;	;	09/08/2022 12:04:34
1;	6:VHFAC;	TmuThreshold;	CH2;	Pass;	;	;	09/08/2022 12:04:34
1;	6:VHFAC;	TmuHysteresis;	CH1;	Pass;	;	;	09/08/2022 12:04:36
1;	6:VHFAC;	TmuHysteresis;	CH2;	Pass;	;	;	09/08/2022 12:04:36
1;	6:VHFAC;	TmuMaxiFrequency;	CH1;	Pass;	;	;	09/08/2022 12:04:37
1;	6:VHFAC;	TmuMaxiFrequency;	CH2;	Pass;	;	;	09/08/2022 12:04:37
1;	6:VHFAC;	TmuLowImpeTermV;	CH1;	Pass;	;	;	09/08/2022 12:04:40
1;	6:VHFAC;	TmuLowImpeTermV;	CH2;	Pass;	;	;	09/08/2022 12:04:40
1;	6:VHFAC;	TmuRelayACBUS;	CH1;	Pass;	;	;	09/08/2022 12:04:42
1;	6:VHFAC;	TmuRelayACBUS;	CH2;	Pass;	;	;	09/08/2022 12:04:42
1;	6:VHFAC;	TmuRelayCALBUS;	CH1;	Pass;	;	;	09/08/2022 12:04:44
1;	6:VHFAC;	TmuRelayCALBUS;	CH2;	Pass;	;	;	09/08/2022 12:04:44
1;	6:VHFAC;	TmuRelayPPMU;	CH1;	Pass;	;	;	09/08/2022 12:04:53
1;	6:VHFAC;	TmuRelayPPMU;	CH2;	Pass;	;	;	09/08/2022 12:04:53
1;	6:VHFAC;	TmuRelayDDS;	SRC0;	Pass;	;	;	09/08/2022 12:04:54
1;	6:VHFAC;	TmuRelayDDS;	SRC1;	Pass;	;	;	09/08/2022 12:04:54
1;	6:VHFAC;	TmuRelayDDS;	CAP0;	Pass;	;	;	09/08/2022 12:04:54
1;	6:VHFAC;	TmuRelayDDS;	CAP1;	Pass;	;	;	09/08/2022 12:04:54
1;	6:VHFAC;	TmuAlarmCheck;	TMU0;	Pass;	;	;	09/08/2022 12:04:55
1;	6:VHFAC;	TmuErrorCheck;	TMU0;	Pass;	;	;	09/08/2022 12:04:56
1;	6:VHFAC;	TmuPset;	TMU0;	Pass;	;	;	09/08/2022 12:04:57
1;	6:VHFAC;	TmuIIM;	CH1;	Pass;	;	;	09/08/2022 12:04:57
1;	6:VHFAC;	MiscAcCalSource;	SRC0;	Pass;	;	;	09/08/2022 12:04:58
1;	6:VHFAC;	MiscDcCalSource;	DC_A;	Pass;	;	;	09/08/2022 12:05:13
1;	6:VHFAC;	MiscDcCalSource;	DC_B;	Pass;	;	;	09/08/2022 12:05:13
1;	6:VHFAC;	SerialBusTiming;	SBUSclk;	Pass;	;	;	09/08/2022 12:05:18
1;	6:VHFAC;	SerialBusTiming;	SBUSio0;	Pass;	;	;	09/08/2022 12:05:18
1;	6:VHFAC;	SerialBusTiming;	SBUSio1;	Pass;	;	;	09/08/2022 12:05:18
1;	6:VHFAC;	SerialBusDriver;	SBUSclk;	Pass;	;	;	09/08/2022 12:05:27
1;	6:VHFAC;	SerialBusDriver;	SBUSio0;	Pass;	;	;	09/08/2022 12:05:27
1;	6:VHFAC;	SerialBusDriver;	SBUSio1;	Pass;	;	;	09/08/2022 12:05:27
1;	6:VHFAC;	SerialBusComprt;	SBUSclk;	Pass;	;	;	09/08/2022 12:05:37
1;	6:VHFAC;	SerialBusComprt;	SBUSio0;	Pass;	;	;	09/08/2022 12:05:37

1;	6:VHFAC;	SerialBusComprt;	SBUSio1;	Pass;	;	;	09/08/2022
12:05:37							
1;	6:VHFAC;	VHF_PPMUVer_V_I;	SRC0;	Pass;	;	;	09/08/2022 12:05:42
1;	6:VHFAC;	VHF_PPMUVer_V_I;	SRC1;	Pass;	;	;	09/08/2022 12:05:42
1;	6:VHFAC;	VHF_PPMUVer_V_I;	CAP0;	Pass;	;	;	09/08/2022 12:05:42
1;	6:VHFAC;	VHF_PPMUVer_V_I;	CAP1;	Pass;	;	;	09/08/2022 12:05:42
1;	6:VHFAC;	VHF_PPMUVer_V_I;	TMU;	Pass;	;	;	09/08/2022 12:05:42
1;	6:VHFAC;	VHF_PPMUVer_V_I;	SBUS;	Pass;	;	;	09/08/2022 12:05:42
1;	6:VHFAC;	VHF_PPMU FIMI;	SRC0;	Pass;	;	;	09/08/2022 12:05:59
1;	6:VHFAC;	VHF_PPMU FIMI;	SRC1;	Pass;	;	;	09/08/2022 12:05:59
1;	6:VHFAC;	VHF_PPMU FIMI;	CAP0;	Pass;	;	;	09/08/2022 12:05:59
1;	6:VHFAC;	VHF_PPMU FIMI;	CAP1;	Pass;	;	;	09/08/2022 12:05:59
1;	6:VHFAC;	VHF_PPMU FIMI;	TMU;	Pass;	;	;	09/08/2022 12:05:59
1;	6:VHFAC;	VHF_PPMU FIMI;	SBUS;	Pass;	;	;	09/08/2022 12:05:59
1;	6:VHFAC;	VHF_PPMUForcMeas;	SRC0;	Pass;	;	;	09/08/2022 12:06:09
1;	6:VHFAC;	VHF_PPMUForcMeas;	SRC1;	Pass;	;	;	09/08/2022 12:06:09
1;	6:VHFAC;	VHF_PPMUForcMeas;	CAP0;	Pass;	;	;	09/08/2022 12:06:09
1;	6:VHFAC;	VHF_PPMUForcMeas;	CAP1;	Pass;	;	;	09/08/2022 12:06:09
1;	6:VHFAC;	VHF_PPMUForcMeas;	TMU;	Pass;	;	;	09/08/2022 12:06:09
1;	6:VHFAC;	VHF_PPMUForcMeas;	SBUS;	Pass;	;	;	09/08/2022 12:06:09
1;	6:VHFAC;	VHF_PPMU_DCPV;	SRC0;	Pass;	;	;	09/08/2022 12:06:29
1;	6:VHFAC;	VHF_PPMU_DCPV;	SRC1;	Pass;	;	;	09/08/2022 12:06:29
1;	6:VHFAC;	VHF_PPMU_DCPV;	CAP0;	Pass;	;	;	09/08/2022 12:06:29
1;	6:VHFAC;	VHF_PPMU_DCPV;	CAP1;	Pass;	;	;	09/08/2022 12:06:29
1;	6:VHFAC;	VHF_PPMU_DCPV;	TMU;	Pass;	;	;	09/08/2022 12:06:29
1;	6:VHFAC;	VHF_PPMU_DCPV;	SBUS;	Pass;	;	;	09/08/2022 12:06:29
1;	6:VHFAC;	VHF_PPMU_VClamp;	SRC0;	Pass;	;	;	09/08/2022 12:06:30
1;	6:VHFAC;	VHF_PPMU_VClamp;	SRC1;	Pass;	;	;	09/08/2022 12:06:30
1;	6:VHFAC;	VHF_PPMU_VClamp;	CAP0;	Pass;	;	;	09/08/2022 12:06:30
1;	6:VHFAC;	VHF_PPMU_VClamp;	CAP1;	Pass;	;	;	09/08/2022 12:06:30
1;	6:VHFAC;	VHF_PPMU_VClamp;	TMU;	Pass;	;	;	09/08/2022 12:06:30
1;	6:VHFAC;	VHF_PPMU_VClamp;	SBUS;	Pass;	;	;	09/08/2022 12:06:30
1;	6:VHFAC;	VHF_PPMU_FSysVMV;	SRC0;	Pass;	;	;	09/08/2022 12:06:30
1;	6:VHFAC;	VHF_PPMU_FSysVMV;	SRC1;	Pass;	;	;	09/08/2022 12:06:30
1;	6:VHFAC;	VHF_PPMU_FSysVMV;	CAP0;	Pass;	;	;	09/08/2022 12:06:30
1;	6:VHFAC;	VHF_PPMU_FSysVMV;	CAP1;	Pass;	;	;	09/08/2022 12:06:30
1;	6:VHFAC;	VHF_PPMU_FSysVMV;	TMU;	Pass;	;	;	09/08/2022 12:06:30
1;	6:VHFAC;	VHF_PPMU_FSysVMV;	SBUS;	Pass;	;	;	09/08/2022 12:06:30
1;	6:VHFAC;	TempMonitor;	SRC0;	Pass;	;	;	09/08/2022 12:06:32
1;	6:VHFAC;	TempMonitor;	SRC1;	Pass;	;	;	09/08/2022 12:06:32
1;	6:VHFAC;	TempMonitor;	CAP0;	Pass;	;	;	09/08/2022 12:06:32
1;	6:VHFAC;	TempMonitor;	CAP1;	Pass;	;	;	09/08/2022 12:06:32
1;	6:VHFAC;	TempMonitor;	Board;	Pass;	;	;	09/08/2022 12:06:32
1;	6:VHFAC;	MWPath;	SRC0;	Pass;	;	;	09/08/2022 12:06:35
1;	6:VHFAC;	MWPath;	SRC1;	Pass;	;	;	09/08/2022 12:06:35
1;	6:VHFAC;	MWPath;	CAP0;	Pass;	;	;	09/08/2022 12:06:35
1;	6:VHFAC;	MWPath;	CAP1;	Pass;	;	;	09/08/2022 12:06:35

DATE: 09/08/2022                    TIME: 11:22:27  
 TESTER: AGRMFLE  
 DRIVE C TOTAL SPACE: 14,998 MB    DRIVE C UNUSED SPACE: 3,305 MB  
 IG-XL VERSION: 5.10.20\_flx (P16)   IG-XL BUILD: 02.09.07.17.21  
 TEST MODE: Full Check                LOOP: By System

\*\*\*\*\* SYSTEM CHECKS \*\*\*\*\*

Name	S/N	Part/All	Total	Pass	Fail	Invalid
ComputerIO		All	1	1	0	0
SMC		All	1	1	0	0

\*\*\*\*\* OPTION CHECKS \*\*\*\*\*

Slot	Option_Name	S/N	Part/All	Total	Pass	Fail	Invalid
1	BBAC-15	0305B7F4	All	1	0	1	0
2	DC-90	0C156CAE	All	1	1	0	0
3	DC-30	0600ECB3	All	1	1	0	0
4	HVD-1	0C33DE1A	All	1	1	0	0
5	DC-30	0C0178E8	All	1	1	0	0
6	VHFAC	03012B8E	All	1	1	0	0
7	SupportBoard-0	0C0FA9AF	All	1	1	0	0
8	HSD-200	0C006192	All	1	1	0	0
9	DC-30	03014654	All	1	1	0	0
10	DC-90	0C311FAE	All	1	1	0	0
11	DC-90	0C130F47	All	1	1	0	0
12	DC-30	0C351261	All	1	1	0	0
13	BBAC-15	03028AE8	All	1	0	1	0
20	SupportBoard-USM	002AF110	All	1	1	0	0

\*\*\*\*\* CONFIGURATION \*\*\*\*\*

Slot	Option_Name	Assembly	Part_Number	S/N	Rev_Date
1.0	BBAC-15	High-Level	805-004-50	0305B7F4	1041-E
	BBAC-15	Primary	949-977-03	0305B142	0819-E
	BBAC-15Rider	Primary	949-987-02	03058D76	0739-D
	PowerBoard	Primary	949-984-04	0305B05A	0949-E
2.0	DC-90	High-Level	805-229-50	0C156CAE	1225-C
	DC-90	Primary	939-229-06	0C156AC2	1150-C
	PowerBoard	Primary	939-228-00	0C17D17D	1225-D
		Sub	949-972-25	0C17DF4F	1206-E
2.1	DC-90PowerSupply	High-Level	805-230-30	002B4735	0439-B
	DC-90PowerSupply	Primary	939-230-03	002AF137	0446-B
		Sub	939-231-03	002AF186	0436-B
3.0	DC-30	High-Level	805-002-60	0600ECB3	1133-B

	DC-30	Primary	949-901-60	0600EB86	0808-B
	DC-30Rider	Primary	949-904-60	0600EBE5	0808-B
	PowerBoard	Primary	949-955-02	0600D208	0531-E
		Sub	949-972-00	0600FFEF	0313-C
4.0	HVD-1	High-Level	805-740-50	0C33DE1A	1735-C
	HVD-1	Primary	939-273-00	0C34AEBB	1042-C
4.1	HVDModuleBoard-0	High-Level	805-740-50	0C33DE1A	1735-C
	HVDModuleBoard-0	Primary	939-274-10	0C34E2A2	1735-C
4.2	HVDModuleBoard-1	High-Level	805-740-50	0C33DE1A	1735-C
	HVDModuleBoard-1	Primary	939-274-10	0C34E2AA	1735-C
4.3	HVDModuleBoard-2	High-Level	805-740-50	0C33DE1A	1735-C
	HVDModuleBoard-2	Primary	939-274-10	0C34E294	1735-C
5.0	DC-30	High-Level	805-002-60	0C0178E8	1206-E
	DC-30	Primary	949-901-60	0C0178A3	1205-E
	DC-30Rider	Primary	949-904-60	0C01B89F	1205-E
	PowerBoard	Primary	949-955-02	0C0177E3	0644-E
		Sub	949-972-25	0C01B6B6	1206-E
6.0	VHFAC	High-Level	805-245-50	03012B8E	0707-E
	VHFAC	Primary	939-245-10	03012826	0707-E
	VHFACCapture0	Primary	939-244-00	030127F8	0552-C
	VHFACCapture1	Primary	939-244-00	030127F8	0552-C
	VHFACSource4	Primary	939-247-10	0301BE08	0613-C
	VHFACSource5	Primary	939-247-10	0301BE08	0613-C
	VHFACTime	Primary	939-245-10	03012826	0707-E
7.0	SupportBoard	High-Level	805-003-50	0C0FA9AF	1223-B
	SupportBoard	Primary	949-909-00	0C0F999F	1223-B
	DCCalModule	Primary	949-938-12	0C0F97CA	0912-C
	PowerBoard	Primary	949-910-30	0C0F96AA	1121-C
		Sub	949-972-25	0C11351A	0544-E
7.1	DSP-1	High-Level	810-503-00	03031F22	0423-B
	DSP-1	Primary	949-939-00	03031F22	0423-B
8.0	HSD-200	High-Level	805-251-50	0C006192	1231-D
	HSD-200	Primary	939-251-30	0C00458C	1231-D
	PowerBoard	Primary	939-253-01	0C0048F7	1037-C
		Sub	949-972-25	0C0046B2	1206-E
9.0	DC-30	High-Level	805-002-60	03014654	1206-B
	DC-30	Primary	949-901-60	0301AD3A	0808-B
	PowerBoard	Primary	949-955-02	0301AD58	0531-E
		Sub	949-972-25	03014153	1206-E
	DC-30Rider	Primary	949-904-60	03018EE6	0808-B
10.0	DC-90	High-Level	805-229-50	0C311FAE	1233-C
	DC-90	Primary	939-229-06	0C311EDE	1150-C
	PowerBoard	Primary	939-228-00	0C310B72	1225-D
		Sub	609-082-00	0C3109B1	1116-A
10.1	DC-90PowerSupply	High-Level	805-230-06	0C1910C9	1115-B
	DC-90PowerSupply	Primary	939-230-06	0C191B48	1115-B
		Sub	939-231-06	0C191B92	1115-B
11.0	DC-90	High-Level	805-229-50	0C130F47	1225-C

DC-90 Primary 939-229-06 0C125A72 1127-C  
PowerBoard Primary 939-228-00 0C12CAF8 1225-D  
Sub 949-972-25 0C13FE9F 1206-E  
11.1 DC-90PowerSupply High-Level 805-230-30 002B4816 0439-B  
DC-90PowerSupply Primary 939-230-03 002AF13E 0446-B  
Sub 939-231-03 002ADA44 0436-B  
12.0 DC-30 High-Level 805-002-60 0C351261 1505-E  
DC-30 Primary 949-901-60 0C35D71B 1418-E  
PowerBoard Primary 949-955-02 0C35D7DE 1505-E  
Sub 609-082-02 0C35E2CC 1421-A  
DC-30Rider Primary 949-904-60 0C360002 1418-E  
13.0 BBAC-15 High-Level 805-004-50 03028AE8 1041-E  
BBAC-15 Primary 949-977-03 0302695A 0819-E  
BBAC-15Rider Primary 949-987-02 030289A7 0739-D  
PowerBoard Primary 949-984-04 03026966 0633-E  
Sub 949-972-25 03024856 0544-E  
20.0 SupportBoard-USM High-Level 939-270-00 002AF110 0549-B  
SupportBoard-USM Primary 939-270-00 002AF110 0549-B

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09/08/2022 12:40:41 Messages with All Verbosity

Info MWPPath Job is running...  
Info SupportBoard-0 Reading sheet ContinuityChannelMap  
Info SupportBoard-0 Reading sheet ContinuityPinMap  
Info SupportBoard-0 Getting Continuity Pin info from Instruments for maps  
Info SupportBoard-0 Building Pin Info array for SupportBoard-0  
Info SupportBoard-0 Building Pin Info array for SupportBoard-USM  
Info SupportBoard-0 Building Pin Info array for BBAC-15  
Info SupportBoard-0 Building Pin Info array for DC-30  
Info SupportBoard-0 Building Pin Info array for DC-90  
Info SupportBoard-0 Building Pin Info array for HSD-200  
Info SupportBoard-0 Building Pin Info array for HVD-1  
Info SupportBoard-0 Building Pin Info array for VHFAC  
Info SupportBoard-0 Determining DIB configuration and validating.  
Info SupportBoard-0 Setting up Pin and Channel Maps...  
Info SupportBoard-0 Setting up SupportBoard-0 pin and channel map...  
Info SupportBoard-0 Setting up SupportBoard-USM pin and channel map...  
Info SupportBoard-0 Setting up BBAC-15 pin and channel map...  
Info SupportBoard-0 Setting up DC-30 pin and channel map...  
Info SupportBoard-0 Setting up DC-90 pin and channel map...  
Info SupportBoard-0 Setting up HSD-200 pin and channel map...  
Info SupportBoard-0 Setting up HVD-1 pin and channel map...  
Info SupportBoard-0 Setting up VHFAC pin and channel map...  
Info SupportBoard-0 Resolving pins and channels...  
Info SupportBoard-0 Validating...  
Info SupportBoard-0 Test Instances  
Info SupportBoard-0 JobList  
Info SupportBoard-0 Global Specs



Info	SupportBoard-0	Flow Table
Info	SupportBoard-0	ContinuityChannelMap
Info	SupportBoard-0	ContinuityPinMap
Info	SupportBoard-0	Validating DSP Procedures...
Info	SupportBoard-0	ContinuityChannelMap
Info	SupportBoard-0	Test Instances
Info	SupportBoard-0	Flow Table
Info	SupportBoard-0	Preparing tests...
Info	SupportBoard-0	Test: T
Info	SupportBoard-0	Driver checks: T
Info	SupportBoard-0	Loading patterns, timing, levels
Info	SupportBoard-0	Validating Resources...
Info	SupportBoard-0	Validating...
Info	SupportBoard-0	Driver Validation
Info	SupportBoard-0	Loading patterns, timing, levels
Info	SupportBoard-0	Validating job 'Continuity'
Info	SupportBoard-0	Validation completed successfully.
Info	SupportBoard-0	Running Exec Interpose Function On Program Validated
Info	SupportBoard-0	Setting up pinouts on IG-XL worksheet.
Info	SupportBoard-0	Beginning SupportBoard-0 continuity test
Pass	SupportBoard-0	sdb 32 (SupportBoard-0, 20 A 29), sdb 32 (SupportBoard-0, 20 A
	29),.000,.989,.989,Pass	
Pass	SupportBoard-0	sdb 34 (SupportBoard-0, 20 C 29), sdb 34 (SupportBoard-0, 20 C
	29),.000,.989,.989,Pass	
Pass	SupportBoard-0	sdb 4 (SupportBoard-0, 8 A 30), sdb 4 (SupportBoard-0, 8 A
	30),.000,1.458,1.458,Pass	
Pass	SupportBoard-0	sdb 5 (SupportBoard-0, 8 B 30), sdb 5 (SupportBoard-0, 8 B
	30),.000,1.560,1.560,Pass	
Pass	SupportBoard-0	sdb 6 (SupportBoard-0, 8 C 30), sdb 6 (SupportBoard-0, 8 C
	30),.000,1.458,1.458,Pass	
Pass	SupportBoard-0	sdb 7 (SupportBoard-0, 8 D 30), sdb 7 (SupportBoard-0, 8 D
	30),.000,1.560,1.560,Pass	
Pass	SupportBoard-0	sdb 8 (SupportBoard-0, 8 A 31), sdb 8 (SupportBoard-0, 8 A
	31),.000,1.458,1.458,Pass	
Pass	SupportBoard-0	sdb 9 (SupportBoard-0, 8 B 31), sdb 9 (SupportBoard-0, 8 B
	31),.000,1.443,1.443,Pass	
Pass	SupportBoard-0	sdb 10 (SupportBoard-0, 8 C 31), sdb 10 (SupportBoard-0, 8 C
	31),.000,1.458,1.458,Pass	
Pass	SupportBoard-0	sdb 11 (SupportBoard-0, 8 D 31), sdb 11 (SupportBoard-0, 8 D
	31),.000,1.458,1.458,Pass	
Pass	SupportBoard-0	sdb 12 (SupportBoard-0, 8 A 32), sdb 12 (SupportBoard-0, 8 A
	32),.000,1.560,1.560,Pass	
Pass	SupportBoard-0	sdb 13 (SupportBoard-0, 8 B 32), sdb 13 (SupportBoard-0, 8 B
	32),.000,1.560,1.560,Pass	
Pass	SupportBoard-0	sdb 14 (SupportBoard-0, 8 C 32), sdb 14 (SupportBoard-0, 8 C
	32),.000,1.560,1.560,Pass	
Pass	SupportBoard-0	sdb 15 (SupportBoard-0, 8 D 32), sdb 15 (SupportBoard-0, 8 D
	32),.000,1.560,1.560,Pass	

Pass SupportBoard-0  
34),.000,1.458,1.458,Pass  
Pass SupportBoard-0  
34),.000,1.458,1.458,Pass  
Pass SupportBoard-0  
34),.000,1.458,1.458,Pass  
Pass SupportBoard-0  
34),.000,1.458,1.458,Pass  
Pass SupportBoard-0  
35),.000,1.560,1.560,Pass  
Pass SupportBoard-0  
35),.000,1.560,1.560,Pass  
Pass SupportBoard-0  
35),.000,1.560,1.560,Pass  
Pass SupportBoard-0  
35),.000,1.560,1.560,Pass  
Pass SupportBoard-0  
36),.000,1.487,1.487,Pass  
Pass SupportBoard-0  
36),.000,1.458,1.458,Pass  
Pass SupportBoard-0  
36),.000,1.487,1.487,Pass  
Pass SupportBoard-0  
36),.000,1.458,1.458,Pass  
Pass SupportBoard-0  
37),.000,1.560,1.560,Pass  
Pass SupportBoard-0  
37),.000,1.487,1.487,Pass  
Pass SupportBoard-0  
37),.000,1.560,1.560,Pass  
Pass SupportBoard-0  
37),.000,1.487,1.487,Pass  
Pass SupportBoard-0  
29),.000,1.487,1.487,Pass  
Pass SupportBoard-0  
29),.000,1.487,1.487,Pass  
Pass SupportBoard-0  
30),.000,1.458,1.458,Pass  
Pass SupportBoard-0  
30),.000,1.575,1.575,Pass  
Pass SupportBoard-0  
30),.000,1.458,1.458,Pass  
Pass SupportBoard-0  
30),.000,1.575,1.575,Pass  
Pass SupportBoard-0  
31),.000,1.458,1.458,Pass  
Pass SupportBoard-0  
31),.000,1.458,1.458,Pass

sdb 16 (SupportBoard-0, 8 A 34), sdb 16 (SupportBoard-0, 8 A  
sdb 17 (SupportBoard-0, 8 B 34), sdb 17 (SupportBoard-0, 8 B  
sdb 18 (SupportBoard-0, 8 C 34), sdb 18 (SupportBoard-0, 8 C  
sdb 19 (SupportBoard-0, 8 D 34), sdb 19 (SupportBoard-0, 8 D  
sdb 20 (SupportBoard-0, 8 A 35), sdb 20 (SupportBoard-0, 8 A  
sdb 21 (SupportBoard-0, 8 B 35), sdb 21 (SupportBoard-0, 8 B  
sdb 22 (SupportBoard-0, 8 C 35), sdb 22 (SupportBoard-0, 8 C  
sdb 23 (SupportBoard-0, 8 D 35), sdb 23 (SupportBoard-0, 8 D  
sdb 24 (SupportBoard-0, 8 A 36), sdb 24 (SupportBoard-0, 8 A  
sdb 25 (SupportBoard-0, 8 B 36), sdb 25 (SupportBoard-0, 8 B  
sdb 26 (SupportBoard-0, 8 C 36), sdb 26 (SupportBoard-0, 8 C  
sdb 27 (SupportBoard-0, 8 D 36), sdb 27 (SupportBoard-0, 8 D  
sdb 28 (SupportBoard-0, 8 A 37), sdb 28 (SupportBoard-0, 8 A  
sdb 29 (SupportBoard-0, 8 B 37), sdb 29 (SupportBoard-0, 8 B  
sdb 30 (SupportBoard-0, 8 C 37), sdb 30 (SupportBoard-0, 8 C  
sdb 31 (SupportBoard-0, 8 D 37), sdb 31 (SupportBoard-0, 8 D  
sdb 33 (SupportBoard-0, 20 B 29), sdb 33 (SupportBoard-0, 20 B  
sdb 35 (SupportBoard-0, 20 D 29), sdb 35 (SupportBoard-0, 20 D  
sdb 36 (SupportBoard-0, 20 A 30), sdb 36 (SupportBoard-0, 20 A  
sdb 37 (SupportBoard-0, 20 B 30), sdb 37 (SupportBoard-0, 20 B  
sdb 38 (SupportBoard-0, 20 C 30), sdb 38 (SupportBoard-0, 20 C  
sdb 39 (SupportBoard-0, 20 D 30), sdb 39 (SupportBoard-0, 20 D  
sdb 40 (SupportBoard-0, 20 A 31), sdb 40 (SupportBoard-0, 20 A  
sdb 41 (SupportBoard-0, 20 B 31), sdb 41 (SupportBoard-0, 20 B

Pass SupportBoard-0 sdb 42 (SupportBoard-0, 20 C 31), sdb 42 (SupportBoard-0, 20 C  
31),.000,1.472,1.472,Pass  
Pass SupportBoard-0 sdb 43 (SupportBoard-0, 20 D 31), sdb 43 (SupportBoard-0, 20 D  
31),.000,1.458,1.458,Pass  
Pass SupportBoard-0 sdb 44 (SupportBoard-0, 20 A 32), sdb 44 (SupportBoard-0, 20 A  
32),.000,1.560,1.560,Pass  
Pass SupportBoard-0 sdb 45 (SupportBoard-0, 20 B 32), sdb 45 (SupportBoard-0, 20 B  
32),.000,1.487,1.487,Pass  
Pass SupportBoard-0 sdb 46 (SupportBoard-0, 20 C 32), sdb 46 (SupportBoard-0, 20 C  
32),.000,1.560,1.560,Pass  
Pass SupportBoard-0 sdb 47 (SupportBoard-0, 20 D 32), sdb 47 (SupportBoard-0, 20 D  
32),.000,1.487,1.487,Pass  
Pass SupportBoard-0 sdb 48 (SupportBoard-0, 20 A 34), sdb 48 (SupportBoard-0, 20 A  
34),.000,1.458,1.458,Pass  
Pass SupportBoard-0 sdb 49 (SupportBoard-0, 20 B 34), sdb 49 (SupportBoard-0, 20 B  
34),.000,1.458,1.458,Pass  
Pass SupportBoard-0 sdb 50 (SupportBoard-0, 20 C 34), sdb 50 (SupportBoard-0, 20 C  
34),.000,1.458,1.458,Pass  
Pass SupportBoard-0 sdb 51 (SupportBoard-0, 20 D 34), sdb 51 (SupportBoard-0, 20 D  
34),.000,1.458,1.458,Pass  
Pass SupportBoard-0 sdb 52 (SupportBoard-0, 20 A 35), sdb 52 (SupportBoard-0, 20 A  
35),.000,1.560,1.560,Pass  
Pass SupportBoard-0 sdb 53 (SupportBoard-0, 20 B 35), sdb 53 (SupportBoard-0, 20 B  
35),.000,1.487,1.487,Pass  
Pass SupportBoard-0 sdb 54 (SupportBoard-0, 20 C 35), sdb 54 (SupportBoard-0, 20 C  
35),.000,1.560,1.560,Pass  
Pass SupportBoard-0 sdb 55 (SupportBoard-0, 20 D 35), sdb 55 (SupportBoard-0, 20 D  
35),.000,1.487,1.487,Pass  
Pass SupportBoard-0 sdb 56 (SupportBoard-0, 20 A 36), sdb 56 (SupportBoard-0, 20 A  
36),.000,1.560,1.560,Pass  
Pass SupportBoard-0 sdb 57 (SupportBoard-0, 20 B 36), sdb 57 (SupportBoard-0, 20 B  
36),.000,1.458,1.458,Pass  
Pass SupportBoard-0 sdb 58 (SupportBoard-0, 20 C 36), sdb 58 (SupportBoard-0, 20 C  
36),.000,1.560,1.560,Pass  
Pass SupportBoard-0 sdb 59 (SupportBoard-0, 20 D 36), sdb 59 (SupportBoard-0, 20 D  
36),.000,1.458,1.458,Pass  
Pass SupportBoard-0 sdb 60 (SupportBoard-0, 20 A 37), sdb 60 (SupportBoard-0, 20 A  
37),.000,1.853,1.853,Pass  
Pass SupportBoard-0 sdb 61 (SupportBoard-0, 20 B 37), sdb 61 (SupportBoard-0, 20 B  
37),.000,1.560,1.560,Pass  
Pass SupportBoard-0 sdb 62 (SupportBoard-0, 20 C 37), sdb 62 (SupportBoard-0, 20 C  
37),.000,1.853,1.853,Pass  
Pass SupportBoard-0 sdb 63 (SupportBoard-0, 20 D 37), sdb 63 (SupportBoard-0, 20 D  
37),.000,1.560,1.560,Pass  
Pass SupportBoard-0 DC\_CAL\_HS (SupportBoard-0, 20 A 13), -5V Return a (SupportBoard-0, 8  
A 24),-.005,5.012,5.017,Pass  
Pass SupportBoard-0 DC\_CAL\_HS (SupportBoard-0, 20 A 13), -5V Return b (SupportBoard-0, 8  
B 24),.114,5.012,4.899,Pass

Pass	SupportBoard-0	DC_CAL_HS (SupportBoard-0, 20 A 13), -5V Return c (SupportBoard-0, 8 C 24),.113,5.012,4.899,Pass
Pass	SupportBoard-0	DC_CAL_HS (SupportBoard-0, 20 A 13), -5V Return d (SupportBoard-0, 8 D 24),.114,5.013,4.898,Pass
Pass	SupportBoard-0	DC_CAL_HS (SupportBoard-0, 20 A 13), +15V a (SupportBoard-0, 8 A 25),-.007,2.751,2.758,Pass
Pass	SupportBoard-0	DC_CAL_HS (SupportBoard-0, 20 A 13), +15V b (SupportBoard-0, 8 B 25),-.006,2.752,2.758,Pass
Pass	SupportBoard-0	DC_CAL_HS (SupportBoard-0, 20 A 13), +15V c (SupportBoard-0, 8 C 25),-.007,2.752,2.759,Pass
Pass	SupportBoard-0	DC_CAL_HS (SupportBoard-0, 20 A 13), +15V d (SupportBoard-0, 8 D 25),-.006,2.751,2.757,Pass
Pass	SupportBoard-0	DC_CAL_HS (SupportBoard-0, 20 A 13), -5V Return e (SupportBoard-0, 20 A 24),-.006,5.012,5.018,Pass
Pass	SupportBoard-0	DC_CAL_HS (SupportBoard-0, 20 A 13), -5V Return f (SupportBoard-0, 20 B 24),.113,5.013,4.900,Pass
Pass	SupportBoard-0	DC_CAL_HS (SupportBoard-0, 20 A 13), -5V Return g (SupportBoard-0, 20 C 24),.100,5.012,4.912,Pass
Pass	SupportBoard-0	DC_CAL_HS (SupportBoard-0, 20 A 13), -5V Return h (SupportBoard-0, 20 D 24),.111,5.012,4.901,Pass
Pass	SupportBoard-0	DC_CAL_HS (SupportBoard-0, 20 A 13), +15V e (SupportBoard-0, 20 A 25),-.006,2.750,2.756,Pass
Pass	SupportBoard-0	DC_CAL_HS (SupportBoard-0, 20 A 13), +15V f (SupportBoard-0, 20 B 25),-.006,2.753,2.759,Pass
Pass	SupportBoard-0	DC_CAL_HS (SupportBoard-0, 20 A 13), +15V g (SupportBoard-0, 20 C 25),-.006,2.755,2.761,Pass
Pass	SupportBoard-0	DC_CAL_HS (SupportBoard-0, 20 A 13), +15V h (SupportBoard-0, 20 D 25),-.005,2.749,2.754,Pass
Info	SupportBoard-0	Test finished
Info	SupportBoard-USM	Beginning SupportBoard-USM continuity test
Pass	SupportBoard-USM	sdb 96 (SupportBoard-0, 33 A 29), sdb 96 (SupportBoard-USM, 33 A 29),.000,.842,.842,Pass
Pass	SupportBoard-USM	sdb 98 (SupportBoard-0, 33 C 29), sdb 98 (SupportBoard-USM, 33 C 29),.000,.857,.857,Pass
Pass	SupportBoard-USM	sdb 64 (SupportBoard-0, 45 A 29), sdb 64 (SupportBoard-USM, 45 A 29),.000,1.575,1.575,Pass
Pass	SupportBoard-USM	sdb 65 (SupportBoard-0, 45 B 29), sdb 65 (SupportBoard-USM, 45 B 29),.000,1.501,1.501,Pass
Pass	SupportBoard-USM	sdb 66 (SupportBoard-0, 45 C 29), sdb 66 (SupportBoard-USM, 45 C 29),.000,1.575,1.575,Pass
Pass	SupportBoard-USM	sdb 67 (SupportBoard-0, 45 D 29), sdb 67 (SupportBoard-USM, 45 D 29),.000,1.501,1.501,Pass
Pass	SupportBoard-USM	sdb 68 (SupportBoard-0, 45 A 30), sdb 68 (SupportBoard-USM, 45 A 30),.000,1.472,1.472,Pass
Pass	SupportBoard-USM	sdb 69 (SupportBoard-0, 45 B 30), sdb 69 (SupportBoard-USM, 45 B 30),.000,1.575,1.575,Pass
Pass	SupportBoard-USM	sdb 70 (SupportBoard-0, 45 C 30), sdb 70 (SupportBoard-USM, 45 C 30),.000,1.472,1.472,Pass

Pass SupportBoard-USM sdb 71 (SupportBoard-0, 45 D 30), sdb 71 (SupportBoard-USM, 45 D 30),.000,1.575,1.575,Pass

Pass SupportBoard-USM sdb 72 (SupportBoard-0, 45 A 31), sdb 72 (SupportBoard-USM, 45 A 31),.000,1.472,1.472,Pass

Pass SupportBoard-USM sdb 73 (SupportBoard-0, 45 B 31), sdb 73 (SupportBoard-USM, 45 B 31),.000,1.472,1.472,Pass

Pass SupportBoard-USM sdb 74 (SupportBoard-0, 45 C 31), sdb 74 (SupportBoard-USM, 45 C 31),.000,1.472,1.472,Pass

Pass SupportBoard-USM sdb 75 (SupportBoard-0, 45 D 31), sdb 75 (SupportBoard-USM, 45 D 31),.000,1.472,1.472,Pass

Pass SupportBoard-USM sdb 76 (SupportBoard-0, 45 A 32), sdb 76 (SupportBoard-USM, 45 A 32),.000,1.575,1.575,Pass

Pass SupportBoard-USM sdb 77 (SupportBoard-0, 45 B 32), sdb 77 (SupportBoard-USM, 45 B 32),.000,1.501,1.501,Pass

Pass SupportBoard-USM sdb 78 (SupportBoard-0, 45 C 32), sdb 78 (SupportBoard-USM, 45 C 32),.000,1.575,1.575,Pass

Pass SupportBoard-USM sdb 79 (SupportBoard-0, 45 D 32), sdb 79 (SupportBoard-USM, 45 D 32),.000,1.501,1.501,Pass

Pass SupportBoard-USM sdb 80 (SupportBoard-0, 45 A 34), sdb 80 (SupportBoard-USM, 45 A 34),.000,1.472,1.472,Pass

Pass SupportBoard-USM sdb 81 (SupportBoard-0, 45 B 34), sdb 81 (SupportBoard-USM, 45 B 34),.000,1.472,1.472,Pass

Pass SupportBoard-USM sdb 82 (SupportBoard-0, 45 C 34), sdb 82 (SupportBoard-USM, 45 C 34),.000,1.472,1.472,Pass

Pass SupportBoard-USM sdb 83 (SupportBoard-0, 45 D 34), sdb 83 (SupportBoard-USM, 45 D 34),.000,1.472,1.472,Pass

Pass SupportBoard-USM sdb 84 (SupportBoard-0, 45 A 35), sdb 84 (SupportBoard-USM, 45 A 35),.000,1.575,1.575,Pass

Pass SupportBoard-USM sdb 85 (SupportBoard-0, 45 B 35), sdb 85 (SupportBoard-USM, 45 B 35),.000,1.501,1.501,Pass

Pass SupportBoard-USM sdb 86 (SupportBoard-0, 45 C 35), sdb 86 (SupportBoard-USM, 45 C 35),.000,1.575,1.575,Pass

Pass SupportBoard-USM sdb 87 (SupportBoard-0, 45 D 35), sdb 87 (SupportBoard-USM, 45 D 35),.000,1.501,1.501,Pass

Pass SupportBoard-USM sdb 88 (SupportBoard-0, 45 A 36), sdb 88 (SupportBoard-USM, 45 A 36),.000,1.575,1.575,Pass

Pass SupportBoard-USM sdb 89 (SupportBoard-0, 45 B 36), sdb 89 (SupportBoard-USM, 45 B 36),.000,1.472,1.472,Pass

Pass SupportBoard-USM sdb 90 (SupportBoard-0, 45 C 36), sdb 90 (SupportBoard-USM, 45 C 36),.000,1.575,1.575,Pass

Pass SupportBoard-USM sdb 91 (SupportBoard-0, 45 D 36), sdb 91 (SupportBoard-USM, 45 D 36),.000,1.472,1.472,Pass

Pass SupportBoard-USM sdb 92 (SupportBoard-0, 45 A 37), sdb 92 (SupportBoard-USM, 45 A 37),.000,1.838,1.838,Pass

Pass SupportBoard-USM sdb 93 (SupportBoard-0, 45 B 37), sdb 93 (SupportBoard-USM, 45 B 37),.000,1.575,1.575,Pass

Pass SupportBoard-USM sdb 94 (SupportBoard-0, 45 C 37), sdb 94 (SupportBoard-USM, 45 C 37),.000,1.838,1.838,Pass

Pass SupportBoard-USM sdb 95 (SupportBoard-0, 45 D 37), sdb 95 (SupportBoard-USM, 45 D 37),.000,1.575,1.575,Pass  
Pass SupportBoard-USM sdb 97 (SupportBoard-0, 33 B 30), sdb 97 (SupportBoard-USM, 33 B 30),.000,1.516,1.516,Pass  
Pass SupportBoard-USM sdb 99 (SupportBoard-0, 33 D 30), sdb 99 (SupportBoard-USM, 33 D 30),.000,1.516,1.516,Pass  
Pass SupportBoard-USM sdb 100 (SupportBoard-0, 33 A 30), sdb 100 (SupportBoard-USM, 33 A 30),.000,1.472,1.472,Pass  
Pass SupportBoard-USM sdb 101 (SupportBoard-0, 33 B 30), sdb 101 (SupportBoard-USM, 33 B 30),.000,1.575,1.575,Pass  
Pass SupportBoard-USM sdb 102 (SupportBoard-0, 33 C 30), sdb 102 (SupportBoard-USM, 33 C 30),.000,1.472,1.472,Pass  
Pass SupportBoard-USM sdb 103 (SupportBoard-0, 33 D 30), sdb 103 (SupportBoard-USM, 33 D 30),.000,1.589,1.589,Pass  
Pass SupportBoard-USM sdb 104 (SupportBoard-0, 33 A 31), sdb 104 (SupportBoard-USM, 33 A 31),.000,1.472,1.472,Pass  
Pass SupportBoard-USM sdb 105 (SupportBoard-0, 33 B 31), sdb 105 (SupportBoard-USM, 33 B 31),.000,1.472,1.472,Pass  
Pass SupportBoard-USM sdb 106 (SupportBoard-0, 33 C 31), sdb 106 (SupportBoard-USM, 33 C 31),.000,1.472,1.472,Pass  
Pass SupportBoard-USM sdb 107 (SupportBoard-0, 33 D 31), sdb 107 (SupportBoard-USM, 33 D 31),.000,1.472,1.472,Pass  
Pass SupportBoard-USM sdb 108 (SupportBoard-0, 33 A 32), sdb 108 (SupportBoard-USM, 33 A 32),.000,1.575,1.575,Pass  
Pass SupportBoard-USM sdb 109 (SupportBoard-0, 33 B 32), sdb 109 (SupportBoard-USM, 33 B 32),.000,1.501,1.501,Pass  
Pass SupportBoard-USM sdb 110 (SupportBoard-0, 33 C 32), sdb 110 (SupportBoard-USM, 33 C 32),.000,1.575,1.575,Pass  
Pass SupportBoard-USM sdb 111 (SupportBoard-0, 33 D 32), sdb 111 (SupportBoard-USM, 33 D 32),.000,1.501,1.501,Pass  
Pass SupportBoard-USM sdb 112 (SupportBoard-0, 33 A 34), sdb 112 (SupportBoard-USM, 33 A 34),.000,1.472,1.472,Pass  
Pass SupportBoard-USM sdb 113 (SupportBoard-0, 33 B 34), sdb 113 (SupportBoard-USM, 33 B 34),.000,1.472,1.472,Pass  
Pass SupportBoard-USM sdb 114 (SupportBoard-0, 33 C 34), sdb 114 (SupportBoard-USM, 33 C 34),.000,1.472,1.472,Pass  
Pass SupportBoard-USM sdb 115 (SupportBoard-0, 33 D 34), sdb 115 (SupportBoard-USM, 33 D 34),.000,1.472,1.472,Pass  
Pass SupportBoard-USM sdb 116 (SupportBoard-0, 33 A 35), sdb 116 (SupportBoard-USM, 33 A 35),.000,1.575,1.575,Pass  
Pass SupportBoard-USM sdb 117 (SupportBoard-0, 33 B 35), sdb 117 (SupportBoard-USM, 33 B 35),.000,1.501,1.501,Pass  
Pass SupportBoard-USM sdb 118 (SupportBoard-0, 33 C 35), sdb 118 (SupportBoard-USM, 33 C 35),.000,1.575,1.575,Pass  
Pass SupportBoard-USM sdb 119 (SupportBoard-0, 33 D 35), sdb 119 (SupportBoard-USM, 33 D 35),.000,1.501,1.501,Pass  
Pass SupportBoard-USM sdb 120 (SupportBoard-0, 33 A 36), sdb 120 (SupportBoard-USM, 33 A 36),.000,1.575,1.575,Pass

Pass	SupportBoard-USM 36),.000,1.472,1.472,Pass	sdb 121 (SupportBoard-0, 33 B 36), sdb 121 (SupportBoard-USM, 33 B
Pass	SupportBoard-USM 36),.000,1.575,1.575,Pass	sdb 122 (SupportBoard-0, 33 C 36), sdb 122 (SupportBoard-USM, 33 C
Pass	SupportBoard-USM 36),.000,1.472,1.472,Pass	sdb 123 (SupportBoard-0, 33 D 36), sdb 123 (SupportBoard-USM, 33 D
Pass	SupportBoard-USM 37),.000,1.853,1.853,Pass	sdb 124 (SupportBoard-0, 33 A 37), sdb 124 (SupportBoard-USM, 33 A
Pass	SupportBoard-USM 37),.000,1.575,1.575,Pass	sdb 125 (SupportBoard-0, 33 B 37), sdb 125 (SupportBoard-USM, 33 B
Pass	SupportBoard-USM 37),.000,1.838,1.838,Pass	sdb 126 (SupportBoard-0, 33 C 37), sdb 126 (SupportBoard-USM, 33 C
Pass	SupportBoard-USM 37),.000,1.575,1.575,Pass	sdb 127 (SupportBoard-0, 33 D 37), sdb 127 (SupportBoard-USM, 33 D
Pass	SupportBoard-USM A 21),-.007,5.514,5.522,Pass	DC_CAL_HS (SupportBoard-0, 20 A 13), +12V a (SupportBoard-USM, 45
Pass	SupportBoard-USM B 21),-.007,5.538,5.545,Pass	DC_CAL_HS (SupportBoard-0, 20 A 13), +12V b (SupportBoard-USM, 45
Pass	SupportBoard-USM C 21),-.007,5.538,5.545,Pass	DC_CAL_HS (SupportBoard-0, 20 A 13), +12V c (SupportBoard-USM, 45
Pass	SupportBoard-USM D 21),-.007,5.538,5.544,Pass	DC_CAL_HS (SupportBoard-0, 20 A 13), +12V d (SupportBoard-USM, 45
Pass	SupportBoard-USM ,.150,5.041,4.891,Pass	DC_CAL_HS (SupportBoard-0, 20 A 13), +5V Return a (SupportBoard-
Pass	SupportBoard-USM ,.264,5.042,4.778,Pass	DC_CAL_HS (SupportBoard-0, 20 A 13), +5V Return b (SupportBoard-
Pass	SupportBoard-USM ,.254,5.042,4.788,Pass	DC_CAL_HS (SupportBoard-0, 20 A 13), +5V Return c (SupportBoard-
Pass	SupportBoard-USM ,.258,5.041,4.783,Pass	DC_CAL_HS (SupportBoard-0, 20 A 13), +5V Return d (SupportBoard-
Pass	SupportBoard-USM A 25),-.007,2.751,2.758,Pass	DC_CAL_HS (SupportBoard-0, 20 A 13), +15V a (SupportBoard-USM, 45
Pass	SupportBoard-USM B 25),-.006,2.750,2.756,Pass	DC_CAL_HS (SupportBoard-0, 20 A 13), +15V b (SupportBoard-USM, 45
Pass	SupportBoard-USM C 25),-.006,2.751,2.758,Pass	DC_CAL_HS (SupportBoard-0, 20 A 13), +15V c (SupportBoard-USM, 45
Pass	SupportBoard-USM D 25),-.007,2.750,2.757,Pass	DC_CAL_HS (SupportBoard-0, 20 A 13), +15V d (SupportBoard-USM, 45
Pass	SupportBoard-USM A 21),-.006,5.514,5.520,Pass	DC_CAL_HS (SupportBoard-0, 20 A 13), +12V e (SupportBoard-USM, 33
Pass	SupportBoard-USM 21),-.007,5.537,5.544,Pass	DC_CAL_HS (SupportBoard-0, 20 A 13), +12V f (SupportBoard-USM, 33 B
Pass	SupportBoard-USM C 21),-.007,5.537,5.544,Pass	DC_CAL_HS (SupportBoard-0, 20 A 13), +12V g (SupportBoard-USM, 33
Pass	SupportBoard-USM D 21),-.007,5.537,5.544,Pass	DC_CAL_HS (SupportBoard-0, 20 A 13), +12V h (SupportBoard-USM, 33
Pass	SupportBoard-USM ,.158,5.043,4.885,Pass	DC_CAL_HS (SupportBoard-0, 20 A 13), +5V Return e (SupportBoard-

Pass SupportBoard-USM DC\_CAL\_HS (SupportBoard-0, 20 A 13), +5V Return f (SupportBoard-USM, 33 B 23),.247,5.041,4.794,Pass  
 Pass SupportBoard-USM DC\_CAL\_HS (SupportBoard-0, 20 A 13), +5V Return g (SupportBoard-USM, 33 C 23),.260,5.042,4.782,Pass  
 Pass SupportBoard-USM DC\_CAL\_HS (SupportBoard-0, 20 A 13), +5V Return h (SupportBoard-USM, 33 D 23),.257,5.043,4.786,Pass  
 Pass SupportBoard-USM DC\_CAL\_HS (SupportBoard-0, 20 A 13), +15V e (SupportBoard-USM, 33 A 25),-.007,2.751,2.757,Pass  
 Pass SupportBoard-USM DC\_CAL\_HS (SupportBoard-0, 20 A 13), +15V f (SupportBoard-USM, 33 B 25),-.006,2.751,2.758,Pass  
 Pass SupportBoard-USM DC\_CAL\_HS (SupportBoard-0, 20 A 13), +15V g (SupportBoard-USM, 33 C 25),-.007,2.755,2.761,Pass  
 Pass SupportBoard-USM DC\_CAL\_HS (SupportBoard-0, 20 A 13), +15V h (SupportBoard-USM, 33 D 25),-.007,2.748,2.755,Pass  
 Info SupportBoard-USM Test finished  
 Info BBAC-15 Beginning BBAC-15 continuity test  
 Pass BBAC-15 src1+ (2 A 3) pin 1 DUT relay stuck closed  
 Pass BBAC-15 src2Ref (2 B 4) pin 2 DUT relay stuck closed  
 Pass BBAC-15 src1+ (BBAC-15, 2 A 3), src2Ref (BBAC-15, 2 B 4),.000,2.185,2.185,Pass  
 Pass BBAC-15 DGSS1 (2 A 9) pin 1 DUT relay stuck closed  
 Pass BBAC-15 cap2+ (2 B 10) pin 2 DUT relay stuck closed  
 Pass BBAC-15 DGSS1 (BBAC-15, 2 A 9), cap2+ (BBAC-15, 2 B 10),.000,1.509,1.509,Pass  
 Pass BBAC-15 accs1- (2 B 16) pin 2 DUT relay stuck closed  
 Pass BBAC-15 cap1- (BBAC-15, 2 A 15), accs1- (BBAC-15, 2 B 16),.000,1.662,1.662,Pass  
 Pass BBAC-15 accs1+ (2 A 17) pin 2 DUT relay stuck closed  
 Pass BBAC-15 cap1+ (BBAC-15, 2 A 11), accs1+ (BBAC-15, 2 A 17),.000,2.490,2.490,Pass  
 Pass BBAC-15 accs2+ (2 A 21) pin 2 DUT relay stuck closed  
 Pass BBAC-15 acccap1- (BBAC-15, 2 B 22), accs2+ (BBAC-15, 2 A 21),.000,1.655,1.655,Pass  
 Pass BBAC-15 acccap2- (BBAC-15, 2 B 24), accs2Ref (BBAC-15, 2 A 19),.000,1.653,1.653,Pass  
 Pass BBAC-15 DGSc2 (BBAC-15, 2 B 12), src1- (BBAC-15, 2 A 7),.000,1.335,1.335,Pass  
 Pass BBAC-15 DGSc1 (BBAC-15, 2 A 13), src1- (BBAC-15, 2 A 7),.000,1.502,1.502,Pass  
 Pass BBAC-15 DGSc1 (BBAC-15, 2 A 13), accs1Ref (BBAC-15, 2 B 18),.000,2.009,2.009,Pass  
 Pass BBAC-15 DGSS2 (2 B 8) pin 1 DUT relay stuck closed  
 Pass BBAC-15 cap2- (2 B 14) pin 2 DUT relay stuck closed  
 Pass BBAC-15 DGSS2 (BBAC-15, 2 B 8), cap2- (BBAC-15, 2 B 14),.000,1.337,1.337,Pass  
 Pass BBAC-15 src2+ (2 B 2) pin 1 DUT relay stuck closed  
 Pass BBAC-15 src2- (2 B 6) pin 2 DUT relay stuck closed  
 Pass BBAC-15 src2+ (BBAC-15, 2 B 2), src2- (BBAC-15, 2 B 6),.000,.989,.989,Pass  
 Pass BBAC-15 src1Ref (2 A 5) pin 1 DUT relay stuck closed  
 Pass BBAC-15 accs2- (2 B 20) pin 2 DUT relay stuck closed  
 Pass BBAC-15 src1Ref (BBAC-15, 2 A 5), accs2- (BBAC-15, 2 B 20),.000,.985,.985,Pass  
 Pass BBAC-15 acccap1+ (BBAC-15, 2 A 23), acccap2+ (BBAC-15, 2 A 25),.000,1.652,1.652,Pass  
 Pass BBAC-15 DIBtrigs1 (BBAC-15, 2 A 37), -5V Return a (SupportBoard-0, 8 A 24),.000,.235,.235,Pass  
 Pass BBAC-15 DIBtrigc1 (BBAC-15, 2 C 37), -5V Return a (SupportBoard-0, 8 A 24),.000,.235,.235,Pass  
 Pass BBAC-15 DIBtrigs2 (BBAC-15, 2 B 36), -5V Return a (SupportBoard-0, 8 A 24),.000,.235,.235,Pass



Pass BBAC-15 DIBtrigc2 (BBAC-15, 2 D 36), -5V Return a (SupportBoard-0, 8 A 24),.000,.235,.235,Pass  
 Pass BBAC-15 src1+ (25 A 3) pin 1 DUT relay stuck closed  
 Pass BBAC-15 src2Ref (25 B 4) pin 2 DUT relay stuck closed  
 Pass BBAC-15 src1+ (BBAC-15, 25 A 3), src2Ref (BBAC-15, 25 B 4),.000,2.180,2.180,Pass  
 Pass BBAC-15 DGSs1 (25 A 9) pin 1 DUT relay stuck closed  
 Pass BBAC-15 cap2+ (25 B 10) pin 2 DUT relay stuck closed  
 Pass BBAC-15 DGSs1 (BBAC-15, 25 A 9), cap2+ (BBAC-15, 25 B 10),.000,1.505,1.505,Pass  
 Pass BBAC-15 accs1- (25 B 16) pin 2 DUT relay stuck closed  
 Pass BBAC-15 cap1- (BBAC-15, 25 A 15), accs1- (BBAC-15, 25 B 16),.000,1.659,1.659,Pass  
 Pass BBAC-15 accs1+ (25 A 17) pin 2 DUT relay stuck closed  
 Pass BBAC-15 cap1+ (BBAC-15, 25 A 11), accs1+ (BBAC-15, 25 A 17),.000,2.488,2.488,Pass  
 Pass BBAC-15 accs2+ (25 A 21) pin 2 DUT relay stuck closed  
 Pass BBAC-15 acccap1- (BBAC-15, 25 B 22), accs2+ (BBAC-15, 25 A 21),.000,1.654,1.654,Pass  
 Pass BBAC-15 acccap2- (BBAC-15, 25 B 24), accs2Ref (BBAC-15, 25 A 19),.000,1.661,1.661,Pass  
 Pass BBAC-15 DGSc2 (BBAC-15, 25 B 12), src1- (BBAC-15, 25 A 7),.000,1.338,1.338,Pass  
 Pass BBAC-15 DGSc1 (BBAC-15, 25 A 13), src1- (BBAC-15, 25 A 7),.000,1.507,1.507,Pass  
 Pass BBAC-15 DGSc1 (BBAC-15, 25 A 13), accs1Ref (BBAC-15, 25 B 18),.000,2.012,2.012,Pass  
 Pass BBAC-15 DGSs2 (25 B 8) pin 1 DUT relay stuck closed  
 Pass BBAC-15 cap2- (25 B 14) pin 2 DUT relay stuck closed  
 Pass BBAC-15 DGSs2 (BBAC-15, 25 B 8), cap2- (BBAC-15, 25 B 14),.000,1.335,1.335,Pass  
 Pass BBAC-15 src2+ (25 B 2) pin 1 DUT relay stuck closed  
 Pass BBAC-15 src2- (25 B 6) pin 2 DUT relay stuck closed  
 Pass BBAC-15 src2+ (BBAC-15, 25 B 2), src2- (BBAC-15, 25 B 6),.000,.983,.983,Pass  
 Pass BBAC-15 src1Ref (25 A 5) pin 1 DUT relay stuck closed  
 Pass BBAC-15 accs2- (25 B 20) pin 2 DUT relay stuck closed  
 Pass BBAC-15 src1Ref (BBAC-15, 25 A 5), accs2- (BBAC-15, 25 B 20),.000,.985,.985,Pass  
 Pass BBAC-15 acccap1+ (BBAC-15, 25 A 23), acccap2+ (BBAC-15, 25 A 25),.000,1.641,1.641,Pass  
 Pass BBAC-15 DIBtrigs1 (BBAC-15, 25 A 37), -5V Return a (SupportBoard-0, 8 A 24),.000,.235,.235,Pass  
 Pass BBAC-15 DIBtrigc1 (BBAC-15, 25 C 37), -5V Return a (SupportBoard-0, 8 A 24),.000,.235,.235,Pass  
 Pass BBAC-15 DIBtrigs2 (BBAC-15, 25 B 36), -5V Return a (SupportBoard-0, 8 A 24),.000,.235,.235,Pass  
 Pass BBAC-15 DIBtrigc2 (BBAC-15, 25 D 36), -5V Return a (SupportBoard-0, 8 A 24),.000,.235,.235,Pass  
 Info BBAC-15 Test finished  
 Info DC-30 Beginning DC-30 continuity test  
 Pass DC-30 Ch 4 Force (4 A 13) pin 2 DUT relay stuck closed  
 Pass DC-30 Gnd 1 (DC-30, 4 A 35), Ch 4 Force (DC-30, 4 A 13),.000,1.165,1.165,Pass  
 Pass DC-30 Gnd 2 (DC-30, 4 A 36), Ch 4 Force (DC-30, 4 A 13),.000,.774,.774,Pass  
 Pass DC-30 acc 8 (4 B 30) pin 2 DUT relay stuck closed  
 Pass DC-30 Ch 1 Sense (DC-30, 4 A 2), acc 8 (DC-30, 4 B 30),.000,1.080,1.080,Pass  
 Pass DC-30 Ch 15 Sense (4 D 26) pin 1 DUT relay stuck closed  
 Pass DC-30 acc 2 (4 B 6) pin 2 DUT relay stuck closed  
 Pass DC-30 Ch 15 Sense (DC-30, 4 D 26), acc 2 (DC-30, 4 B 6),.000,1.177,1.177,Pass  
 Pass DC-30 Ch 20 Sense (4 A 16) pin 1 DUT relay stuck closed

Pass DC-30 acc 5 (4 B 18) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 20 Sense (DC-30, 4 A 16), acc 5 (DC-30, 4 B 18),.000,2.066,2.066,Pass  
Pass DC-30 Ch 11 Sense (4 D 10) pin 1 DUT relay stuck closed  
Pass DC-30 acc 3 (4 B 10) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 11 Sense (DC-30, 4 D 10), acc 3 (DC-30, 4 B 10),.000,1.865,1.865,Pass  
Pass DC-30 Ch 11 Sense (4 D 10) pin 1 DUT relay stuck closed  
Pass DC-30 acc 10 (4 C 6) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 11 Sense (DC-30, 4 D 10), acc 10 (DC-30, 4 C 6),.000,2.035,2.035,Pass  
Pass DC-30 Ch 14 Sense (4 D 22) pin 1 DUT relay stuck closed  
Pass DC-30 acc 6 (4 B 22) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 14 Sense (DC-30, 4 D 22), acc 6 (DC-30, 4 B 22),.000,1.949,1.949,Pass  
Pass DC-30 Ch 1 Sense (4 A 2) pin 1 DUT relay stuck closed  
Pass DC-30 acc 9 (4 C 2) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 1 Sense (DC-30, 4 A 2), acc 9 (DC-30, 4 C 2),.000,.890,.890,Pass  
Pass DC-30 Ch 9 Sense (4 D 2) pin 1 DUT relay stuck closed  
Pass DC-30 acc 1 (4 B 2) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 9 Sense (DC-30, 4 D 2), acc 1 (DC-30, 4 B 2),.000,1.606,1.606,Pass  
Pass DC-30 Ch 12 Sense (4 D 14) pin 1 DUT relay stuck closed  
Pass DC-30 acc 4 (4 B 14) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 12 Sense (DC-30, 4 D 14), acc 4 (DC-30, 4 B 14),.000,1.848,1.848,Pass  
Pass DC-30 Ch 15 Sense (4 D 26) pin 1 DUT relay stuck closed  
Pass DC-30 acc 7 (4 B 26) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 15 Sense (DC-30, 4 D 26), acc 7 (DC-30, 4 B 26),.000,3.116,3.116,Pass  
Pass DC-30 Ch 17 Sense (4 A 4) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 1 Force (4 A 1) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 17 Sense (DC-30, 4 A 4), Ch 1 Force (DC-30, 4 A 1),.000,2.354,2.354,Pass  
Pass DC-30 Ch 10 Sense (4 D 6) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 10 Sense (DC-30, 4 D 6), Ch 1 Force (DC-30, 4 A 1),.000,2.347,2.347,Pass  
Pass DC-30 Ch 3 Sense (4 A 10) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 11 Force (4 D 9) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 3 Sense (DC-30, 4 A 10), Ch 11 Force (DC-30, 4 D 9),.000,2.523,2.523,Pass  
Pass DC-30 Ch 11 Force (4 D 9) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 3 Sense (DC-30, 4 A 10), Ch 11 Force (DC-30, 4 D 9),.000,2.523,2.523,Pass  
Pass DC-30 Ch 13 Sense (4 D 18) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 4 Force (4 A 13) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 13 Sense (DC-30, 4 D 18), Ch 4 Force (DC-30, 4 A 13),.000,2.321,2.321,Pass  
Pass DC-30 Ch 6 Sense (4 A 22) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 14 Force (4 D 21) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 6 Sense (DC-30, 4 A 22), Ch 14 Force (DC-30, 4 D 21),.000,2.524,2.524,Pass  
Pass DC-30 Ch 19 Sense (4 A 12) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 12 Force (4 D 13) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 19 Sense (DC-30, 4 A 12), Ch 12 Force (DC-30, 4 D 13),.000,2.331,2.331,Pass  
Pass DC-30 Ch 20 Force (4 A 15) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 19 Sense (DC-30, 4 A 12), Ch 20 Force (DC-30, 4 A 15),.000,1.618,1.618,Pass  
Pass DC-30 Ch 5 Sense (4 A 18) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 6 Force (4 A 21) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 5 Sense (DC-30, 4 A 18), Ch 6 Force (DC-30, 4 A 21),.000,2.326,2.326,Pass  
Pass DC-30 Ch 1 Sense (4 A 2) pin 1 DUT relay stuck closed

Pass DC-30 Ch 2 Force (4 A 5) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 1 Sense (DC-30, 4 A 2), Ch 2 Force (DC-30, 4 A 5),.000,.954,.954,Pass  
Pass DC-30 Ch 7 Sense (4 A 26) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 8 Force (4 A 29) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 7 Sense (DC-30, 4 A 26), Ch 8 Force (DC-30, 4 A 29),.000,2.343,2.343,Pass  
Pass DC-30 Ch 12 Sense (4 D 14) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 10 Force (4 D 5) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 12 Sense (DC-30, 4 D 14), Ch 10 Force (DC-30, 4 D 5),.000,2.325,2.325,Pass  
Info DC-30 Voltage measurement without connecting measuring pin = -3.93488770350814E-03  
Info DC-30 Voltage measurement before forcing voltage = -4.06008446589112E-03  
Pass DC-30 Ch 14 Sense (4 D 22) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 16 Force (4 D 29) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 14 Sense (DC-30, 4 D 22), Ch 16 Force (DC-30, 4 D 29),.000,1.179,1.179,Pass  
Pass DC-30 Ch 2 Sense (4 A 6) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 3 Force (4 A 9) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 2 Sense (DC-30, 4 A 6), Ch 3 Force (DC-30, 4 A 9),.000,2.328,2.328,Pass  
Pass DC-30 Ch 4 Sense (4 A 14) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 5 Force (4 A 17) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 4 Sense (DC-30, 4 A 14), Ch 5 Force (DC-30, 4 A 17),.000,2.336,2.336,Pass  
Pass DC-30 Ch 18 Sense (4 A 8) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 19 Force (4 A 11) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 18 Sense (DC-30, 4 A 8), Ch 19 Force (DC-30, 4 A 11),.000,1.592,1.592,Pass  
Pass DC-30 Ch 15 Sense (4 D 26) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 13 Force (4 D 17) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 15 Sense (DC-30, 4 D 26), Ch 13 Force (DC-30, 4 D 17),.000,2.332,2.332,Pass  
Pass DC-30 Ch 16 Sense (4 D 30) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 7 Force (4 A 25) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 16 Sense (DC-30, 4 D 30), Ch 7 Force (DC-30, 4 A 25),.000,2.331,2.331,Pass  
Pass DC-30 Ch 3 Sense (DC-30, 4 A 10), Ch 1 Guard (DC-30, 4 B 1),.000,.336,.336,Pass  
Pass DC-30 Ch 12 Sense (DC-30, 4 D 14), Ch 2 Guard (DC-30, 4 B 5),.000,1.306,1.306,Pass  
Pass DC-30 Ch 12 Sense (DC-30, 4 D 14), Ch 3 Guard (DC-30, 4 B 9),.000,.664,.664,Pass  
Pass DC-30 Ch 19 Sense (DC-30, 4 A 12), Ch 4 Guard (DC-30, 4 B 13),.000,1.311,1.311,Pass  
Pass DC-30 Ch 15 Sense (DC-30, 4 D 26), Ch 5 Guard (DC-30, 4 B 17),.000,1.307,1.307,Pass  
Pass DC-30 Ch 17 Sense (DC-30, 4 A 4), Ch 9 Guard (DC-30, 4 C 1),.000,1.307,1.307,Pass  
Pass DC-30 Ch 1 Sense (DC-30, 4 A 2), Ch 10 Guard (DC-30, 4 C 5),.000,.338,.338,Pass  
Pass DC-30 Ch 2 Sense (DC-30, 4 A 6), Ch 11 Guard (DC-30, 4 C 9),.000,1.311,1.311,Pass  
Pass DC-30 Ch 20 Sense (DC-30, 4 A 16), Ch 12 Guard (DC-30, 4 C 13),.000,1.302,1.302,Pass  
Pass DC-30 Ch 18 Sense (DC-30, 4 A 8), Ch 13 Guard (DC-30, 4 C 17),.000,1.314,1.314,Pass  
Pass DC-30 Ch 5 Sense (DC-30, 4 A 18), Ch 14 Guard (DC-30, 4 C 21),.000,1.308,1.308,Pass  
Pass DC-30 Ch 7 Sense (DC-30, 4 A 26), Ch 16 Guard (DC-30, 4 C 29),.000,1.305,1.305,Pass  
Pass DC-30 Ch 12 Sense (DC-30, 4 D 14), Ch 18 Guard (DC-30, 4 B 8),.000,1.309,1.309,Pass  
Pass DC-30 Ch 3 Sense (DC-30, 4 A 10), Ch 19 Guard (DC-30, 4 B 12),.000,1.308,1.308,Pass  
Pass DC-30 Ch 19 Sense (DC-30, 4 A 12), Ch 20 Guard (DC-30, 4 B 16),.000,1.311,1.311,Pass  
Pass DC-30 Ch 16 Sense (DC-30, 4 D 30), Ch 4 Force (DC-30, 4 A 13),.000,1.571,1.571,Pass  
Pass DC-30 Ch 15 Force (4 D 25) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 1 Sense (DC-30, 4 A 2), Ch 15 Force (DC-30, 4 D 25),.000,.786,.786,Pass  
Pass DC-30 Ch 1 Sense (4 A 2) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 9 Force (4 D 1) pin 2 DUT relay stuck closed

Pass DC-30 Ch 1 Sense (DC-30, 4 A 2), Ch 9 Force (DC-30, 4 D 1),.000,.587,.587,Pass  
Pass DC-30 Ch 17 Force (4 A 3) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 1 Sense (DC-30, 4 A 2), Ch 17 Force (DC-30, 4 A 3),.000,.651,.651,Pass  
Pass DC-30 Ch 17 Force (4 A 3) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 1 Sense (DC-30, 4 A 2), Ch 17 Force (DC-30, 4 A 3),.000,.722,.722,Pass  
Pass DC-30 Ch 1 Sense (DC-30, 4 A 2), Ch 6 Guard (DC-30, 4 B 21),.000,.267,.267,Pass  
Pass DC-30 Ch 1 Sense (DC-30, 4 A 2), Ch 7 Guard (DC-30, 4 B 25),.000,.270,.270,Pass  
Pass DC-30 Ch 1 Sense (DC-30, 4 A 2), Ch 8 Guard (DC-30, 4 B 29),.000,.223,.223,Pass  
Pass DC-30 Ch 1 Sense (DC-30, 4 A 2), Ch 15 Guard (DC-30, 4 C 25),.000,.267,.267,Pass  
Pass DC-30 Ch 1 Sense (DC-30, 4 A 2), Ch 17 Guard (DC-30, 4 B 4),.000,.189,.189,Pass  
Pass DC-30 Dig 1 (DC-30, 4 A 38), Ch 4 Force (DC-30, 4 A 13),.000,.238,.238,Pass  
Pass DC-30 Dig 2 (DC-30, 4 B 38), Ch 4 Force (DC-30, 4 A 13),.000,.240,.240,Pass  
Pass DC-30 Gnd 4 (DC-30, 4 B 36), Ch 4 Force (DC-30, 4 A 13),.000,.698,.698,Pass  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI1\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI1\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI1\_TmuFE\_10VHiZ Period  
Pass DC-30 VI1\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI2\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI2\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI2\_TmuFE\_10VHiZ Period  
Pass DC-30 VI2\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI3\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI3\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI3\_TmuFE\_10VHiZ Period  
Pass DC-30 VI3\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI4\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI4\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI4\_TmuFE\_10VHiZ Period  
Pass DC-30 VI4\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI5\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI5\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI5\_TmuFE\_10VHiZ Period  
Pass DC-30 VI5\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI6\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI6\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI6\_TmuFE\_10VHiZ Period  
Pass DC-30 VI6\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI7\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI7\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI7\_TmuFE\_10VHiZ Period  
Pass DC-30 VI7\_TmuFE\_10VHiZ Period

Pass DC-30 50 Ohm Imp  
Pass DC-30 VI8\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI8\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI8\_TmuFE\_10VHiZ Period  
Pass DC-30 VI8\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI9\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI9\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI9\_TmuFE\_10VHiZ Period  
Pass DC-30 VI9\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI10\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI10\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI10\_TmuFE\_10VHiZ Period  
Pass DC-30 VI10\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI11\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI11\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI11\_TmuFE\_10VHiZ Period  
Pass DC-30 VI11\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI12\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI12\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI12\_TmuFE\_10VHiZ Period  
Pass DC-30 VI12\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI13\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI13\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI13\_TmuFE\_10VHiZ Period  
Pass DC-30 VI13\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI14\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI14\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI14\_TmuFE\_10VHiZ Period  
Pass DC-30 VI14\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI15\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI15\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI15\_TmuFE\_10VHiZ Period  
Pass DC-30 VI15\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI16\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI16\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI16\_TmuFE\_10VHiZ Period  
Pass DC-30 VI16\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI17\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI17\_TmuFE\_10V50Ohm Period

Pass DC-30 VI17\_TmuFE\_10VHiZ Period  
Pass DC-30 VI17\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI18\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI18\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI18\_TmuFE\_10VHiZ Period  
Pass DC-30 VI18\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI19\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI19\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI19\_TmuFE\_10VHiZ Period  
Pass DC-30 VI19\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI20\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI20\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI20\_TmuFE\_10VHiZ Period  
Pass DC-30 VI20\_TmuFE\_10VHiZ Period  
Pass DC-30 TmuFeRelayStuckclosed  
Pass DC-30 TmuFeRelayStuckclosed  
Pass DC-30 TmuFeRelayStuckclosed  
Pass DC-30 TmuFeRelayStuckclosed  
Pass DC-30 TmuFeRelayStuckclosed  
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Pass DC-30 TmuFeRelayStuckclosed  
Pass DC-30 TmuFeRelayStuckclosed  
Pass DC-30 Ch 4 Force (7 A 13) pin 2 DUT relay stuck closed  
Pass DC-30 Gnd 1 (DC-30, 7 A 35), Ch 4 Force (DC-30, 7 A 13),.000,1.170,1.170,Pass  
Pass DC-30 Gnd 2 (DC-30, 7 A 36), Ch 4 Force (DC-30, 7 A 13),.000,.781,.781,Pass  
Pass DC-30 acc 8 (7 B 30) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 1 Sense (DC-30, 7 A 2), acc 8 (DC-30, 7 B 30),.000,1.091,1.091,Pass  
Pass DC-30 Ch 15 Sense (7 D 26) pin 1 DUT relay stuck closed  
Pass DC-30 acc 2 (7 B 6) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 15 Sense (DC-30, 7 D 26), acc 2 (DC-30, 7 B 6),.000,1.184,1.184,Pass  
Pass DC-30 Ch 20 Sense (7 A 16) pin 1 DUT relay stuck closed  
Pass DC-30 acc 5 (7 B 18) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 20 Sense (DC-30, 7 A 16), acc 5 (DC-30, 7 B 18),.000,2.065,2.065,Pass

Pass DC-30 Ch 11 Sense (7 D 10) pin 1 DUT relay stuck closed  
Pass DC-30 acc 3 (7 B 10) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 11 Sense (DC-30, 7 D 10), acc 3 (DC-30, 7 B 10),.000,1.861,1.861,Pass  
Pass DC-30 Ch 11 Sense (7 D 10) pin 1 DUT relay stuck closed  
Pass DC-30 acc 10 (7 C 6) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 11 Sense (DC-30, 7 D 10), acc 10 (DC-30, 7 C 6),.000,2.030,2.030,Pass  
Pass DC-30 Ch 14 Sense (7 D 22) pin 1 DUT relay stuck closed  
Pass DC-30 acc 6 (7 B 22) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 14 Sense (DC-30, 7 D 22), acc 6 (DC-30, 7 B 22),.000,1.949,1.949,Pass  
Pass DC-30 Ch 1 Sense (7 A 2) pin 1 DUT relay stuck closed  
Pass DC-30 acc 9 (7 C 2) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 1 Sense (DC-30, 7 A 2), acc 9 (DC-30, 7 C 2),.000,.888,.888,Pass  
Pass DC-30 Ch 9 Sense (7 D 2) pin 1 DUT relay stuck closed  
Pass DC-30 acc 1 (7 B 2) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 9 Sense (DC-30, 7 D 2), acc 1 (DC-30, 7 B 2),.000,1.606,1.606,Pass  
Pass DC-30 Ch 12 Sense (7 D 14) pin 1 DUT relay stuck closed  
Pass DC-30 acc 4 (7 B 14) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 12 Sense (DC-30, 7 D 14), acc 4 (DC-30, 7 B 14),.000,1.853,1.853,Pass  
Pass DC-30 Ch 15 Sense (7 D 26) pin 1 DUT relay stuck closed  
Pass DC-30 acc 7 (7 B 26) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 15 Sense (DC-30, 7 D 26), acc 7 (DC-30, 7 B 26),.000,3.111,3.111,Pass  
Pass DC-30 Ch 17 Sense (7 A 4) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 1 Force (7 A 1) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 17 Sense (DC-30, 7 A 4), Ch 1 Force (DC-30, 7 A 1),.000,2.353,2.353,Pass  
Pass DC-30 Ch 10 Sense (7 D 6) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 10 Sense (DC-30, 7 D 6), Ch 1 Force (DC-30, 7 A 1),.000,2.347,2.347,Pass  
Pass DC-30 Ch 3 Sense (7 A 10) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 11 Force (7 D 9) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 3 Sense (DC-30, 7 A 10), Ch 11 Force (DC-30, 7 D 9),.000,2.527,2.527,Pass  
Pass DC-30 Ch 11 Force (7 D 9) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 3 Sense (DC-30, 7 A 10), Ch 11 Force (DC-30, 7 D 9),.000,2.527,2.527,Pass  
Pass DC-30 Ch 13 Sense (7 D 18) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 4 Force (7 A 13) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 13 Sense (DC-30, 7 D 18), Ch 4 Force (DC-30, 7 A 13),.000,2.332,2.332,Pass  
Pass DC-30 Ch 6 Sense (7 A 22) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 14 Force (7 D 21) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 6 Sense (DC-30, 7 A 22), Ch 14 Force (DC-30, 7 D 21),.000,2.524,2.524,Pass  
Pass DC-30 Ch 19 Sense (7 A 12) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 12 Force (7 D 13) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 19 Sense (DC-30, 7 A 12), Ch 12 Force (DC-30, 7 D 13),.000,2.330,2.330,Pass  
Pass DC-30 Ch 20 Force (7 A 15) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 19 Sense (DC-30, 7 A 12), Ch 20 Force (DC-30, 7 A 15),.000,1.614,1.614,Pass  
Pass DC-30 Ch 5 Sense (7 A 18) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 6 Force (7 A 21) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 5 Sense (DC-30, 7 A 18), Ch 6 Force (DC-30, 7 A 21),.000,2.334,2.334,Pass  
Pass DC-30 Ch 1 Sense (7 A 2) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 2 Force (7 A 5) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 1 Sense (DC-30, 7 A 2), Ch 2 Force (DC-30, 7 A 5),.000,.954,.954,Pass

Pass DC-30 Ch 7 Sense (7 A 26) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 8 Force (7 A 29) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 7 Sense (DC-30, 7 A 26), Ch 8 Force (DC-30, 7 A 29),.000,2.348,2.348,Pass  
Pass DC-30 Ch 12 Sense (7 D 14) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 10 Force (7 D 5) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 12 Sense (DC-30, 7 D 14), Ch 10 Force (DC-30, 7 D 5),.000,2.329,2.329,Pass  
Info DC-30 Voltage measurement without connecting measuring pin = -3.66463814862072E-03  
Info DC-30 Voltage measurement before forcing voltage = -3.66463814862072E-03  
Pass DC-30 Ch 14 Sense (7 D 22) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 16 Force (7 D 29) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 14 Sense (DC-30, 7 D 22), Ch 16 Force (DC-30, 7 D 29),.000,1.173,1.173,Pass  
Pass DC-30 Ch 2 Sense (7 A 6) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 3 Force (7 A 9) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 2 Sense (DC-30, 7 A 6), Ch 3 Force (DC-30, 7 A 9),.000,2.316,2.316,Pass  
Pass DC-30 Ch 4 Sense (7 A 14) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 5 Force (7 A 17) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 4 Sense (DC-30, 7 A 14), Ch 5 Force (DC-30, 7 A 17),.000,2.323,2.323,Pass  
Pass DC-30 Ch 18 Sense (7 A 8) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 19 Force (7 A 11) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 18 Sense (DC-30, 7 A 8), Ch 19 Force (DC-30, 7 A 11),.000,1.588,1.588,Pass  
Pass DC-30 Ch 15 Sense (7 D 26) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 13 Force (7 D 17) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 15 Sense (DC-30, 7 D 26), Ch 13 Force (DC-30, 7 D 17),.000,2.325,2.325,Pass  
Pass DC-30 Ch 16 Sense (7 D 30) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 7 Force (7 A 25) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 16 Sense (DC-30, 7 D 30), Ch 7 Force (DC-30, 7 A 25),.000,2.327,2.327,Pass  
Pass DC-30 Ch 3 Sense (DC-30, 7 A 10), Ch 1 Guard (DC-30, 7 B 1),.000,.336,.336,Pass  
Pass DC-30 Ch 12 Sense (DC-30, 7 D 14), Ch 2 Guard (DC-30, 7 B 5),.000,1.309,1.309,Pass  
Pass DC-30 Ch 12 Sense (DC-30, 7 D 14), Ch 3 Guard (DC-30, 7 B 9),.000,.664,.664,Pass  
Pass DC-30 Ch 19 Sense (DC-30, 7 A 12), Ch 4 Guard (DC-30, 7 B 13),.000,1.307,1.307,Pass  
Pass DC-30 Ch 15 Sense (DC-30, 7 D 26), Ch 5 Guard (DC-30, 7 B 17),.000,1.310,1.310,Pass  
Pass DC-30 Ch 17 Sense (DC-30, 7 A 4), Ch 9 Guard (DC-30, 7 C 1),.000,1.306,1.306,Pass  
Pass DC-30 Ch 1 Sense (DC-30, 7 A 2), Ch 10 Guard (DC-30, 7 C 5),.000,.337,.337,Pass  
Pass DC-30 Ch 2 Sense (DC-30, 7 A 6), Ch 11 Guard (DC-30, 7 C 9),.000,1.306,1.306,Pass  
Pass DC-30 Ch 20 Sense (DC-30, 7 A 16), Ch 12 Guard (DC-30, 7 C 13),.000,1.307,1.307,Pass  
Pass DC-30 Ch 18 Sense (DC-30, 7 A 8), Ch 13 Guard (DC-30, 7 C 17),.000,1.304,1.304,Pass  
Pass DC-30 Ch 5 Sense (DC-30, 7 A 18), Ch 14 Guard (DC-30, 7 C 21),.000,1.310,1.310,Pass  
Pass DC-30 Ch 7 Sense (DC-30, 7 A 26), Ch 16 Guard (DC-30, 7 C 29),.000,1.304,1.304,Pass  
Pass DC-30 Ch 12 Sense (DC-30, 7 D 14), Ch 18 Guard (DC-30, 7 B 8),.000,1.311,1.311,Pass  
Pass DC-30 Ch 3 Sense (DC-30, 7 A 10), Ch 19 Guard (DC-30, 7 B 12),.000,1.313,1.313,Pass  
Pass DC-30 Ch 19 Sense (DC-30, 7 A 12), Ch 20 Guard (DC-30, 7 B 16),.000,1.303,1.303,Pass  
Pass DC-30 Ch 16 Sense (DC-30, 7 D 30), Ch 4 Force (DC-30, 7 A 13),.000,1.573,1.573,Pass  
Pass DC-30 Ch 15 Force (7 D 25) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 1 Sense (DC-30, 7 A 2), Ch 15 Force (DC-30, 7 D 25),.000,.782,.782,Pass  
Pass DC-30 Ch 1 Sense (7 A 2) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 9 Force (7 D 1) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 1 Sense (DC-30, 7 A 2), Ch 9 Force (DC-30, 7 D 1),.000,.600,.600,Pass  
Pass DC-30 Ch 17 Force (7 A 3) pin 2 DUT relay stuck closed



Pass DC-30 Ch 1 Sense (DC-30, 7 A 2), Ch 17 Force (DC-30, 7 A 3),.000,.663,.663,Pass  
Pass DC-30 Ch 17 Force (7 A 3) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 1 Sense (DC-30, 7 A 2), Ch 17 Force (DC-30, 7 A 3),.000,.734,.734,Pass  
Pass DC-30 Ch 1 Sense (DC-30, 7 A 2), Ch 6 Guard (DC-30, 7 B 21),.000,.268,.268,Pass  
Pass DC-30 Ch 1 Sense (DC-30, 7 A 2), Ch 7 Guard (DC-30, 7 B 25),.000,.269,.269,Pass  
Pass DC-30 Ch 1 Sense (DC-30, 7 A 2), Ch 8 Guard (DC-30, 7 B 29),.000,.222,.222,Pass  
Pass DC-30 Ch 1 Sense (DC-30, 7 A 2), Ch 15 Guard (DC-30, 7 C 25),.000,.267,.267,Pass  
Pass DC-30 Ch 1 Sense (DC-30, 7 A 2), Ch 17 Guard (DC-30, 7 B 4),.000,.192,.192,Pass  
Pass DC-30 Dig 1 (DC-30, 7 A 38), Ch 4 Force (DC-30, 7 A 13),.000,.240,.240,Pass  
Pass DC-30 Dig 2 (DC-30, 7 B 38), Ch 4 Force (DC-30, 7 A 13),.000,.240,.240,Pass  
Pass DC-30 Gnd 4 (DC-30, 7 B 36), Ch 4 Force (DC-30, 7 A 13),.000,.700,.700,Pass  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI1\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI1\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI1\_TmuFE\_10VHiZ Period  
Pass DC-30 VI1\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI2\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI2\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI2\_TmuFE\_10VHiZ Period  
Pass DC-30 VI2\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI3\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI3\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI3\_TmuFE\_10VHiZ Period  
Pass DC-30 VI3\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI4\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI4\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI4\_TmuFE\_10VHiZ Period  
Pass DC-30 VI4\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI5\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI5\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI5\_TmuFE\_10VHiZ Period  
Pass DC-30 VI5\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI6\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI6\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI6\_TmuFE\_10VHiZ Period  
Pass DC-30 VI6\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI7\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI7\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI7\_TmuFE\_10VHiZ Period  
Pass DC-30 VI7\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI8\_TmuFE\_10V50Ohm Period

Pass DC-30 VI8\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI8\_TmuFE\_10VHiZ Period  
Pass DC-30 VI8\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI9\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI9\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI9\_TmuFE\_10VHiZ Period  
Pass DC-30 VI9\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI10\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI10\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI10\_TmuFE\_10VHiZ Period  
Pass DC-30 VI10\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI11\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI11\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI11\_TmuFE\_10VHiZ Period  
Pass DC-30 VI11\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI12\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI12\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI12\_TmuFE\_10VHiZ Period  
Pass DC-30 VI12\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI13\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI13\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI13\_TmuFE\_10VHiZ Period  
Pass DC-30 VI13\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI14\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI14\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI14\_TmuFE\_10VHiZ Period  
Pass DC-30 VI14\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI15\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI15\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI15\_TmuFE\_10VHiZ Period  
Pass DC-30 VI15\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI16\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI16\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI16\_TmuFE\_10VHiZ Period  
Pass DC-30 VI16\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI17\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI17\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI17\_TmuFE\_10VHiZ Period  
Pass DC-30 VI17\_TmuFE\_10VHiZ Period

Pass DC-30 50 Ohm Imp  
Pass DC-30 VI18\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI18\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI18\_TmuFE\_10VHiZ Period  
Pass DC-30 VI18\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI19\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI19\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI19\_TmuFE\_10VHiZ Period  
Pass DC-30 VI19\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI20\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI20\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI20\_TmuFE\_10VHiZ Period  
Pass DC-30 VI20\_TmuFE\_10VHiZ Period  
Pass DC-30 TmuFeRelayStuckclosed  
Pass DC-30 TmuFeRelayStuckclosed  
Pass DC-30 TmuFeRelayStuckclosed  
Pass DC-30 TmuFeRelayStuckclosed  
Pass DC-30 TmuFeRelayStuckclosed  
Pass DC-30 TmuFeRelayStuckclosed  
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Pass DC-30 TmuFeRelayStuckclosed  
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Pass DC-30 TmuFeRelayStuckclosed  
Pass DC-30 TmuFeRelayStuckclosed  
Pass DC-30 TmuFeRelayStuckclosed  
Pass DC-30 Ch 4 Force (18 A 13) pin 2 DUT relay stuck closed  
Pass DC-30 Gnd 1 (DC-30, 18 A 35), Ch 4 Force (DC-30, 18 A 13),.000,1.180,1.180,Pass  
Pass DC-30 Gnd 2 (DC-30, 18 A 36), Ch 4 Force (DC-30, 18 A 13),.000,.784,.784,Pass  
Pass DC-30 acc 8 (18 B 30) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 1 Sense (DC-30, 18 A 2), acc 8 (DC-30, 18 B 30),.000,1.091,1.091,Pass  
Pass DC-30 Ch 15 Sense (18 D 26) pin 1 DUT relay stuck closed  
Pass DC-30 acc 2 (18 B 6) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 15 Sense (DC-30, 18 D 26), acc 2 (DC-30, 18 B 6),.000,1.188,1.188,Pass  
Pass DC-30 Ch 20 Sense (18 A 16) pin 1 DUT relay stuck closed  
Pass DC-30 acc 5 (18 B 18) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 20 Sense (DC-30, 18 A 16), acc 5 (DC-30, 18 B 18),.000,2.066,2.066,Pass  
Pass DC-30 Ch 11 Sense (18 D 10) pin 1 DUT relay stuck closed  
Pass DC-30 acc 3 (18 B 10) pin 2 DUT relay stuck closed

Pass DC-30 Ch 11 Sense (DC-30, 18 D 10), acc 3 (DC-30, 18 B 10),.000,1.865,1.865,Pass  
Pass DC-30 Ch 11 Sense (18 D 10) pin 1 DUT relay stuck closed  
Pass DC-30 acc 10 (18 C 6) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 11 Sense (DC-30, 18 D 10), acc 10 (DC-30, 18 C 6),.000,2.036,2.036,Pass  
Pass DC-30 Ch 14 Sense (18 D 22) pin 1 DUT relay stuck closed  
Pass DC-30 acc 6 (18 B 22) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 14 Sense (DC-30, 18 D 22), acc 6 (DC-30, 18 B 22),.000,1.947,1.947,Pass  
Pass DC-30 Ch 1 Sense (18 A 2) pin 1 DUT relay stuck closed  
Pass DC-30 acc 9 (18 C 2) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 1 Sense (DC-30, 18 A 2), acc 9 (DC-30, 18 C 2),.000,.891,.891,Pass  
Pass DC-30 Ch 9 Sense (18 D 2) pin 1 DUT relay stuck closed  
Pass DC-30 acc 1 (18 B 2) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 9 Sense (DC-30, 18 D 2), acc 1 (DC-30, 18 B 2),.000,1.611,1.611,Pass  
Pass DC-30 Ch 12 Sense (18 D 14) pin 1 DUT relay stuck closed  
Pass DC-30 acc 4 (18 B 14) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 12 Sense (DC-30, 18 D 14), acc 4 (DC-30, 18 B 14),.000,1.841,1.841,Pass  
Pass DC-30 Ch 15 Sense (18 D 26) pin 1 DUT relay stuck closed  
Pass DC-30 acc 7 (18 B 26) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 15 Sense (DC-30, 18 D 26), acc 7 (DC-30, 18 B 26),.000,3.112,3.112,Pass  
Pass DC-30 Ch 17 Sense (18 A 4) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 1 Force (18 A 1) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 17 Sense (DC-30, 18 A 4), Ch 1 Force (DC-30, 18 A 1),.000,2.359,2.359,Pass  
Pass DC-30 Ch 10 Sense (18 D 6) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 10 Sense (DC-30, 18 D 6), Ch 1 Force (DC-30, 18 A 1),.000,2.352,2.352,Pass  
Pass DC-30 Ch 3 Sense (18 A 10) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 11 Force (18 D 9) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 3 Sense (DC-30, 18 A 10), Ch 11 Force (DC-30, 18 D 9),.000,2.526,2.526,Pass  
Pass DC-30 Ch 11 Force (18 D 9) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 3 Sense (DC-30, 18 A 10), Ch 11 Force (DC-30, 18 D 9),.000,2.527,2.527,Pass  
Pass DC-30 Ch 13 Sense (18 D 18) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 4 Force (18 A 13) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 13 Sense (DC-30, 18 D 18), Ch 4 Force (DC-30, 18 A 13),.000,2.331,2.331,Pass  
Pass DC-30 Ch 6 Sense (18 A 22) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 14 Force (18 D 21) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 6 Sense (DC-30, 18 A 22), Ch 14 Force (DC-30, 18 D 21),.000,2.526,2.526,Pass  
Pass DC-30 Ch 19 Sense (18 A 12) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 12 Force (18 D 13) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 19 Sense (DC-30, 18 A 12), Ch 12 Force (DC-30, 18 D 13),.000,2.330,2.330,Pass  
Pass DC-30 Ch 20 Force (18 A 15) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 19 Sense (DC-30, 18 A 12), Ch 20 Force (DC-30, 18 A 15),.000,1.608,1.608,Pass  
Pass DC-30 Ch 5 Sense (18 A 18) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 6 Force (18 A 21) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 5 Sense (DC-30, 18 A 18), Ch 6 Force (DC-30, 18 A 21),.000,2.325,2.325,Pass  
Pass DC-30 Ch 1 Sense (18 A 2) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 2 Force (18 A 5) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 1 Sense (DC-30, 18 A 2), Ch 2 Force (DC-30, 18 A 5),.000,.951,.951,Pass  
Pass DC-30 Ch 7 Sense (18 A 26) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 8 Force (18 A 29) pin 2 DUT relay stuck closed

Pass DC-30 Ch 7 Sense (DC-30, 18 A 26), Ch 8 Force (DC-30, 18 A 29),.000,2.355,2.355,Pass  
Pass DC-30 Ch 12 Sense (18 D 14) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 10 Force (18 D 5) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 12 Sense (DC-30, 18 D 14), Ch 10 Force (DC-30, 18 D 5),.000,2.328,2.328,Pass  
Info DC-30 Voltage measurement without connecting measuring pin = -7.10590463131666E-03  
Info DC-30 Voltage measurement before forcing voltage = -7.04329973086715E-03  
Pass DC-30 Ch 14 Sense (18 D 22) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 16 Force (18 D 29) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 14 Sense (DC-30, 18 D 22), Ch 16 Force (DC-30, 18 D 29),.000,1.178,1.178,Pass  
Pass DC-30 Ch 2 Sense (18 A 6) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 3 Force (18 A 9) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 2 Sense (DC-30, 18 A 6), Ch 3 Force (DC-30, 18 A 9),.000,2.328,2.328,Pass  
Pass DC-30 Ch 4 Sense (18 A 14) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 5 Force (18 A 17) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 4 Sense (DC-30, 18 A 14), Ch 5 Force (DC-30, 18 A 17),.000,2.320,2.320,Pass  
Pass DC-30 Ch 18 Sense (18 A 8) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 19 Force (18 A 11) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 18 Sense (DC-30, 18 A 8), Ch 19 Force (DC-30, 18 A 11),.000,1.595,1.595,Pass  
Pass DC-30 Ch 15 Sense (18 D 26) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 13 Force (18 D 17) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 15 Sense (DC-30, 18 D 26), Ch 13 Force (DC-30, 18 D 17),.000,2.331,2.331,Pass  
Pass DC-30 Ch 16 Sense (18 D 30) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 7 Force (18 A 25) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 16 Sense (DC-30, 18 D 30), Ch 7 Force (DC-30, 18 A 25),.000,2.323,2.323,Pass  
Pass DC-30 Ch 3 Sense (DC-30, 18 A 10), Ch 1 Guard (DC-30, 18 B 1),.000,.336,.336,Pass  
Pass DC-30 Ch 12 Sense (DC-30, 18 D 14), Ch 2 Guard (DC-30, 18 B 5),.000,1.305,1.305,Pass  
Pass DC-30 Ch 12 Sense (DC-30, 18 D 14), Ch 3 Guard (DC-30, 18 B 9),.000,.664,.664,Pass  
Pass DC-30 Ch 19 Sense (DC-30, 18 A 12), Ch 4 Guard (DC-30, 18 B 13),.000,1.306,1.306,Pass  
Pass DC-30 Ch 15 Sense (DC-30, 18 D 26), Ch 5 Guard (DC-30, 18 B 17),.000,1.314,1.314,Pass  
Pass DC-30 Ch 17 Sense (DC-30, 18 A 4), Ch 9 Guard (DC-30, 18 C 1),.000,1.310,1.310,Pass  
Pass DC-30 Ch 1 Sense (DC-30, 18 A 2), Ch 10 Guard (DC-30, 18 C 5),.000,.337,.337,Pass  
Pass DC-30 Ch 2 Sense (DC-30, 18 A 6), Ch 11 Guard (DC-30, 18 C 9),.000,1.309,1.309,Pass  
Pass DC-30 Ch 20 Sense (DC-30, 18 A 16), Ch 12 Guard (DC-30, 18 C 13),.000,1.310,1.310,Pass  
Pass DC-30 Ch 18 Sense (DC-30, 18 A 8), Ch 13 Guard (DC-30, 18 C 17),.000,1.306,1.306,Pass  
Pass DC-30 Ch 5 Sense (DC-30, 18 A 18), Ch 14 Guard (DC-30, 18 C 21),.000,1.308,1.308,Pass  
Pass DC-30 Ch 7 Sense (DC-30, 18 A 26), Ch 16 Guard (DC-30, 18 C 29),.000,1.315,1.315,Pass  
Pass DC-30 Ch 12 Sense (DC-30, 18 D 14), Ch 18 Guard (DC-30, 18 B 8),.000,1.305,1.305,Pass  
Pass DC-30 Ch 3 Sense (DC-30, 18 A 10), Ch 19 Guard (DC-30, 18 B 12),.000,1.307,1.307,Pass  
Pass DC-30 Ch 19 Sense (DC-30, 18 A 12), Ch 20 Guard (DC-30, 18 B 16),.000,1.304,1.304,Pass  
Pass DC-30 Ch 16 Sense (DC-30, 18 D 30), Ch 4 Force (DC-30, 18 A 13),.000,1.577,1.577,Pass  
Pass DC-30 Ch 15 Force (18 D 25) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 1 Sense (DC-30, 18 A 2), Ch 15 Force (DC-30, 18 D 25),.000,.788,.788,Pass  
Pass DC-30 Ch 1 Sense (18 A 2) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 9 Force (18 D 1) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 1 Sense (DC-30, 18 A 2), Ch 9 Force (DC-30, 18 D 1),.000,.601,.601,Pass  
Pass DC-30 Ch 17 Force (18 A 3) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 1 Sense (DC-30, 18 A 2), Ch 17 Force (DC-30, 18 A 3),.000,.663,.663,Pass  
Pass DC-30 Ch 17 Force (18 A 3) pin 2 DUT relay stuck closed

Pass DC-30 Ch 1 Sense (DC-30, 18 A 2), Ch 17 Force (DC-30, 18 A 3),.000,.734,.734,Pass  
Pass DC-30 Ch 1 Sense (DC-30, 18 A 2), Ch 6 Guard (DC-30, 18 B 21),.000,.269,.269,Pass  
Pass DC-30 Ch 1 Sense (DC-30, 18 A 2), Ch 7 Guard (DC-30, 18 B 25),.000,.270,.270,Pass  
Pass DC-30 Ch 1 Sense (DC-30, 18 A 2), Ch 8 Guard (DC-30, 18 B 29),.000,.224,.224,Pass  
Pass DC-30 Ch 1 Sense (DC-30, 18 A 2), Ch 15 Guard (DC-30, 18 C 25),.000,.269,.269,Pass  
Pass DC-30 Ch 1 Sense (DC-30, 18 A 2), Ch 17 Guard (DC-30, 18 B 4),.000,.193,.193,Pass  
Pass DC-30 Dig 1 (DC-30, 18 A 38), Ch 4 Force (DC-30, 18 A 13),.000,.242,.242,Pass  
Pass DC-30 Dig 2 (DC-30, 18 B 38), Ch 4 Force (DC-30, 18 A 13),.000,.242,.242,Pass  
Pass DC-30 Gnd 4 (DC-30, 18 B 36), Ch 4 Force (DC-30, 18 A 13),.000,.713,.713,Pass  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI1\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI1\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI1\_TmuFE\_10VHiZ Period  
Pass DC-30 VI1\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI2\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI2\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI2\_TmuFE\_10VHiZ Period  
Pass DC-30 VI2\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI3\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI3\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI3\_TmuFE\_10VHiZ Period  
Pass DC-30 VI3\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI4\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI4\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI4\_TmuFE\_10VHiZ Period  
Pass DC-30 VI4\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI5\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI5\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI5\_TmuFE\_10VHiZ Period  
Pass DC-30 VI5\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI6\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI6\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI6\_TmuFE\_10VHiZ Period  
Pass DC-30 VI6\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI7\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI7\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI7\_TmuFE\_10VHiZ Period  
Pass DC-30 VI7\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI8\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI8\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI8\_TmuFE\_10VHiZ Period

Pass DC-30 VI8\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI9\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI9\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI9\_TmuFE\_10VHiZ Period  
Pass DC-30 VI9\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI10\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI10\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI10\_TmuFE\_10VHiZ Period  
Pass DC-30 VI10\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI11\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI11\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI11\_TmuFE\_10VHiZ Period  
Pass DC-30 VI11\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI12\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI12\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI12\_TmuFE\_10VHiZ Period  
Pass DC-30 VI12\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI13\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI13\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI13\_TmuFE\_10VHiZ Period  
Pass DC-30 VI13\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI14\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI14\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI14\_TmuFE\_10VHiZ Period  
Pass DC-30 VI14\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI15\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI15\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI15\_TmuFE\_10VHiZ Period  
Pass DC-30 VI15\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI16\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI16\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI16\_TmuFE\_10VHiZ Period  
Pass DC-30 VI16\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI17\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI17\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI17\_TmuFE\_10VHiZ Period  
Pass DC-30 VI17\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI18\_TmuFE\_10V50Ohm Period

Pass DC-30 VI18\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI18\_TmuFE\_10VHiZ Period  
Pass DC-30 VI18\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI19\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI19\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI19\_TmuFE\_10VHiZ Period  
Pass DC-30 VI19\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI20\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI20\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI20\_TmuFE\_10VHiZ Period  
Pass DC-30 VI20\_TmuFE\_10VHiZ Period  
Pass DC-30 TmuFeRelayStuckclosed  
Pass DC-30 TmuFeRelayStuckclosed  
Pass DC-30 TmuFeRelayStuckclosed  
Pass DC-30 TmuFeRelayStuckclosed  
Pass DC-30 TmuFeRelayStuckclosed  
Pass DC-30 TmuFeRelayStuckclosed  
Pass DC-30 TmuFeRelayStuckclosed  
Pass DC-30 TmuFeRelayStuckclosed  
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Pass DC-30 TmuFeRelayStuckclosed  
Pass DC-30 TmuFeRelayStuckclosed  
Pass DC-30 TmuFeRelayStuckclosed  
Pass DC-30 TmuFeRelayStuckclosed  
Pass DC-30 TmuFeRelayStuckclosed  
Pass DC-30 TmuFeRelayStuckclosed  
Pass DC-30 TmuFeRelayStuckclosed  
Pass DC-30 TmuFeRelayStuckclosed  
Pass DC-30 Ch 4 Force (24 A 13) pin 2 DUT relay stuck closed  
Pass DC-30 Gnd 1 (DC-30, 24 A 35), Ch 4 Force (DC-30, 24 A 13),.000,1.167,1.167,Pass  
Pass DC-30 Gnd 2 (DC-30, 24 A 36), Ch 4 Force (DC-30, 24 A 13),.000,.772,.772,Pass  
Pass DC-30 acc 8 (24 B 30) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 1 Sense (DC-30, 24 A 2), acc 8 (DC-30, 24 B 30),.000,1.077,1.077,Pass  
Pass DC-30 Ch 15 Sense (24 D 26) pin 1 DUT relay stuck closed  
Pass DC-30 acc 2 (24 B 6) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 15 Sense (DC-30, 24 D 26), acc 2 (DC-30, 24 B 6),.000,1.178,1.178,Pass  
Pass DC-30 Ch 20 Sense (24 A 16) pin 1 DUT relay stuck closed  
Pass DC-30 acc 5 (24 B 18) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 20 Sense (DC-30, 24 A 16), acc 5 (DC-30, 24 B 18),.000,2.066,2.066,Pass  
Pass DC-30 Ch 11 Sense (24 D 10) pin 1 DUT relay stuck closed  
Pass DC-30 acc 3 (24 B 10) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 11 Sense (DC-30, 24 D 10), acc 3 (DC-30, 24 B 10),.000,1.862,1.862,Pass  
Pass DC-30 Ch 11 Sense (24 D 10) pin 1 DUT relay stuck closed



Pass DC-30 acc 10 (24 C 6) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 11 Sense (DC-30, 24 D 10), acc 10 (DC-30, 24 C 6),.000,2.035,2.035,Pass  
Pass DC-30 Ch 14 Sense (24 D 22) pin 1 DUT relay stuck closed  
Pass DC-30 acc 6 (24 B 22) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 14 Sense (DC-30, 24 D 22), acc 6 (DC-30, 24 B 22),.000,1.949,1.949,Pass  
Pass DC-30 Ch 1 Sense (24 A 2) pin 1 DUT relay stuck closed  
Pass DC-30 acc 9 (24 C 2) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 1 Sense (DC-30, 24 A 2), acc 9 (DC-30, 24 C 2),.000,.891,.891,Pass  
Pass DC-30 Ch 9 Sense (24 D 2) pin 1 DUT relay stuck closed  
Pass DC-30 acc 1 (24 B 2) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 9 Sense (DC-30, 24 D 2), acc 1 (DC-30, 24 B 2),.000,1.604,1.604,Pass  
Pass DC-30 Ch 12 Sense (24 D 14) pin 1 DUT relay stuck closed  
Pass DC-30 acc 4 (24 B 14) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 12 Sense (DC-30, 24 D 14), acc 4 (DC-30, 24 B 14),.000,1.842,1.842,Pass  
Pass DC-30 Ch 15 Sense (24 D 26) pin 1 DUT relay stuck closed  
Pass DC-30 acc 7 (24 B 26) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 15 Sense (DC-30, 24 D 26), acc 7 (DC-30, 24 B 26),.000,3.118,3.118,Pass  
Pass DC-30 Ch 17 Sense (24 A 4) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 1 Force (24 A 1) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 17 Sense (DC-30, 24 A 4), Ch 1 Force (DC-30, 24 A 1),.000,2.356,2.356,Pass  
Pass DC-30 Ch 10 Sense (24 D 6) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 10 Sense (DC-30, 24 D 6), Ch 1 Force (DC-30, 24 A 1),.000,2.351,2.351,Pass  
Pass DC-30 Ch 3 Sense (24 A 10) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 11 Force (24 D 9) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 3 Sense (DC-30, 24 A 10), Ch 11 Force (DC-30, 24 D 9),.000,2.518,2.518,Pass  
Pass DC-30 Ch 11 Force (24 D 9) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 3 Sense (DC-30, 24 A 10), Ch 11 Force (DC-30, 24 D 9),.000,2.518,2.518,Pass  
Pass DC-30 Ch 13 Sense (24 D 18) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 4 Force (24 A 13) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 13 Sense (DC-30, 24 D 18), Ch 4 Force (DC-30, 24 A 13),.000,2.326,2.326,Pass  
Pass DC-30 Ch 6 Sense (24 A 22) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 14 Force (24 D 21) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 6 Sense (DC-30, 24 A 22), Ch 14 Force (DC-30, 24 D 21),.000,2.521,2.521,Pass  
Pass DC-30 Ch 19 Sense (24 A 12) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 12 Force (24 D 13) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 19 Sense (DC-30, 24 A 12), Ch 12 Force (DC-30, 24 D 13),.000,2.339,2.339,Pass  
Pass DC-30 Ch 20 Force (24 A 15) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 19 Sense (DC-30, 24 A 12), Ch 20 Force (DC-30, 24 A 15),.000,1.612,1.612,Pass  
Pass DC-30 Ch 5 Sense (24 A 18) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 6 Force (24 A 21) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 5 Sense (DC-30, 24 A 18), Ch 6 Force (DC-30, 24 A 21),.000,2.332,2.332,Pass  
Pass DC-30 Ch 1 Sense (24 A 2) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 2 Force (24 A 5) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 1 Sense (DC-30, 24 A 2), Ch 2 Force (DC-30, 24 A 5),.000,.945,.945,Pass  
Pass DC-30 Ch 7 Sense (24 A 26) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 8 Force (24 A 29) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 7 Sense (DC-30, 24 A 26), Ch 8 Force (DC-30, 24 A 29),.000,2.348,2.348,Pass  
Pass DC-30 Ch 12 Sense (24 D 14) pin 1 DUT relay stuck closed

Pass DC-30 Ch 10 Force (24 D 5) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 12 Sense (DC-30, 24 D 14), Ch 10 Force (DC-30, 24 D 5),.000,2.332,2.332,Pass  
Info DC-30 Voltage measurement without connecting measuring pin = -3.55548807419837E-03  
Info DC-30 Voltage measurement before forcing voltage = -3.36810480803251E-03  
Pass DC-30 Ch 14 Sense (24 D 22) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 16 Force (24 D 29) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 14 Sense (DC-30, 24 D 22), Ch 16 Force (DC-30, 24 D 29),.000,1.169,1.169,Pass  
Pass DC-30 Ch 2 Sense (24 A 6) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 3 Force (24 A 9) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 2 Sense (DC-30, 24 A 6), Ch 3 Force (DC-30, 24 A 9),.000,2.323,2.323,Pass  
Pass DC-30 Ch 4 Sense (24 A 14) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 5 Force (24 A 17) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 4 Sense (DC-30, 24 A 14), Ch 5 Force (DC-30, 24 A 17),.000,2.330,2.330,Pass  
Pass DC-30 Ch 18 Sense (24 A 8) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 19 Force (24 A 11) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 18 Sense (DC-30, 24 A 8), Ch 19 Force (DC-30, 24 A 11),.000,1.589,1.589,Pass  
Pass DC-30 Ch 15 Sense (24 D 26) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 13 Force (24 D 17) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 15 Sense (DC-30, 24 D 26), Ch 13 Force (DC-30, 24 D 17),.000,2.331,2.331,Pass  
Pass DC-30 Ch 16 Sense (24 D 30) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 7 Force (24 A 25) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 16 Sense (DC-30, 24 D 30), Ch 7 Force (DC-30, 24 A 25),.000,2.324,2.324,Pass  
Pass DC-30 Ch 3 Sense (DC-30, 24 A 10), Ch 1 Guard (DC-30, 24 B 1),.000,.336,.336,Pass  
Pass DC-30 Ch 12 Sense (DC-30, 24 D 14), Ch 2 Guard (DC-30, 24 B 5),.000,1.309,1.309,Pass  
Pass DC-30 Ch 12 Sense (DC-30, 24 D 14), Ch 3 Guard (DC-30, 24 B 9),.000,.662,.662,Pass  
Pass DC-30 Ch 19 Sense (DC-30, 24 A 12), Ch 4 Guard (DC-30, 24 B 13),.000,1.313,1.313,Pass  
Pass DC-30 Ch 15 Sense (DC-30, 24 D 26), Ch 5 Guard (DC-30, 24 B 17),.000,1.307,1.307,Pass  
Pass DC-30 Ch 17 Sense (DC-30, 24 A 4), Ch 9 Guard (DC-30, 24 C 1),.000,1.307,1.307,Pass  
Pass DC-30 Ch 1 Sense (DC-30, 24 A 2), Ch 10 Guard (DC-30, 24 C 5),.000,.337,.337,Pass  
Pass DC-30 Ch 2 Sense (DC-30, 24 A 6), Ch 11 Guard (DC-30, 24 C 9),.000,1.308,1.308,Pass  
Pass DC-30 Ch 20 Sense (DC-30, 24 A 16), Ch 12 Guard (DC-30, 24 C 13),.000,1.309,1.309,Pass  
Pass DC-30 Ch 18 Sense (DC-30, 24 A 8), Ch 13 Guard (DC-30, 24 C 17),.000,1.307,1.307,Pass  
Pass DC-30 Ch 5 Sense (DC-30, 24 A 18), Ch 14 Guard (DC-30, 24 C 21),.000,1.312,1.312,Pass  
Pass DC-30 Ch 7 Sense (DC-30, 24 A 26), Ch 16 Guard (DC-30, 24 C 29),.000,1.314,1.314,Pass  
Pass DC-30 Ch 12 Sense (DC-30, 24 D 14), Ch 18 Guard (DC-30, 24 B 8),.000,1.309,1.309,Pass  
Pass DC-30 Ch 3 Sense (DC-30, 24 A 10), Ch 19 Guard (DC-30, 24 B 12),.000,1.307,1.307,Pass  
Pass DC-30 Ch 19 Sense (DC-30, 24 A 12), Ch 20 Guard (DC-30, 24 B 16),.000,1.313,1.313,Pass  
Pass DC-30 Ch 16 Sense (DC-30, 24 D 30), Ch 4 Force (DC-30, 24 A 13),.000,1.567,1.567,Pass  
Pass DC-30 Ch 15 Force (24 D 25) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 1 Sense (DC-30, 24 A 2), Ch 15 Force (DC-30, 24 D 25),.000,.775,.775,Pass  
Pass DC-30 Ch 1 Sense (24 A 2) pin 1 DUT relay stuck closed  
Pass DC-30 Ch 9 Force (24 D 1) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 1 Sense (DC-30, 24 A 2), Ch 9 Force (DC-30, 24 D 1),.000,.585,.585,Pass  
Pass DC-30 Ch 17 Force (24 A 3) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 1 Sense (DC-30, 24 A 2), Ch 17 Force (DC-30, 24 A 3),.000,.649,.649,Pass  
Pass DC-30 Ch 17 Force (24 A 3) pin 2 DUT relay stuck closed  
Pass DC-30 Ch 1 Sense (DC-30, 24 A 2), Ch 17 Force (DC-30, 24 A 3),.000,.721,.721,Pass  
Pass DC-30 Ch 1 Sense (DC-30, 24 A 2), Ch 6 Guard (DC-30, 24 B 21),.000,.266,.266,Pass

Pass DC-30 Ch 1 Sense (DC-30, 24 A 2), Ch 7 Guard (DC-30, 24 B 25),.000,.268,.268,Pass  
Pass DC-30 Ch 1 Sense (DC-30, 24 A 2), Ch 8 Guard (DC-30, 24 B 29),.000,.225,.225,Pass  
Pass DC-30 Ch 1 Sense (DC-30, 24 A 2), Ch 15 Guard (DC-30, 24 C 25),.000,.267,.267,Pass  
Pass DC-30 Ch 1 Sense (DC-30, 24 A 2), Ch 17 Guard (DC-30, 24 B 4),.000,.189,.189,Pass  
Pass DC-30 Dig 1 (DC-30, 24 A 38), Ch 4 Force (DC-30, 24 A 13),.000,.239,.239,Pass  
Pass DC-30 Dig 2 (DC-30, 24 B 38), Ch 4 Force (DC-30, 24 A 13),.000,.239,.239,Pass  
Pass DC-30 Gnd 4 (DC-30, 24 B 36), Ch 4 Force (DC-30, 24 A 13),.000,.709,.709,Pass  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI1\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI1\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI1\_TmuFE\_10VHiZ Period  
Pass DC-30 VI1\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI2\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI2\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI2\_TmuFE\_10VHiZ Period  
Pass DC-30 VI2\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI3\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI3\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI3\_TmuFE\_10VHiZ Period  
Pass DC-30 VI3\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI4\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI4\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI4\_TmuFE\_10VHiZ Period  
Pass DC-30 VI4\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI5\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI5\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI5\_TmuFE\_10VHiZ Period  
Pass DC-30 VI5\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI6\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI6\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI6\_TmuFE\_10VHiZ Period  
Pass DC-30 VI6\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI7\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI7\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI7\_TmuFE\_10VHiZ Period  
Pass DC-30 VI7\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI8\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI8\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI8\_TmuFE\_10VHiZ Period  
Pass DC-30 VI8\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp

Pass DC-30 VI9\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI9\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI9\_TmuFE\_10VHiZ Period  
Pass DC-30 VI9\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI10\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI10\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI10\_TmuFE\_10VHiZ Period  
Pass DC-30 VI10\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI11\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI11\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI11\_TmuFE\_10VHiZ Period  
Pass DC-30 VI11\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI12\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI12\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI12\_TmuFE\_10VHiZ Period  
Pass DC-30 VI12\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI13\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI13\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI13\_TmuFE\_10VHiZ Period  
Pass DC-30 VI13\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI14\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI14\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI14\_TmuFE\_10VHiZ Period  
Pass DC-30 VI14\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI15\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI15\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI15\_TmuFE\_10VHiZ Period  
Pass DC-30 VI15\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI16\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI16\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI16\_TmuFE\_10VHiZ Period  
Pass DC-30 VI16\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI17\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI17\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI17\_TmuFE\_10VHiZ Period  
Pass DC-30 VI17\_TmuFE\_10VHiZ Period  
Pass DC-30 50 Ohm Imp  
Pass DC-30 VI18\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI18\_TmuFE\_10V50Ohm Period  
Pass DC-30 VI18\_TmuFE\_10VHiZ Period

Pass DC-30 VI18\_TmuFE\_10VHiZ Period  
 Pass DC-30 50 Ohm Imp  
 Pass DC-30 VI19\_TmuFE\_10V50Ohm Period  
 Pass DC-30 VI19\_TmuFE\_10V50Ohm Period  
 Pass DC-30 VI19\_TmuFE\_10VHiZ Period  
 Pass DC-30 VI19\_TmuFE\_10VHiZ Period  
 Pass DC-30 50 Ohm Imp  
 Pass DC-30 VI20\_TmuFE\_10V50Ohm Period  
 Pass DC-30 VI20\_TmuFE\_10V50Ohm Period  
 Pass DC-30 VI20\_TmuFE\_10VHiZ Period  
 Pass DC-30 VI20\_TmuFE\_10VHiZ Period  
 Pass DC-30 TmuFeRelayStuckclosed  
 Pass DC-30 TmuFeRelayStuckclosed  
 Pass DC-30 TmuFeRelayStuckclosed  
 Pass DC-30 TmuFeRelayStuckclosed  
 Pass DC-30 TmuFeRelayStuckclosed  
 Pass DC-30 TmuFeRelayStuckclosed  
 Pass DC-30 TmuFeRelayStuckclosed  
 Pass DC-30 TmuFeRelayStuckclosed  
 Pass DC-30 TmuFeRelayStuckclosed  
 Pass DC-30 TmuFeRelayStuckclosed  
 Pass DC-30 TmuFeRelayStuckclosed  
 Pass DC-30 TmuFeRelayStuckclosed  
 Pass DC-30 TmuFeRelayStuckclosed  
 Pass DC-30 TmuFeRelayStuckclosed  
 Pass DC-30 TmuFeRelayStuckclosed  
 Pass DC-30 TmuFeRelayStuckclosed  
 Pass DC-30 TmuFeRelayStuckclosed  
 Pass DC-30 TmuFeRelayStuckclosed  
 Pass DC-30 TmuFeRelayStuckclosed  
 Info DC-30 Test finished  
 Info DC-90 Beginning DC-90 continuity test  
 Info DC-90 Testing DC90 in Slot# 2  
 Pass DC-90 DIB\_Slot Pin: 3 B 2 Or 3 A 34 Open  
 Pass DC-90 VI\_1 DIB\_Access High K214\_1 Stuck Closed  
 Pass DC-90 DIB\_Slot Pin: 3 B 10 Or 3 A 34 Open  
 Pass DC-90 VI\_2 DIB\_Access High K214\_2 Stuck Closed  
 Pass DC-90 DIB\_Slot Pin: 3 B 18 Or 3 B 34 Open  
 Pass DC-90 VI\_3 DIB\_Access High K214\_3 Stuck Closed  
 Pass DC-90 DIB\_Slot Pin: 3 B 26 Or 3 B 34 Open  
 Pass DC-90 VI\_4 DIB\_Access High K214\_4 Stuck Closed  
 Pass DC-90 DIB\_Slot Pin: 3 B 6 Or 3 A 34 Open  
 Pass DC-90 VI\_1 DIB\_Access High K213\_1 Stuck Closed  
 Pass DC-90 DIB\_Slot Pin: 3 B 14 Or 3 A 34 Open  
 Pass DC-90 VI\_2 DIB\_Access High K213\_2 Stuck Closed  
 Pass DC-90 DIB\_Slot Pin: 3 B 18 Or 3 B 34 Open  
 Pass DC-90 VI\_3 DIB\_Access High K213\_3 Stuck Closed

Pass DC-90 DIB\_Slot Pin: 3 B 30 Or 3 B 34 Open  
Pass DC-90 VI\_4 DIB\_Access High K213\_4 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 3 C 2 Or 3 B 3 Open  
Pass DC-90 VI\_1 DIB\_Access Low K215\_1 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 3 C 10 Or 3 B 11 Open  
Pass DC-90 VI\_2 DIB\_Access Low K215\_2 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 3 C 18 Or 3 B 19 Open  
Pass DC-90 VI\_3 DIB\_Access Low K215\_3 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 3 C 26 Or 3 B 27 Open  
Pass DC-90 VI\_4 DIB\_Access Low K215\_4 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 3 C 6 Or 3 B 7 Open  
Pass DC-90 VI\_1 DIB\_Access Low K13\_1 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 3 C 14 Or 3 B 15 Open  
Pass DC-90 VI\_2 DIB\_Access Low K13\_2 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 3 C 22 Or 3 B 23 Open  
Pass DC-90 VI\_3 DIB\_Access Low K13\_3 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 3 C 30 Or 3 B 31 Open  
Pass DC-90 VI\_4 DIB\_Access Low K13\_4 Stuck Closed  
Pass DC-90 HS VI-1, DIB\_Slot Pin: 3 C 2 Or K211\_1 Stuck Open  
Pass DC-90 HS VI\_1 , K211\_1 Stuck Closed  
Pass DC-90 VI\_1 High Force K29\_1 Stuck Closed  
Pass DC-90 HF VI-1, DIB\_Slot Pin: 3 B 1 Or K29\_1 Stuck Open  
Pass DC-90 VI\_1 High Force K31\_1 Stuck Closed  
Pass DC-90 HF VI-1, DIB\_Slot Pin: 3 A 3 Or K31\_1 Stuck Open  
Pass DC-90 VI\_1 High Force K34\_1 Stuck Closed  
Pass DC-90 HF VI-1, DIB\_Slot Pin: 3 A 4 Or K34\_1 Stuck Open  
Pass DC-90 VI\_1 High Force K36\_1 Stuck Closed  
Pass DC-90 HF VI-1, DIB\_Slot Pin: 3 C 1 Or K36\_1 Stuck Open  
Pass DC-90 VI\_1 High Force K38\_1 Stuck Closed  
Pass DC-90 HF VI-1, DIB\_Slot Pin: 3 C 4 Or K38\_1 Stuck Open  
Pass DC-90 LS VI-1, DIB\_Slot Pin: 3 C 3 Or K216\_1 Stuck Open  
Pass DC-90 LS VI-1 , K216\_1 Stuck Close  
Pass DC-90 VI\_1 Low Force K44\_1 Stuck Closed  
Pass DC-90 LF.1 DIB\_Slot Pin: 3 C 2 Or K44\_1 Stuck Open  
Pass DC-90 VI\_1 Low Force K46\_1 Stuck Closed  
Pass DC-90 LF.1 DIB\_Slot Pin: 3 C 2 Or K46\_1 Stuck Open  
Pass DC-90 VI\_1 Low Force K202\_1 Stuck Closed  
Pass DC-90 LF.1 DIB\_Slot Pin: 3 C 2 Or K202\_1 Stuck Open  
Pass DC-90 VI\_1 Low Force K50\_1 Stuck Closed  
Pass DC-90 LF.1 DIB\_Slot Pin: 3 C 2 Or K50\_1 Stuck Open  
Pass DC-90 VI\_1 Low Force K52\_1 Stuck Closed  
Pass DC-90 LF.1 DIB\_Slot Pin: 3 C 2 Or K52\_1 Stuck Open  
Pass DC-90 HS VI-1, DIB\_Slot Pin: 3 C 6 Or K212\_1 Stuck Open  
Pass DC-90 HS VI\_1 , K212\_1 Stuck Closed  
Pass DC-90 VI\_1 High Force K201\_1 Stuck Closed  
Pass DC-90 HF VI-1, DIB\_Slot Pin: 3 B 5 Or K31\_1 Stuck Open  
Pass DC-90 VI\_1 High Force K32\_1 Stuck Closed  
Pass DC-90 HF VI-1, DIB\_Slot Pin: 3 A 7 Or K32\_1 Stuck Open

Pass DC-90 VI\_1 High Force K35\_1 Stuck Closed  
Pass DC-90 HF VI-1, DIB\_Slot Pin: 3 A 8 Or K35\_1 Stuck Open  
Pass DC-90 VI\_1 High Force K37\_1 Stuck Closed  
Pass DC-90 HF VI-1, DIB\_Slot Pin: 3 C 5 Or K37\_1 Stuck Open  
Pass DC-90 VI\_1 High Force K39\_1 Stuck Closed  
Pass DC-90 HF VI-1, DIB\_Slot Pin: 3 D 3 Or K39\_1 Stuck Open  
Pass DC-90 LS VI-1, DIB\_Slot Pin: 3 C 7 Or K19\_1 Stuck Open  
Pass DC-90 LS VI-1 , K19\_1 Stuck Close  
Pass DC-90 VI\_1 Low Force K45\_1 Stuck Closed  
Pass DC-90 LF.1 DIB\_Slot Pin: 3 C 6 Or K45\_1 Stuck Open  
Pass DC-90 VI\_1 Low Force K47\_1 Stuck Closed  
Pass DC-90 LF.1 DIB\_Slot Pin: 3 C 6 Or K47\_1 Stuck Open  
Pass DC-90 VI\_1 Low Force K49\_1 Stuck Closed  
Pass DC-90 LF.1 DIB\_Slot Pin: 3 C 6 Or K49\_1 Stuck Open  
Pass DC-90 VI\_1 Low Force K51\_1 Stuck Closed  
Pass DC-90 LF.1 DIB\_Slot Pin: 3 C 6 Or K51\_1 Stuck Open  
Pass DC-90 VI\_1 Low Force K53\_1 Stuck Closed  
Pass DC-90 LF.1 DIB\_Slot Pin: 3 C 6 Or K53\_1 Stuck Open  
Pass DC-90 HS VI-2, DIB\_Slot Pin: 3 C 10 Or K211\_2 Stuck Open  
Pass DC-90 HS VI\_2 , K211\_2 Stuck Closed  
Pass DC-90 VI\_2 High Force K29\_2 Stuck Closed  
Pass DC-90 HF VI-2, DIB\_Slot Pin: 3 B 9 Or K29\_2 Stuck Open  
Pass DC-90 VI\_2 High Force K31\_2 Stuck Closed  
Pass DC-90 HF VI-2, DIB\_Slot Pin: 3 A 11 Or K31\_2 Stuck Open  
Pass DC-90 VI\_2 High Force K34\_2 Stuck Closed  
Pass DC-90 HF VI-2, DIB\_Slot Pin: 3 A 12 Or K34\_2 Stuck Open  
Pass DC-90 VI\_2 High Force K36\_2 Stuck Closed  
Pass DC-90 HF VI-2, DIB\_Slot Pin: 3 C 9 Or K36\_2 Stuck Open  
Pass DC-90 VI\_2 High Force K38\_2 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 3 C 19 Or K38\_2 Stuck Open  
Pass DC-90 LS VI-2, DIB\_Slot Pin: 3 C 11 Or K216\_2 Stuck Open  
Pass DC-90 LS VI-2 , K216\_2 Stuck Close  
Pass DC-90 VI\_2 Low Force K44\_2 Stuck Closed  
Pass DC-90 LF.2 DIB\_Slot Pin: 3 C 10 Or K44\_2 Stuck Open  
Pass DC-90 VI\_2 Low Force K46\_2 Stuck Closed  
Pass DC-90 LF.2 DIB\_Slot Pin: 3 C 10 Or K46\_2 Stuck Open  
Pass DC-90 VI\_2 Low Force K202\_2 Stuck Closed  
Pass DC-90 LF.2 DIB\_Slot Pin: 3 C 10 Or K202\_2 Stuck Open  
Pass DC-90 VI\_2 Low Force K50\_2 Stuck Closed  
Pass DC-90 LF.2 DIB\_Slot Pin: 3 C 10 Or K50\_2 Stuck Open  
Pass DC-90 VI\_2 Low Force K52\_2 Stuck Closed  
Pass DC-90 LF.2 DIB\_Slot Pin: 3 C 10 Or K52\_2 Stuck Open  
Pass DC-90 HS VI-2, DIB\_Slot Pin: 3 C 14 Or K212\_2 Stuck Open  
Pass DC-90 HS VI\_2 , K212\_2 Stuck Closed  
Pass DC-90 VI\_2 High Force K201\_2 Stuck Closed  
Pass DC-90 HF VI-2, DIB\_Slot Pin: 3 B 13 Or K2201\_2 Stuck Open  
Pass DC-90 VI\_2 High Force K32\_2 Stuck Closed  
Pass DC-90 HF VI-2, DIB\_Slot Pin: 3 A 15 Or K32\_2 Stuck Open

Pass DC-90 VI\_2 High Force K35\_2 Stuck Closed  
Pass DC-90 HF VI-2, DIB\_Slot Pin: 3 A 16 Or K35\_2 Stuck Open  
Pass DC-90 VI\_2 High Force K37\_2 Stuck Closed  
Pass DC-90 HF VI-2, DIB\_Slot Pin: 3 C 13 Or K37\_2 Stuck Open  
Pass DC-90 VI\_2 High Force K39\_2 Stuck Closed  
Pass DC-90 HF VI-2, DIB\_Slot Pin: 3 D 11 Or K39\_2 Stuck Open  
Pass DC-90 LS VI-2, DIB\_Slot Pin: 3 C 15 Or K19\_2 Stuck Open  
Pass DC-90 LS VI-2 , K19\_2 Stuck Close  
Pass DC-90 VI\_2 Low Force K45\_2 Stuck Closed  
Pass DC-90 LF.2 DIB\_Slot Pin: 3 C 14 Or K45\_2 Stuck Open  
Pass DC-90 VI\_2 Low Force K47\_2 Stuck Closed  
Pass DC-90 LF.2 DIB\_Slot Pin: 3 C 14 Or K47\_2 Stuck Open  
Pass DC-90 VI\_2 Low Force K49\_2 Stuck Closed  
Pass DC-90 LF.2 DIB\_Slot Pin: 3 C 14 Or K49\_2 Stuck Open  
Pass DC-90 VI\_2 Low Force K51\_2 Stuck Closed  
Pass DC-90 LF.2 DIB\_Slot Pin: 3 C 14 Or K51\_2 Stuck Open  
Pass DC-90 VI\_2 Low Force K53\_2 Stuck Closed  
Pass DC-90 LF.2 DIB\_Slot Pin: 3 C 14 Or K53\_2 Stuck Open  
Pass DC-90 HS VI-3, DIB\_Slot Pin: 3 C 18 Or K211\_3 Stuck Open  
Pass DC-90 HS VI\_3 , K211\_3 Stuck Closed  
Pass DC-90 VI\_3 High Force K29\_3 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 3 B 17 Or K29\_3 Stuck Open  
Pass DC-90 VI\_3 High Force K31\_3 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 3 A 19 Or K31\_3 Stuck Open  
Pass DC-90 VI\_3 High Force K34\_3 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 3 A 20 Or K34\_3 Stuck Open  
Pass DC-90 VI\_3 High Force K36\_3 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 3 C 17 Or K36\_3 Stuck Open  
Pass DC-90 VI\_3 High Force K38\_3 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 3 C 20 Or K38\_3 Stuck Open  
Pass DC-90 LS VI-3, DIB\_Slot Pin: 3 C 19 Or K216\_3 Stuck Open  
Pass DC-90 LS VI-3 , K216\_3 Stuck Close  
Pass DC-90 VI\_3 Low Force K44\_3 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 3 C 18 Or K44\_3 Stuck Open  
Pass DC-90 VI\_3 Low Force K46\_3 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 3 C 18 Or K46\_3 Stuck Open  
Pass DC-90 VI\_3 Low Force K202\_3 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 3 C 18 Or K202\_3 Stuck Open  
Pass DC-90 VI\_3 Low Force K50\_3 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 3 C 18 Or K50\_3 Stuck Open  
Pass DC-90 VI\_3 Low Force K52\_3 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 3 C 18 Or K52\_3 Stuck Open  
Pass DC-90 HS VI-3, DIB\_Slot Pin: 3 C 22 Or K212\_3 Stuck Open  
Pass DC-90 HS VI\_3 , K212\_3 Stuck Closed  
Pass DC-90 VI\_3 High Force K201\_3 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 3 B 21 Or K201\_3 Stuck Open  
Pass DC-90 VI\_3 High Force K32\_3 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 3 A 23 Or K32\_3 Stuck Open



Pass DC-90 VI\_3 High Force K35\_3 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 3 A 24 Or K35\_3 Stuck Open  
Pass DC-90 VI\_3 High Force K37\_3 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 3 C 21 Or K37\_3 Stuck Open  
Pass DC-90 VI\_3 High Force K39\_3 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 3 D 19 Or K39\_3 Stuck Open  
Pass DC-90 LS VI-3, DIB\_Slot Pin: 3 C 23 Or K19\_3 Stuck Open  
Pass DC-90 LS VI-3 , K19\_3 Stuck Close  
Pass DC-90 VI\_3 Low Force K45\_3 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 3 C 22 Or K45\_3 Stuck Open  
Pass DC-90 VI\_3 Low Force K47\_3 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 3 C 22 Or K47\_3 Stuck Open  
Pass DC-90 VI\_3 Low Force K49\_3 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 3 C 22 Or K49\_3 Stuck Open  
Pass DC-90 VI\_3 Low Force K51\_3 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 3 C 22 Or K51\_3 Stuck Open  
Pass DC-90 VI\_3 Low Force K53\_3 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 3 C 22 Or K53\_3 Stuck Open  
Pass DC-90 HS VI-4, DIB\_Slot Pin: 3 C 26 Or K211\_4 Stuck Open  
Pass DC-90 HS VI\_4 , K211\_4 Stuck Closed  
Pass DC-90 VI\_4 High Force K29\_4 Stuck Closed  
Pass DC-90 HF VI-4, DIB\_Slot Pin: 3 B 25 Or K29\_4 Stuck Open  
Pass DC-90 VI\_4 High Force K31\_4 Stuck Closed  
Pass DC-90 HF VI-4, DIB\_Slot Pin: 3 A 27 Or K31\_4 Stuck Open  
Pass DC-90 VI\_4 High Force K34\_4 Stuck Closed  
Pass DC-90 HF VI-4, DIB\_Slot Pin: 3 A 28 Or K34\_4 Stuck Open  
Pass DC-90 VI\_4 High Force K36\_4 Stuck Closed  
Pass DC-90 HF VI-4, DIB\_Slot Pin: 3 C 25 Or K36\_4 Stuck Open  
Pass DC-90 VI\_4 High Force K38\_4 Stuck Closed  
Pass DC-90 HF VI-4, DIB\_Slot Pin: 3 C 28 Or K38\_4 Stuck Open  
Pass DC-90 LS VI-4, DIB\_Slot Pin: 3 C 27 Or K216\_4 Stuck Open  
Pass DC-90 LS VI-4 , K216\_4 Stuck Close  
Pass DC-90 VI\_4 Low Force K44\_4 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 3 C 26 Or K44\_4 Stuck Open  
Pass DC-90 VI\_4 Low Force K46\_4 Stuck Closed  
Pass DC-90 LF.4 DIB\_Slot Pin: 3 C 26 Or K46\_4 Stuck Open  
Pass DC-90 VI\_4 Low Force K202\_4 Stuck Closed  
Pass DC-90 LF.4 DIB\_Slot Pin: 3 C 26 Or K202\_4 Stuck Open  
Pass DC-90 VI\_4 Low Force K50\_4 Stuck Closed  
Pass DC-90 LF.4 DIB\_Slot Pin: 3 C 26 Or K50\_4 Stuck Open  
Pass DC-90 VI\_4 Low Force K52\_4 Stuck Closed  
Pass DC-90 LF.4 DIB\_Slot Pin: 3 C 26 Or K52\_4 Stuck Open  
Pass DC-90 HS VI-4, DIB\_Slot Pin: 3 C 30 Or K212\_4 Stuck Open  
Pass DC-90 HS VI\_4 , K212\_4 Stuck Closed  
Pass DC-90 VI\_4 High Force K201\_4 Stuck Closed  
Pass DC-90 HF VI-4, DIB\_Slot Pin: 3 B 29 Or K201\_4 Stuck Open  
Pass DC-90 VI\_4 High Force K32\_4 Stuck Closed  
Pass DC-90 HF VI-4, DIB\_Slot Pin: 3 A 31 Or K32\_4 Stuck Open

Pass DC-90 VI\_4 High Force K35\_4 Stuck Closed  
Pass DC-90 HF VI-4, DIB\_Slot Pin: 3 A 32 Or K35\_4 Stuck Open  
Pass DC-90 VI\_4 High Force K37\_4 Stuck Closed  
Pass DC-90 HF VI-4, DIB\_Slot Pin: 3 C 29 Or K37\_4 Stuck Open  
Pass DC-90 VI\_4 High Force K39\_4 Stuck Closed  
Pass DC-90 HF VI-4, DIB\_Slot Pin: 3 D 27 Or K39\_4 Stuck Open  
Pass DC-90 LS VI-4, DIB\_Slot Pin: 3 C 31 Or K19\_4 Stuck Open  
Pass DC-90 LS VI-4 , K19\_4 Stuck Close  
Pass DC-90 VI\_4 Low Force K45\_4 Stuck Closed  
Pass DC-90 LF.4 DIB\_Slot Pin: 3 C 30 Or K45\_4 Stuck Open  
Pass DC-90 VI\_4 Low Force K47\_4 Stuck Closed  
Pass DC-90 LF.4 DIB\_Slot Pin: 3 C 30 Or K47\_4 Stuck Open  
Pass DC-90 VI\_4 Low Force K49\_4 Stuck Closed  
Pass DC-90 LF.4 DIB\_Slot Pin: 3 C 30 Or K49\_4 Stuck Open  
Pass DC-90 VI\_4 Low Force K51\_4 Stuck Closed  
Pass DC-90 LF.4 DIB\_Slot Pin: 3 C 30 Or K51\_4 Stuck Open  
Pass DC-90 VI\_4 Low Force K53\_4 Stuck Closed  
Pass DC-90 LF.4 DIB\_Slot Pin: 3 C 30 Or K53\_4 Stuck Open  
Pass DC-90 DIB\_PIN DGS 1 sh Closed using pinHF2A.1  
Pass DC-90 DIB\_PIN DGS 2 sh Closed using pinHF2A.1  
Pass DC-90 DIB\_PIN TMU1\_sh Closed using pinHF2A.1  
Pass DC-90 DIB\_PIN TMU2\_sh Closed using pinHF2A.1  
Pass DC-90 DIB\_PIN XOR1\_sh Closed using pinHF2A.1  
Pass DC-90 DIB\_PIN XOR2\_sh Closed using pinHF2A.1  
Pass DC-90 DIB\_PIN HF2A.1 Or TMU1 Open  
Pass DC-90 DIB\_PIN HF2A.1 Or TMU1 Closed  
Pass DC-90 DIB\_PIN HF2A.1 Or TMU2 Open  
Pass DC-90 DIB\_PIN HF2A.1 Or TMU2 Closed  
Pass DC-90 DIB\_PIN HF2A.1 Or XOR1 Closed  
Pass DC-90 DIB\_PIN HF2A.1 Or XOR2 Closed  
Pass DC-90 DIB\_PIN HF2A.1 Or DGS 1 Closed  
Pass DC-90 DIB\_PIN HF3A.1 Or DGS 2 Closed  
Pass DC-90 S3DC90\_PinHS1A to TMU1 HighSide, K199\_1  
Pass DC-90 S3DC90\_PinHS2A to TMU1 HighSide, K199\_2  
Pass DC-90 S3DC90\_PinHS3A to TMU2 HighSide, K199\_3  
Pass DC-90 S3DC90\_PinHS4A to TMU2 HighSide, K199\_4  
Pass DC-90 S3DC90\_PinHS1B to TMU1 HighSide, K124\_1  
Pass DC-90 S3DC90\_PinHS2B to TMU1 HighSide, K124\_2  
Pass DC-90 S3DC90\_PinHS3B to TMU2 HighSide, K124\_3  
Pass DC-90 S3DC90\_PinHS4B to TMU2 HighSide, K124\_4  
Pass DC-90 S3DC90\_PinLS1A to TMU1 LowSide, K57\_1  
Pass DC-90 S3DC90\_PinLS2A to TMU1 LowSide, K57\_2  
Pass DC-90 S3DC90\_PinLS3A to TMU2 LowSide, K57\_3  
Pass DC-90 S3DC90\_PinLS4A to TMU2 LowSide, K57\_4  
Pass DC-90 S3DC90\_PinLS1B to TMU1 LowSide, K58\_1  
Pass DC-90 S3DC90\_PinLS2B to TMU1 LowSide, K58\_2  
Pass DC-90 S3DC90\_PinLS3B to TMU2 LowSide, K58\_3  
Pass DC-90 S3DC90\_PinLS4B to TMU2 LowSide, K58\_4

Info DC-90 Testing DC90 in Slot# 10  
Pass DC-90 DIB\_Slot Pin: 21 B 2 Or 21 A 34 Open  
Pass DC-90 VI\_1 DIB\_Access High K214\_1 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 21 B 10 Or 21 A 34 Open  
Pass DC-90 VI\_2 DIB\_Access High K214\_2 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 21 B 18 Or 21 B 34 Open  
Pass DC-90 VI\_3 DIB\_Access High K214\_3 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 21 B 26 Or 21 B 34 Open  
Pass DC-90 VI\_4 DIB\_Access High K214\_4 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 21 B 6 Or 21 A 34 Open  
Pass DC-90 VI\_1 DIB\_Access High K213\_1 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 21 B 14 Or 21 A 34 Open  
Pass DC-90 VI\_2 DIB\_Access High K213\_2 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 21 B 18 Or 21 B 34 Open  
Pass DC-90 VI\_3 DIB\_Access High K213\_3 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 21 B 30 Or 21 B 34 Open  
Pass DC-90 VI\_4 DIB\_Access High K213\_4 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 21 C 2 Or 21 B 3 Open  
Pass DC-90 VI\_1 DIB\_Access Low K215\_1 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 21 C 10 Or 21 B 11 Open  
Pass DC-90 VI\_2 DIB\_Access Low K215\_2 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 21 C 18 Or 21 B 19 Open  
Pass DC-90 VI\_3 DIB\_Access Low K215\_3 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 21 C 26 Or 21 B 27 Open  
Pass DC-90 VI\_4 DIB\_Access Low K215\_4 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 21 C 6 Or 21 B 7 Open  
Pass DC-90 VI\_1 DIB\_Access Low K13\_1 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 21 C 14 Or 21 B 15 Open  
Pass DC-90 VI\_2 DIB\_Access Low K13\_2 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 21 C 22 Or 21 B 23 Open  
Pass DC-90 VI\_3 DIB\_Access Low K13\_3 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 21 C 30 Or 21 B 31 Open  
Pass DC-90 VI\_4 DIB\_Access Low K13\_4 Stuck Closed  
Pass DC-90 HS VI-1, DIB\_Slot Pin: 21 C 2 Or K211\_1 Stuck Open  
Pass DC-90 HS VI\_1 , K211\_1 Stuck Closed  
Pass DC-90 VI\_1 High Force K29\_1 Stuck Closed  
Pass DC-90 HF VI-1, DIB\_Slot Pin: 21 B 1 Or K29\_1 Stuck Open  
Pass DC-90 VI\_1 High Force K31\_1 Stuck Closed  
Pass DC-90 HF VI-1, DIB\_Slot Pin: 21 A 3 Or K31\_1 Stuck Open  
Pass DC-90 VI\_1 High Force K34\_1 Stuck Closed  
Pass DC-90 HF VI-1, DIB\_Slot Pin: 21 A 4 Or K34\_1 Stuck Open  
Pass DC-90 VI\_1 High Force K36\_1 Stuck Closed  
Pass DC-90 HF VI-1, DIB\_Slot Pin: 21 C 1 Or K36\_1 Stuck Open  
Pass DC-90 VI\_1 High Force K38\_1 Stuck Closed  
Pass DC-90 HF VI-1, DIB\_Slot Pin: 21 C 4 Or K38\_1 Stuck Open  
Pass DC-90 LS VI-1, DIB\_Slot Pin: 21 C 3 Or K216\_1 Stuck Open  
Pass DC-90 LS VI-1 , K216\_1 Stuck Close  
Pass DC-90 VI\_1 Low Force K44\_1 Stuck Closed

Pass DC-90 LF.1 DIB\_Slot Pin: 21 C 2 Or K44\_1 Stuck Open  
Pass DC-90 VI\_1 Low Force K46\_1 Stuck Closed  
Pass DC-90 LF.1 DIB\_Slot Pin: 21 C 2 Or K46\_1 Stuck Open  
Pass DC-90 VI\_1 Low Force K202\_1 Stuck Closed  
Pass DC-90 LF.1 DIB\_Slot Pin: 21 C 2 Or K202\_1 Stuck Open  
Pass DC-90 VI\_1 Low Force K50\_1 Stuck Closed  
Pass DC-90 LF.1 DIB\_Slot Pin: 21 C 2 Or K50\_1 Stuck Open  
Pass DC-90 VI\_1 Low Force K52\_1 Stuck Closed  
Pass DC-90 LF.1 DIB\_Slot Pin: 21 C 2 Or K52\_1 Stuck Open  
Pass DC-90 HS VI-1, DIB\_Slot Pin: 21 C 6 Or K212\_1 Stuck Open  
Pass DC-90 HS VI\_1 , K212\_1 Stuck Closed  
Pass DC-90 VI\_1 High Force K201\_1 Stuck Closed  
Pass DC-90 HF VI-1, DIB\_Slot Pin: 21 B 5 Or K31\_1 Stuck Open  
Pass DC-90 VI\_1 High Force K32\_1 Stuck Closed  
Pass DC-90 HF VI-1, DIB\_Slot Pin: 21 A 7 Or K32\_1 Stuck Open  
Pass DC-90 VI\_1 High Force K35\_1 Stuck Closed  
Pass DC-90 HF VI-1, DIB\_Slot Pin: 21 A 8 Or K35\_1 Stuck Open  
Pass DC-90 VI\_1 High Force K37\_1 Stuck Closed  
Pass DC-90 HF VI-1, DIB\_Slot Pin: 21 C 5 Or K37\_1 Stuck Open  
Pass DC-90 VI\_1 High Force K39\_1 Stuck Closed  
Pass DC-90 HF VI-1, DIB\_Slot Pin: 21 D 3 Or K39\_1 Stuck Open  
Pass DC-90 LS VI-1, DIB\_Slot Pin: 21 C 7 Or K19\_1 Stuck Open  
Pass DC-90 LS VI-1 , K19\_1 Stuck Close  
Pass DC-90 VI\_1 Low Force K45\_1 Stuck Closed  
Pass DC-90 LF.1 DIB\_Slot Pin: 21 C 6 Or K45\_1 Stuck Open  
Pass DC-90 VI\_1 Low Force K47\_1 Stuck Closed  
Pass DC-90 LF.1 DIB\_Slot Pin: 21 C 6 Or K47\_1 Stuck Open  
Pass DC-90 VI\_1 Low Force K49\_1 Stuck Closed  
Pass DC-90 LF.1 DIB\_Slot Pin: 21 C 6 Or K49\_1 Stuck Open  
Pass DC-90 VI\_1 Low Force K51\_1 Stuck Closed  
Pass DC-90 LF.1 DIB\_Slot Pin: 21 C 6 Or K51\_1 Stuck Open  
Pass DC-90 VI\_1 Low Force K53\_1 Stuck Closed  
Pass DC-90 LF.1 DIB\_Slot Pin: 21 C 6 Or K53\_1 Stuck Open  
Pass DC-90 HS VI-2, DIB\_Slot Pin: 21 C 10 Or K211\_2 Stuck Open  
Pass DC-90 HS VI\_2 , K211\_2 Stuck Closed  
Pass DC-90 VI\_2 High Force K29\_2 Stuck Closed  
Pass DC-90 HF VI-2, DIB\_Slot Pin: 21 B 9 Or K29\_2 Stuck Open  
Pass DC-90 VI\_2 High Force K31\_2 Stuck Closed  
Pass DC-90 HF VI-2, DIB\_Slot Pin: 21 A 11 Or K31\_2 Stuck Open  
Pass DC-90 VI\_2 High Force K34\_2 Stuck Closed  
Pass DC-90 HF VI-2, DIB\_Slot Pin: 21 A 12 Or K34\_2 Stuck Open  
Pass DC-90 VI\_2 High Force K36\_2 Stuck Closed  
Pass DC-90 HF VI-2, DIB\_Slot Pin: 21 C 9 Or K36\_2 Stuck Open  
Pass DC-90 VI\_2 High Force K38\_2 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 21 C 19 Or K38\_2 Stuck Open  
Pass DC-90 LS VI-2, DIB\_Slot Pin: 21 C 11 Or K216\_2 Stuck Open  
Pass DC-90 LS VI-2 , K216\_2 Stuck Close  
Pass DC-90 VI\_2 Low Force K44\_2 Stuck Closed

Pass DC-90 LF.2 DIB\_Slot Pin: 21 C 10 Or K44\_2 Stuck Open  
Pass DC-90 VI\_2 Low Force K46\_2 Stuck Closed  
Pass DC-90 LF.2 DIB\_Slot Pin: 21 C 10 Or K46\_2 Stuck Open  
Pass DC-90 VI\_2 Low Force K202\_2 Stuck Closed  
Pass DC-90 LF.2 DIB\_Slot Pin: 21 C 10 Or K202\_2 Stuck Open  
Pass DC-90 VI\_2 Low Force K50\_2 Stuck Closed  
Pass DC-90 LF.2 DIB\_Slot Pin: 21 C 10 Or K50\_2 Stuck Open  
Pass DC-90 VI\_2 Low Force K52\_2 Stuck Closed  
Pass DC-90 LF.2 DIB\_Slot Pin: 21 C 10 Or K52\_2 Stuck Open  
Pass DC-90 HS VI-2, DIB\_Slot Pin: 21 C 14 Or K212\_2 Stuck Open  
Pass DC-90 HS VI\_2 , K212\_2 Stuck Closed  
Pass DC-90 VI\_2 High Force K201\_2 Stuck Closed  
Pass DC-90 HF VI-2, DIB\_Slot Pin: 21 B 13 Or K2201\_2 Stuck Open  
Pass DC-90 VI\_2 High Force K32\_2 Stuck Closed  
Pass DC-90 HF VI-2, DIB\_Slot Pin: 21 A 15 Or K32\_2 Stuck Open  
Pass DC-90 VI\_2 High Force K35\_2 Stuck Closed  
Pass DC-90 HF VI-2, DIB\_Slot Pin: 21 A 16 Or K35\_2 Stuck Open  
Pass DC-90 VI\_2 High Force K37\_2 Stuck Closed  
Pass DC-90 HF VI-2, DIB\_Slot Pin: 21 C 13 Or K37\_2 Stuck Open  
Pass DC-90 VI\_2 High Force K39\_2 Stuck Closed  
Pass DC-90 HF VI-2, DIB\_Slot Pin: 21 D 11 Or K39\_2 Stuck Open  
Pass DC-90 LS VI-2, DIB\_Slot Pin: 21 C 15 Or K19\_2 Stuck Open  
Pass DC-90 LS VI-2 , K19\_2 Stuck Close  
Pass DC-90 VI\_2 Low Force K45\_2 Stuck Closed  
Pass DC-90 LF.2 DIB\_Slot Pin: 21 C 14 Or K45\_2 Stuck Open  
Pass DC-90 VI\_2 Low Force K47\_2 Stuck Closed  
Pass DC-90 LF.2 DIB\_Slot Pin: 21 C 14 Or K47\_2 Stuck Open  
Pass DC-90 VI\_2 Low Force K49\_2 Stuck Closed  
Pass DC-90 LF.2 DIB\_Slot Pin: 21 C 14 Or K49\_2 Stuck Open  
Pass DC-90 VI\_2 Low Force K51\_2 Stuck Closed  
Pass DC-90 LF.2 DIB\_Slot Pin: 21 C 14 Or K51\_2 Stuck Open  
Pass DC-90 VI\_2 Low Force K53\_2 Stuck Closed  
Pass DC-90 LF.2 DIB\_Slot Pin: 21 C 14 Or K53\_2 Stuck Open  
Pass DC-90 HS VI-3, DIB\_Slot Pin: 21 C 18 Or K211\_3 Stuck Open  
Pass DC-90 HS VI\_3 , K211\_3 Stuck Closed  
Pass DC-90 VI\_3 High Force K29\_3 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 21 B 17 Or K29\_3 Stuck Open  
Pass DC-90 VI\_3 High Force K31\_3 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 21 A 19 Or K31\_3 Stuck Open  
Pass DC-90 VI\_3 High Force K34\_3 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 21 A 20 Or K34\_3 Stuck Open  
Pass DC-90 VI\_3 High Force K36\_3 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 21 C 17 Or K36\_3 Stuck Open  
Pass DC-90 VI\_3 High Force K38\_3 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 21 C 20 Or K38\_3 Stuck Open  
Pass DC-90 LS VI-3, DIB\_Slot Pin: 21 C 19 Or K216\_3 Stuck Open  
Pass DC-90 LS VI-3 , K216\_3 Stuck Close  
Pass DC-90 VI\_3 Low Force K44\_3 Stuck Closed

Pass DC-90 LF.3 DIB\_Slot Pin: 21 C 18 Or K44\_3 Stuck Open  
Pass DC-90 VI\_3 Low Force K46\_3 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 21 C 18 Or K46\_3 Stuck Open  
Pass DC-90 VI\_3 Low Force K202\_3 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 21 C 18 Or K202\_3 Stuck Open  
Pass DC-90 VI\_3 Low Force K50\_3 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 21 C 18 Or K50\_3 Stuck Open  
Pass DC-90 VI\_3 Low Force K52\_3 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 21 C 18 Or K52\_3 Stuck Open  
Pass DC-90 HS VI-3, DIB\_Slot Pin: 21 C 22 Or K212\_3 Stuck Open  
Pass DC-90 HS VI\_3 , K212\_3 Stuck Closed  
Pass DC-90 VI\_3 High Force K201\_3 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 21 B 21 Or K201\_3 Stuck Open  
Pass DC-90 VI\_3 High Force K32\_3 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 21 A 23 Or K32\_3 Stuck Open  
Pass DC-90 VI\_3 High Force K35\_3 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 21 A 24 Or K35\_3 Stuck Open  
Pass DC-90 VI\_3 High Force K37\_3 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 21 C 21 Or K37\_3 Stuck Open  
Pass DC-90 VI\_3 High Force K39\_3 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 21 D 19 Or K39\_3 Stuck Open  
Pass DC-90 LS VI-3, DIB\_Slot Pin: 21 C 23 Or K19\_3 Stuck Open  
Pass DC-90 LS VI-3 , K19\_3 Stuck Close  
Pass DC-90 VI\_3 Low Force K45\_3 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 21 C 22 Or K45\_3 Stuck Open  
Pass DC-90 VI\_3 Low Force K47\_3 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 21 C 22 Or K47\_3 Stuck Open  
Pass DC-90 VI\_3 Low Force K49\_3 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 21 C 22 Or K49\_3 Stuck Open  
Pass DC-90 VI\_3 Low Force K51\_3 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 21 C 22 Or K51\_3 Stuck Open  
Pass DC-90 VI\_3 Low Force K53\_3 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 21 C 22 Or K53\_3 Stuck Open  
Pass DC-90 HS VI-4, DIB\_Slot Pin: 21 C 26 Or K211\_4 Stuck Open  
Pass DC-90 HS VI\_4 , K211\_4 Stuck Closed  
Pass DC-90 VI\_4 High Force K29\_4 Stuck Closed  
Pass DC-90 HF VI-4, DIB\_Slot Pin: 21 B 25 Or K29\_4 Stuck Open  
Pass DC-90 VI\_4 High Force K31\_4 Stuck Closed  
Pass DC-90 HF VI-4, DIB\_Slot Pin: 21 A 27 Or K31\_4 Stuck Open  
Pass DC-90 VI\_4 High Force K34\_4 Stuck Closed  
Pass DC-90 HF VI-4, DIB\_Slot Pin: 21 A 28 Or K34\_4 Stuck Open  
Pass DC-90 VI\_4 High Force K36\_4 Stuck Closed  
Pass DC-90 HF VI-4, DIB\_Slot Pin: 21 C 25 Or K36\_4 Stuck Open  
Pass DC-90 VI\_4 High Force K38\_4 Stuck Closed  
Pass DC-90 HF VI-4, DIB\_Slot Pin: 21 C 28 Or K38\_4 Stuck Open  
Pass DC-90 LS VI-4, DIB\_Slot Pin: 21 C 27 Or K216\_4 Stuck Open  
Pass DC-90 LS VI-4 , K216\_4 Stuck Close  
Pass DC-90 VI\_4 Low Force K44\_4 Stuck Closed

Pass DC-90 LF.3 DIB\_Slot Pin: 21 C 26 Or K44\_4 Stuck Open  
Pass DC-90 VI\_4 Low Force K46\_4 Stuck Closed  
Pass DC-90 LF.4 DIB\_Slot Pin: 21 C 26 Or K46\_4 Stuck Open  
Pass DC-90 VI\_4 Low Force K202\_4 Stuck Closed  
Pass DC-90 LF.4 DIB\_Slot Pin: 21 C 26 Or K202\_4 Stuck Open  
Pass DC-90 VI\_4 Low Force K50\_4 Stuck Closed  
Pass DC-90 LF.4 DIB\_Slot Pin: 21 C 26 Or K50\_4 Stuck Open  
Pass DC-90 VI\_4 Low Force K52\_4 Stuck Closed  
Pass DC-90 LF.4 DIB\_Slot Pin: 21 C 26 Or K52\_4 Stuck Open  
Pass DC-90 HS VI-4, DIB\_Slot Pin: 21 C 30 Or K212\_4 Stuck Open  
Pass DC-90 HS VI\_4, K212\_4 Stuck Closed  
Pass DC-90 VI\_4 High Force K201\_4 Stuck Closed  
Pass DC-90 HF VI-4, DIB\_Slot Pin: 21 B 29 Or K201\_4 Stuck Open  
Pass DC-90 VI\_4 High Force K32\_4 Stuck Closed  
Pass DC-90 HF VI-4, DIB\_Slot Pin: 21 A 31 Or K32\_4 Stuck Open  
Pass DC-90 VI\_4 High Force K35\_4 Stuck Closed  
Pass DC-90 HF VI-4, DIB\_Slot Pin: 21 A 32 Or K35\_4 Stuck Open  
Pass DC-90 VI\_4 High Force K37\_4 Stuck Closed  
Pass DC-90 HF VI-4, DIB\_Slot Pin: 21 C 29 Or K37\_4 Stuck Open  
Pass DC-90 VI\_4 High Force K39\_4 Stuck Closed  
Pass DC-90 HF VI-4, DIB\_Slot Pin: 21 D 27 Or K39\_4 Stuck Open  
Pass DC-90 LS VI-4, DIB\_Slot Pin: 21 C 31 Or K19\_4 Stuck Open  
Pass DC-90 LS VI-4, K19\_4 Stuck Close  
Pass DC-90 VI\_4 Low Force K45\_4 Stuck Closed  
Pass DC-90 LF.4 DIB\_Slot Pin: 21 C 30 Or K45\_4 Stuck Open  
Pass DC-90 VI\_4 Low Force K47\_4 Stuck Closed  
Pass DC-90 LF.4 DIB\_Slot Pin: 21 C 30 Or K47\_4 Stuck Open  
Pass DC-90 VI\_4 Low Force K49\_4 Stuck Closed  
Pass DC-90 LF.4 DIB\_Slot Pin: 21 C 30 Or K49\_4 Stuck Open  
Pass DC-90 VI\_4 Low Force K51\_4 Stuck Closed  
Pass DC-90 LF.4 DIB\_Slot Pin: 21 C 30 Or K51\_4 Stuck Open  
Pass DC-90 VI\_4 Low Force K53\_4 Stuck Closed  
Pass DC-90 LF.4 DIB\_Slot Pin: 21 C 30 Or K53\_4 Stuck Open  
Pass DC-90 DIB\_PIN DGS 1 sh Closed using pinHF2A.1  
Pass DC-90 DIB\_PIN DGS 2 sh Closed using pinHF2A.1  
Pass DC-90 DIB\_PIN TMU1\_sh Closed using pinHF2A.1  
Pass DC-90 DIB\_PIN TMU2\_sh Closed using pinHF2A.1  
Pass DC-90 DIB\_PIN XOR1\_sh Closed using pinHF2A.1  
Pass DC-90 DIB\_PIN XOR2\_sh Closed using pinHF2A.1  
Pass DC-90 DIB\_PIN HF2A.1 Or TMU1 Open  
Pass DC-90 DIB\_PIN HF2A.1 Or TMU1 Closed  
Pass DC-90 DIB\_PIN HF2A.1 Or TMU2 Open  
Pass DC-90 DIB\_PIN HF2A.1 Or TMU2 Closed  
Pass DC-90 DIB\_PIN HF2A.1 Or XOR1 Closed  
Pass DC-90 DIB\_PIN HF2A.1 Or XOR2 Closed  
Pass DC-90 DIB\_PIN HF2A.1 Or DGS 1 Closed  
Pass DC-90 DIB\_PIN HF3A.1 Or DGS 2 Closed  
Pass DC-90 S21DC90\_PinHS1A to TMU1 HighSide, K199\_1

Pass DC-90 S21DC90\_PinHS2A to TMU1 HighSide, K199\_2  
Pass DC-90 S21DC90\_PinHS3A to TMU2 HighSide, K199\_3  
Pass DC-90 S21DC90\_PinHS4A to TMU2 HighSide, K199\_4  
Pass DC-90 S21DC90\_PinHS1B to TMU1 HighSide, K124\_1  
Pass DC-90 S21DC90\_PinHS2B to TMU1 HighSide, K124\_2  
Pass DC-90 S21DC90\_PinHS3B to TMU2 HighSide, K124\_3  
Pass DC-90 S21DC90\_PinHS4B to TMU2 HighSide, K124\_4  
Pass DC-90 S21DC90\_PinLS1A to TMU1 LowSide, K57\_1  
Pass DC-90 S21DC90\_PinLS2A to TMU1 LowSide, K57\_2  
Pass DC-90 S21DC90\_PinLS3A to TMU2 LowSide, K57\_3  
Pass DC-90 S21DC90\_PinLS4A to TMU2 LowSide, K57\_4  
Pass DC-90 S21DC90\_PinLS1B to TMU1 LowSide, K58\_1  
Pass DC-90 S21DC90\_PinLS2B to TMU1 LowSide, K58\_2  
Pass DC-90 S21DC90\_PinLS3B to TMU2 LowSide, K58\_3  
Pass DC-90 S21DC90\_PinLS4B to TMU2 LowSide, K58\_4  
Info DC-90 Testing DC90 in Slot# 11  
Pass DC-90 DIB\_Slot Pin: 23 B 2 Or 23 A 34 Open  
Pass DC-90 VI\_1 DIB\_Access High K214\_1 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 23 B 10 Or 23 A 34 Open  
Pass DC-90 VI\_2 DIB\_Access High K214\_2 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 23 B 18 Or 23 B 34 Open  
Pass DC-90 VI\_3 DIB\_Access High K214\_3 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 23 B 26 Or 23 B 34 Open  
Pass DC-90 VI\_4 DIB\_Access High K214\_4 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 23 B 6 Or 23 A 34 Open  
Pass DC-90 VI\_1 DIB\_Access High K213\_1 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 23 B 14 Or 23 A 34 Open  
Pass DC-90 VI\_2 DIB\_Access High K213\_2 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 23 B 18 Or 23 B 34 Open  
Pass DC-90 VI\_3 DIB\_Access High K213\_3 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 23 B 30 Or 23 B 34 Open  
Pass DC-90 VI\_4 DIB\_Access High K213\_4 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 23 C 2 Or 23 B 3 Open  
Pass DC-90 VI\_1 DIB\_Access Low K215\_1 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 23 C 10 Or 23 B 11 Open  
Pass DC-90 VI\_2 DIB\_Access Low K215\_2 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 23 C 18 Or 23 B 19 Open  
Pass DC-90 VI\_3 DIB\_Access Low K215\_3 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 23 C 26 Or 23 B 27 Open  
Pass DC-90 VI\_4 DIB\_Access Low K215\_4 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 23 C 6 Or 23 B 7 Open  
Pass DC-90 VI\_1 DIB\_Access Low K13\_1 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 23 C 14 Or 23 B 15 Open  
Pass DC-90 VI\_2 DIB\_Access Low K13\_2 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 23 C 22 Or 23 B 23 Open  
Pass DC-90 VI\_3 DIB\_Access Low K13\_3 Stuck Closed  
Pass DC-90 DIB\_Slot Pin: 23 C 30 Or 23 B 31 Open  
Pass DC-90 VI\_4 DIB\_Access Low K13\_4 Stuck Closed



Pass DC-90 HS VI-1, DIB\_Slot Pin: 23 C 2 Or K211\_1 Stuck Open  
Pass DC-90 HS VI\_1 , K211\_1 Stuck Closed  
Pass DC-90 VI\_1 High Force K29\_1 Stuck Closed  
Pass DC-90 HF VI-1, DIB\_Slot Pin: 23 B 1 Or K29\_1 Stuck Open  
Pass DC-90 VI\_1 High Force K31\_1 Stuck Closed  
Pass DC-90 HF VI-1, DIB\_Slot Pin: 23 A 3 Or K31\_1 Stuck Open  
Pass DC-90 VI\_1 High Force K34\_1 Stuck Closed  
Pass DC-90 HF VI-1, DIB\_Slot Pin: 23 A 4 Or K34\_1 Stuck Open  
Pass DC-90 VI\_1 High Force K36\_1 Stuck Closed  
Pass DC-90 HF VI-1, DIB\_Slot Pin: 23 C 1 Or K36\_1 Stuck Open  
Pass DC-90 VI\_1 High Force K38\_1 Stuck Closed  
Pass DC-90 HF VI-1, DIB\_Slot Pin: 23 C 4 Or K38\_1 Stuck Open  
Pass DC-90 LS VI-1, DIB\_Slot Pin: 23 C 3 Or K216\_1 Stuck Open  
Pass DC-90 LS VI-1 , K216\_1 Stuck Close  
Pass DC-90 VI\_1 Low Force K44\_1 Stuck Closed  
Pass DC-90 LF.1 DIB\_Slot Pin: 23 C 2 Or K44\_1 Stuck Open  
Pass DC-90 VI\_1 Low Force K46\_1 Stuck Closed  
Pass DC-90 LF.1 DIB\_Slot Pin: 23 C 2 Or K46\_1 Stuck Open  
Pass DC-90 VI\_1 Low Force K202\_1 Stuck Closed  
Pass DC-90 LF.1 DIB\_Slot Pin: 23 C 2 Or K202\_1 Stuck Open  
Pass DC-90 VI\_1 Low Force K50\_1 Stuck Closed  
Pass DC-90 LF.1 DIB\_Slot Pin: 23 C 2 Or K50\_1 Stuck Open  
Pass DC-90 VI\_1 Low Force K52\_1 Stuck Closed  
Pass DC-90 LF.1 DIB\_Slot Pin: 23 C 2 Or K52\_1 Stuck Open  
Pass DC-90 HS VI-1, DIB\_Slot Pin: 23 C 6 Or K212\_1 Stuck Open  
Pass DC-90 HS VI\_1 , K212\_1 Stuck Closed  
Pass DC-90 VI\_1 High Force K201\_1 Stuck Closed  
Pass DC-90 HF VI-1, DIB\_Slot Pin: 23 B 5 Or K31\_1 Stuck Open  
Pass DC-90 VI\_1 High Force K32\_1 Stuck Closed  
Pass DC-90 HF VI-1, DIB\_Slot Pin: 23 A 7 Or K32\_1 Stuck Open  
Pass DC-90 VI\_1 High Force K35\_1 Stuck Closed  
Pass DC-90 HF VI-1, DIB\_Slot Pin: 23 A 8 Or K35\_1 Stuck Open  
Pass DC-90 VI\_1 High Force K37\_1 Stuck Closed  
Pass DC-90 HF VI-1, DIB\_Slot Pin: 23 C 5 Or K37\_1 Stuck Open  
Pass DC-90 VI\_1 High Force K39\_1 Stuck Closed  
Pass DC-90 HF VI-1, DIB\_Slot Pin: 23 D 3 Or K39\_1 Stuck Open  
Pass DC-90 LS VI-1, DIB\_Slot Pin: 23 C 7 Or K19\_1 Stuck Open  
Pass DC-90 LS VI-1 , K19\_1 Stuck Close  
Pass DC-90 VI\_1 Low Force K45\_1 Stuck Closed  
Pass DC-90 LF.1 DIB\_Slot Pin: 23 C 6 Or K45\_1 Stuck Open  
Pass DC-90 VI\_1 Low Force K47\_1 Stuck Closed  
Pass DC-90 LF.1 DIB\_Slot Pin: 23 C 6 Or K47\_1 Stuck Open  
Pass DC-90 VI\_1 Low Force K49\_1 Stuck Closed  
Pass DC-90 LF.1 DIB\_Slot Pin: 23 C 6 Or K49\_1 Stuck Open  
Pass DC-90 VI\_1 Low Force K51\_1 Stuck Closed  
Pass DC-90 LF.1 DIB\_Slot Pin: 23 C 6 Or K51\_1 Stuck Open  
Pass DC-90 VI\_1 Low Force K53\_1 Stuck Closed  
Pass DC-90 LF.1 DIB\_Slot Pin: 23 C 6 Or K53\_1 Stuck Open

Pass DC-90 HS VI-2, DIB\_Slot Pin: 23 C 10 Or K211\_2 Stuck Open  
Pass DC-90 HS VI\_2 , K211\_2 Stuck Closed  
Pass DC-90 VI\_2 High Force K29\_2 Stuck Closed  
Pass DC-90 HF VI-2, DIB\_Slot Pin: 23 B 9 Or K29\_2 Stuck Open  
Pass DC-90 VI\_2 High Force K31\_2 Stuck Closed  
Pass DC-90 HF VI-2, DIB\_Slot Pin: 23 A 11 Or K31\_2 Stuck Open  
Pass DC-90 VI\_2 High Force K34\_2 Stuck Closed  
Pass DC-90 HF VI-2, DIB\_Slot Pin: 23 A 12 Or K34\_2 Stuck Open  
Pass DC-90 VI\_2 High Force K36\_2 Stuck Closed  
Pass DC-90 HF VI-2, DIB\_Slot Pin: 23 C 9 Or K36\_2 Stuck Open  
Pass DC-90 VI\_2 High Force K38\_2 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 23 C 19 Or K38\_2 Stuck Open  
Pass DC-90 LS VI-2, DIB\_Slot Pin: 23 C 11 Or K216\_2 Stuck Open  
Pass DC-90 LS VI-2 , K216\_2 Stuck Close  
Pass DC-90 VI\_2 Low Force K44\_2 Stuck Closed  
Pass DC-90 LF.2 DIB\_Slot Pin: 23 C 10 Or K44\_2 Stuck Open  
Pass DC-90 VI\_2 Low Force K46\_2 Stuck Closed  
Pass DC-90 LF.2 DIB\_Slot Pin: 23 C 10 Or K46\_2 Stuck Open  
Pass DC-90 VI\_2 Low Force K202\_2 Stuck Closed  
Pass DC-90 LF.2 DIB\_Slot Pin: 23 C 10 Or K202\_2 Stuck Open  
Pass DC-90 VI\_2 Low Force K50\_2 Stuck Closed  
Pass DC-90 LF.2 DIB\_Slot Pin: 23 C 10 Or K50\_2 Stuck Open  
Pass DC-90 VI\_2 Low Force K52\_2 Stuck Closed  
Pass DC-90 LF.2 DIB\_Slot Pin: 23 C 10 Or K52\_2 Stuck Open  
Pass DC-90 HS VI-2, DIB\_Slot Pin: 23 C 14 Or K212\_2 Stuck Open  
Pass DC-90 HS VI\_2 , K212\_2 Stuck Closed  
Pass DC-90 VI\_2 High Force K201\_2 Stuck Closed  
Pass DC-90 HF VI-2, DIB\_Slot Pin: 23 B 13 Or K2201\_2 Stuck Open  
Pass DC-90 VI\_2 High Force K32\_2 Stuck Closed  
Pass DC-90 HF VI-2, DIB\_Slot Pin: 23 A 15 Or K32\_2 Stuck Open  
Pass DC-90 VI\_2 High Force K35\_2 Stuck Closed  
Pass DC-90 HF VI-2, DIB\_Slot Pin: 23 A 16 Or K35\_2 Stuck Open  
Pass DC-90 VI\_2 High Force K37\_2 Stuck Closed  
Pass DC-90 HF VI-2, DIB\_Slot Pin: 23 C 13 Or K37\_2 Stuck Open  
Pass DC-90 VI\_2 High Force K39\_2 Stuck Closed  
Pass DC-90 HF VI-2, DIB\_Slot Pin: 23 D 11 Or K39\_2 Stuck Open  
Pass DC-90 LS VI-2, DIB\_Slot Pin: 23 C 15 Or K19\_2 Stuck Open  
Pass DC-90 LS VI-2 , K19\_2 Stuck Close  
Pass DC-90 VI\_2 Low Force K45\_2 Stuck Closed  
Pass DC-90 LF.2 DIB\_Slot Pin: 23 C 14 Or K45\_2 Stuck Open  
Pass DC-90 VI\_2 Low Force K47\_2 Stuck Closed  
Pass DC-90 LF.2 DIB\_Slot Pin: 23 C 14 Or K47\_2 Stuck Open  
Pass DC-90 VI\_2 Low Force K49\_2 Stuck Closed  
Pass DC-90 LF.2 DIB\_Slot Pin: 23 C 14 Or K49\_2 Stuck Open  
Pass DC-90 VI\_2 Low Force K51\_2 Stuck Closed  
Pass DC-90 LF.2 DIB\_Slot Pin: 23 C 14 Or K51\_2 Stuck Open  
Pass DC-90 VI\_2 Low Force K53\_2 Stuck Closed  
Pass DC-90 LF.2 DIB\_Slot Pin: 23 C 14 Or K53\_2 Stuck Open

Pass DC-90 HS VI-3, DIB\_Slot Pin: 23 C 18 Or K211\_3 Stuck Open  
Pass DC-90 HS VI\_3 , K211\_3 Stuck Closed  
Pass DC-90 VI\_3 High Force K29\_3 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 23 B 17 Or K29\_3 Stuck Open  
Pass DC-90 VI\_3 High Force K31\_3 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 23 A 19 Or K31\_3 Stuck Open  
Pass DC-90 VI\_3 High Force K34\_3 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 23 A 20 Or K34\_3 Stuck Open  
Pass DC-90 VI\_3 High Force K36\_3 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 23 C 17 Or K36\_3 Stuck Open  
Pass DC-90 VI\_3 High Force K38\_3 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 23 C 20 Or K38\_3 Stuck Open  
Pass DC-90 LS VI-3, DIB\_Slot Pin: 23 C 19 Or K216\_3 Stuck Open  
Pass DC-90 LS VI-3 , K216\_3 Stuck Close  
Pass DC-90 VI\_3 Low Force K44\_3 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 23 C 18 Or K44\_3 Stuck Open  
Pass DC-90 VI\_3 Low Force K46\_3 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 23 C 18 Or K46\_3 Stuck Open  
Pass DC-90 VI\_3 Low Force K202\_3 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 23 C 18 Or K202\_3 Stuck Open  
Pass DC-90 VI\_3 Low Force K50\_3 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 23 C 18 Or K50\_3 Stuck Open  
Pass DC-90 VI\_3 Low Force K52\_3 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 23 C 18 Or K52\_3 Stuck Open  
Pass DC-90 HS VI-3, DIB\_Slot Pin: 23 C 22 Or K212\_3 Stuck Open  
Pass DC-90 HS VI\_3 , K212\_3 Stuck Closed  
Pass DC-90 VI\_3 High Force K201\_3 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 23 B 21 Or K201\_3 Stuck Open  
Pass DC-90 VI\_3 High Force K32\_3 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 23 A 23 Or K32\_3 Stuck Open  
Pass DC-90 VI\_3 High Force K35\_3 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 23 A 24 Or K35\_3 Stuck Open  
Pass DC-90 VI\_3 High Force K37\_3 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 23 C 21 Or K37\_3 Stuck Open  
Pass DC-90 VI\_3 High Force K39\_3 Stuck Closed  
Pass DC-90 HF VI-3, DIB\_Slot Pin: 23 D 19 Or K39\_3 Stuck Open  
Pass DC-90 LS VI-3, DIB\_Slot Pin: 23 C 23 Or K19\_3 Stuck Open  
Pass DC-90 LS VI-3 , K19\_3 Stuck Close  
Pass DC-90 VI\_3 Low Force K45\_3 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 23 C 22 Or K45\_3 Stuck Open  
Pass DC-90 VI\_3 Low Force K47\_3 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 23 C 22 Or K47\_3 Stuck Open  
Pass DC-90 VI\_3 Low Force K49\_3 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 23 C 22 Or K49\_3 Stuck Open  
Pass DC-90 VI\_3 Low Force K51\_3 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 23 C 22 Or K51\_3 Stuck Open  
Pass DC-90 VI\_3 Low Force K53\_3 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 23 C 22 Or K53\_3 Stuck Open

Pass DC-90 HS VI-4, DIB\_Slot Pin: 23 C 26 Or K211\_4 Stuck Open  
Pass DC-90 HS VI\_4 , K211\_4 Stuck Closed  
Pass DC-90 VI\_4 High Force K29\_4 Stuck Closed  
Pass DC-90 HF VI-4, DIB\_Slot Pin: 23 B 25 Or K29\_4 Stuck Open  
Pass DC-90 VI\_4 High Force K31\_4 Stuck Closed  
Pass DC-90 HF VI-4, DIB\_Slot Pin: 23 A 27 Or K31\_4 Stuck Open  
Pass DC-90 VI\_4 High Force K34\_4 Stuck Closed  
Pass DC-90 HF VI-4, DIB\_Slot Pin: 23 A 28 Or K34\_4 Stuck Open  
Pass DC-90 VI\_4 High Force K36\_4 Stuck Closed  
Pass DC-90 HF VI-4, DIB\_Slot Pin: 23 C 25 Or K36\_4 Stuck Open  
Pass DC-90 VI\_4 High Force K38\_4 Stuck Closed  
Pass DC-90 HF VI-4, DIB\_Slot Pin: 23 C 28 Or K38\_4 Stuck Open  
Pass DC-90 LS VI-4, DIB\_Slot Pin: 23 C 27 Or K216\_4 Stuck Open  
Pass DC-90 LS VI-4 , K216\_4 Stuck Close  
Pass DC-90 VI\_4 Low Force K44\_4 Stuck Closed  
Pass DC-90 LF.3 DIB\_Slot Pin: 23 C 26 Or K44\_4 Stuck Open  
Pass DC-90 VI\_4 Low Force K46\_4 Stuck Closed  
Pass DC-90 LF.4 DIB\_Slot Pin: 23 C 26 Or K46\_4 Stuck Open  
Pass DC-90 VI\_4 Low Force K202\_4 Stuck Closed  
Pass DC-90 LF.4 DIB\_Slot Pin: 23 C 26 Or K202\_4 Stuck Open  
Pass DC-90 VI\_4 Low Force K50\_4 Stuck Closed  
Pass DC-90 LF.4 DIB\_Slot Pin: 23 C 26 Or K50\_4 Stuck Open  
Pass DC-90 VI\_4 Low Force K52\_4 Stuck Closed  
Pass DC-90 LF.4 DIB\_Slot Pin: 23 C 26 Or K52\_4 Stuck Open  
Pass DC-90 HS VI-4, DIB\_Slot Pin: 23 C 30 Or K212\_4 Stuck Open  
Pass DC-90 HS VI\_4 , K212\_4 Stuck Closed  
Pass DC-90 VI\_4 High Force K201\_4 Stuck Closed  
Pass DC-90 HF VI-4, DIB\_Slot Pin: 23 B 29 Or K201\_4 Stuck Open  
Pass DC-90 VI\_4 High Force K32\_4 Stuck Closed  
Pass DC-90 HF VI-4, DIB\_Slot Pin: 23 A 31 Or K32\_4 Stuck Open  
Pass DC-90 VI\_4 High Force K35\_4 Stuck Closed  
Pass DC-90 HF VI-4, DIB\_Slot Pin: 23 A 32 Or K35\_4 Stuck Open  
Pass DC-90 VI\_4 High Force K37\_4 Stuck Closed  
Pass DC-90 HF VI-4, DIB\_Slot Pin: 23 C 29 Or K37\_4 Stuck Open  
Pass DC-90 VI\_4 High Force K39\_4 Stuck Closed  
Pass DC-90 HF VI-4, DIB\_Slot Pin: 23 D 27 Or K39\_4 Stuck Open  
Pass DC-90 LS VI-4, DIB\_Slot Pin: 23 C 31 Or K19\_4 Stuck Open  
Pass DC-90 LS VI-4 , K19\_4 Stuck Close  
Pass DC-90 VI\_4 Low Force K45\_4 Stuck Closed  
Pass DC-90 LF.4 DIB\_Slot Pin: 23 C 30 Or K45\_4 Stuck Open  
Pass DC-90 VI\_4 Low Force K47\_4 Stuck Closed  
Pass DC-90 LF.4 DIB\_Slot Pin: 23 C 30 Or K47\_4 Stuck Open  
Pass DC-90 VI\_4 Low Force K49\_4 Stuck Closed  
Pass DC-90 LF.4 DIB\_Slot Pin: 23 C 30 Or K49\_4 Stuck Open  
Pass DC-90 VI\_4 Low Force K51\_4 Stuck Closed  
Pass DC-90 LF.4 DIB\_Slot Pin: 23 C 30 Or K51\_4 Stuck Open  
Pass DC-90 VI\_4 Low Force K53\_4 Stuck Closed  
Pass DC-90 LF.4 DIB\_Slot Pin: 23 C 30 Or K53\_4 Stuck Open

Pass DC-90 DIB\_PIN DGS 1 sh Closed using pinHF2A.1  
 Pass DC-90 DIB\_PIN DGS 2 sh Closed using pinHF2A.1  
 Pass DC-90 DIB\_PIN TMU1\_sh Closed using pinHF2A.1  
 Pass DC-90 DIB\_PIN TMU2\_sh Closed using pinHF2A.1  
 Pass DC-90 DIB\_PIN XOR1\_sh Closed using pinHF2A.1  
 Pass DC-90 DIB\_PIN XOR2\_sh Closed using pinHF2A.1  
 Pass DC-90 DIB\_PIN HF2A.1 Or TMU1 Open  
 Pass DC-90 DIB\_PIN HF2A.1 Or TMU1 Closed  
 Pass DC-90 DIB\_PIN HF2A.1 Or TMU2 Open  
 Pass DC-90 DIB\_PIN HF2A.1 Or TMU2 Closed  
 Pass DC-90 DIB\_PIN HF2A.1 Or XOR1 Closed  
 Pass DC-90 DIB\_PIN HF2A.1 Or XOR2 Closed  
 Pass DC-90 DIB\_PIN HF2A.1 Or DGS 1 Closed  
 Pass DC-90 DIB\_PIN HF3A.1 Or DGS 2 Closed  
 Pass DC-90 S23DC90\_PinHS1A to TMU1 HighSide, K199\_1  
 Pass DC-90 S23DC90\_PinHS2A to TMU1 HighSide, K199\_2  
 Pass DC-90 S23DC90\_PinHS3A to TMU2 HighSide, K199\_3  
 Pass DC-90 S23DC90\_PinHS4A to TMU2 HighSide, K199\_4  
 Pass DC-90 S23DC90\_PinHS1B to TMU1 HighSide, K124\_1  
 Pass DC-90 S23DC90\_PinHS2B to TMU1 HighSide, K124\_2  
 Pass DC-90 S23DC90\_PinHS3B to TMU2 HighSide, K124\_3  
 Pass DC-90 S23DC90\_PinHS4B to TMU2 HighSide, K124\_4  
 Pass DC-90 S23DC90\_PinLS1A to TMU1 LowSide, K57\_1  
 Pass DC-90 S23DC90\_PinLS2A to TMU1 LowSide, K57\_2  
 Pass DC-90 S23DC90\_PinLS3A to TMU2 LowSide, K57\_3  
 Pass DC-90 S23DC90\_PinLS4A to TMU2 LowSide, K57\_4  
 Pass DC-90 S23DC90\_PinLS1B to TMU1 LowSide, K58\_1  
 Pass DC-90 S23DC90\_PinLS2B to TMU1 LowSide, K58\_2  
 Pass DC-90 S23DC90\_PinLS3B to TMU2 LowSide, K58\_3  
 Pass DC-90 S23DC90\_PinLS4B to TMU2 LowSide, K58\_4  
 Info DC-90 Test finished  
 Info HSD-200 Beginning HSD-200 continuity test  
 Pass HSD-200 ch25 (HSD-200, 17 A 17), Acc6 (HSD-200, 17 A 23),.000,.425,.425,Pass  
 Pass HSD-200 DGS3 (HSD-200, 17 B 10), ch4 (HSD-200, 17 C 33),.000,.522,.522,Pass  
 Pass HSD-200 DGS2 (HSD-200, 17 B 22), ch4 (HSD-200, 17 C 33),.000,.524,.524,Pass  
 Pass HSD-200 DGS1 (HSD-200, 17 B 34), ch4 (HSD-200, 17 C 33),.000,.997,.997,Pass  
 Pass HSD-200 ch47 (HSD-200, 17 B 2), ch48 (HSD-200, 17 D 2),.000,1.195,1.195,Pass  
 Pass HSD-200 ch45 (HSD-200, 17 A 3), ch46 (HSD-200, 17 C 3),.000,1.983,1.983,Pass  
 Pass HSD-200 ch43 (HSD-200, 17 B 4), ch44 (HSD-200, 17 D 4),.000,1.981,1.981,Pass  
 Pass HSD-200 ch41 (HSD-200, 17 A 5), ch42 (HSD-200, 17 C 5),.000,.857,.857,Pass  
 Pass HSD-200 ch39 (HSD-200, 17 B 6), ch40 (HSD-200, 17 D 6),.000,.854,.854,Pass  
 Pass HSD-200 ch37 (HSD-200, 17 A 7), ch38 (HSD-200, 17 C 7),.000,1.196,1.196,Pass  
 Pass HSD-200 ch35 (HSD-200, 17 B 8), ch36 (HSD-200, 17 D 8),.000,1.198,1.198,Pass  
 Pass HSD-200 ch33 (HSD-200, 17 A 9), ch34 (HSD-200, 17 C 9),.000,.998,.998,Pass  
 Pass HSD-200 ch31 (HSD-200, 17 B 14), ch32 (HSD-200, 17 D 14),.000,1.190,1.190,Pass  
 Pass HSD-200 ch29 (HSD-200, 17 A 15), ch30 (HSD-200, 17 C 15),.000,1.194,1.194,Pass  
 Pass HSD-200 ch27 (HSD-200, 17 B 16), ch28 (HSD-200, 17 D 16),.000,1.196,1.196,Pass  
 Pass HSD-200 ch25 (HSD-200, 17 A 17), ch26 (HSD-200, 17 C 17),.000,1.190,1.190,Pass

Pass	HSD-200	ch23 (HSD-200, 17 B 18), ch24 (HSD-200, 17 D 18),.000,1.199,1.199,Pass
Pass	HSD-200	ch21 (HSD-200, 17 A 19), ch22 (HSD-200, 17 C 19),.000,1.195,1.195,Pass
Pass	HSD-200	ch19 (HSD-200, 17 B 20), ch20 (HSD-200, 17 D 20),.000,1.197,1.197,Pass
Pass	HSD-200	ch17 (HSD-200, 17 A 21), ch18 (HSD-200, 17 C 21),.000,.996,.996,Pass
Pass	HSD-200	ch13 (HSD-200, 17 B 26), ch14 (HSD-200, 17 D 26),.000,1.196,1.196,Pass
Pass	HSD-200	ch15 (HSD-200, 17 A 27), ch16 (HSD-200, 17 C 27),.000,1.193,1.193,Pass
Pass	HSD-200	ch7 (HSD-200, 17 B 28), ch8 (HSD-200, 17 D 28),.000,1.192,1.192,Pass
Pass	HSD-200	ch5 (HSD-200, 17 A 29), ch6 (HSD-200, 17 C 29),.000,1.191,1.191,Pass
Pass	HSD-200	ch11 (HSD-200, 17 B 30), ch12 (HSD-200, 17 D 30),.000,1.189,1.189,Pass
Pass	HSD-200	ch9 (HSD-200, 17 A 31), ch10 (HSD-200, 17 C 31),.000,1.196,1.196,Pass
Pass	HSD-200	ch1 (HSD-200, 17 B 32), ch2 (HSD-200, 17 D 32),.000,.854,.854,Pass
Pass	HSD-200	ch3 (HSD-200, 17 A 33), ch4 (HSD-200, 17 C 33),.000,1.001,1.001,Pass
Pass	HSD-200	ch9 (HSD-200, 17 A 31), Acc1 (HSD-200, 17 B 36),.000,.770,.770,Pass
Pass	HSD-200	ch47 (HSD-200, 17 B 2), Acc2 (HSD-200, 17 A 35),.000,.793,.793,Pass
Pass	HSD-200	ch47 (HSD-200, 17 B 2), Acc3 (HSD-200, 17 C 35),.000,.794,.794,Pass
Pass	HSD-200	ch3 (HSD-200, 17 A 33), Acc4 (HSD-200, 17 D 34),.000,.659,.659,Pass
Pass	HSD-200	ch21 (HSD-200, 17 A 19), Acc5 (HSD-200, 17 B 24),.000,.785,.785,Pass
Pass	HSD-200	ch5 (HSD-200, 17 A 29), Acc7 (HSD-200, 17 C 23),.000,.790,.790,Pass
Pass	HSD-200	ch18 (HSD-200, 17 C 21), Acc8 (HSD-200, 17 D 22),.000,.647,.647,Pass
Pass	HSD-200	ch33 (HSD-200, 17 A 9), Acc12 (HSD-200, 17 D 10),.000,.678,.678,Pass
Pass	HSD-200	ch25 (HSD-200, 17 A 17), Acc10 (HSD-200, 17 A 11),.000,.798,.798,Pass
Pass	HSD-200	ch25 (HSD-200, 17 A 17), Acc11 (HSD-200, 17 C 11),.000,.765,.765,Pass
Pass	HSD-200	ch37 (HSD-200, 17 A 7), Acc9 (HSD-200, 17 B 12),.000,.787,.787,Pass
Info	HSD-200	Test finished
Info	HVD-1	Beginning HVD-1 continuity test
Pass	HVD-1	ch 1 in (HVD-1, 5 A 1), ch 17 out (HVD-1, 5 A 4),.000,1.119,1.119,Pass
Pass	HVD-1	ch 1 in (HVD-1, 5 A 1), ch 10 out (HVD-1, 5 D 6),.000,1.118,1.118,Pass
Pass	HVD-1	ch 1 in (HVD-1, 5 A 1), acc 2 (HVD-1, 5 B 6),.000,1.121,1.121,Pass
Pass	HVD-1	ch 18 in (HVD-1, 5 A 7), ch 3 out (HVD-1, 5 A 10),.000,1.125,1.125,Pass
Pass	HVD-1	ch 18 in (HVD-1, 5 A 7), acc 11 (HVD-1, 5 C 10),.000,1.145,1.145,Pass
Pass	HVD-1	ch 4 in (HVD-1, 5 A 13), ch 20 out (HVD-1, 5 A 16),.000,1.120,1.120,Pass
Pass	HVD-1	ch 4 in (HVD-1, 5 A 13), ch 13 out (HVD-1, 5 D 18),.000,1.117,1.117,Pass
Pass	HVD-1	ch 4 in (HVD-1, 5 A 13), acc 5 (HVD-1, 5 B 18),.000,1.119,1.119,Pass
Pass	HVD-1	ch 21 in (HVD-1, 5 A 19), ch 6 out (HVD-1, 5 A 22),.000,1.118,1.118,Pass
Pass	HVD-1	ch 21 in (HVD-1, 5 A 19), acc 14 (HVD-1, 5 C 22),.000,1.140,1.140,Pass
Pass	HVD-1	ch 7 in (HVD-1, 5 A 25), ch 23 out (HVD-1, 5 A 28),.000,1.133,1.133,Pass
Pass	HVD-1	ch 7 in (HVD-1, 5 A 25), acc 8 (HVD-1, 5 B 30),.000,1.131,1.131,Pass
Pass	HVD-1	ch 7 in (HVD-1, 5 A 25), ch 16 out (HVD-1, 5 D 30),.000,1.124,1.124,Pass
Pass	HVD-1	ch 24 in (HVD-1, 5 A 31), acc 17 (HVD-1, 5 A 34),.000,1.260,1.260,Pass
Pass	HVD-1	ch 24 in (HVD-1, 5 A 31), acc 19 (HVD-1, 5 C 34),.000,1.261,1.261,Pass
Pass	HVD-1	ch 24 in (HVD-1, 5 A 31), acc 22 (HVD-1, 5 B 36),.000,1.266,1.266,Pass
Pass	HVD-1	ch 3 in (HVD-1, 5 A 9), ch 2 out (HVD-1, 5 A 6),.000,1.438,1.438,Pass
Pass	HVD-1	ch 3 in (HVD-1, 5 A 9), acc 10 (HVD-1, 5 C 6),.000,1.442,1.442,Pass
Pass	HVD-1	ch 3 in (HVD-1, 5 A 9), ch 11 out (HVD-1, 5 D 10),.000,1.441,1.441,Pass
Pass	HVD-1	ch 12 in (HVD-1, 5 D 13), ch 19 out (HVD-1, 5 A 12),.000,.912,.912,Pass
Pass	HVD-1	ch 20 in (HVD-1, 5 A 15), ch 19 out (HVD-1, 5 A 12),.000,.912,.912,Pass
Pass	HVD-1	ch 6 in (HVD-1, 5 A 21), ch 5 out (HVD-1, 5 A 18),.000,1.440,1.440,Pass
Pass	HVD-1	ch 6 in (HVD-1, 5 A 21), acc 13 (HVD-1, 5 C 18),.000,1.444,1.444,Pass

Pass HVD-1 ch 6 in (HVD-1, 5 A 21), ch 14 out (HVD-1, 5 D 22),.000,1.435,1.435,Pass  
Pass HVD-1 ch 15 in (HVD-1, 5 D 25), ch 22 out (HVD-1, 5 A 24),.000,.922,.922,Pass  
Pass HVD-1 ch 23 in (HVD-1, 5 A 27), ch 22 out (HVD-1, 5 A 24),.000,.920,.920,Pass  
Pass HVD-1 ch 2 in (HVD-1, 5 A 5), ch 1 out (HVD-1, 5 A 2),.000,2.541,2.541,Pass  
Pass HVD-1 ch 2 in (HVD-1, 5 A 5), acc 9 (HVD-1, 5 C 2),.000,2.547,2.547,Pass  
Pass HVD-1 ch 2 in (HVD-1, 5 A 5), acc 21 (HVD-1, 5 A 36),.000,2.555,2.555,Pass  
Pass HVD-1 ch 2 in (HVD-1, 5 A 5), acc 23 (HVD-1, 5 C 36),.000,2.549,2.549,Pass  
Pass HVD-1 ch 19 in (HVD-1, 5 A 11), ch 18 out (HVD-1, 5 A 8),.000,1.252,1.252,Pass  
Pass HVD-1 ch 5 in (HVD-1, 5 A 17), ch 4 out (HVD-1, 5 A 14),.000,1.255,1.255,Pass  
Pass HVD-1 ch 5 in (HVD-1, 5 A 17), acc 12 (HVD-1, 5 C 14),.000,1.255,1.255,Pass  
Pass HVD-1 ch 22 in (HVD-1, 5 A 23), ch 21 out (HVD-1, 5 A 20),.000,1.253,1.253,Pass  
Pass HVD-1 ch 8 in (HVD-1, 5 A 29), ch 7 out (HVD-1, 5 A 26),.000,1.256,1.256,Pass  
Pass HVD-1 ch 8 in (HVD-1, 5 A 29), acc 15 (HVD-1, 5 C 26),.000,1.254,1.254,Pass  
Pass HVD-1 ch 10 in (HVD-1, 5 D 5), acc 4 (HVD-1, 5 B 14),.000,1.015,1.015,Pass  
Pass HVD-1 ch 10 in (HVD-1, 5 D 5), ch 12 out (HVD-1, 5 D 14),.000,1.014,1.014,Pass  
Pass HVD-1 ch 13 in (HVD-1, 5 D 17), ch 15 out (HVD-1, 5 D 26),.000,1.015,1.015,Pass  
Pass HVD-1 ch 13 in (HVD-1, 5 D 17), acc 7 (HVD-1, 5 B 26),.000,1.020,1.020,Pass  
Pass HVD-1 ch 24 in (HVD-1, 5 A 31), acc 3 (HVD-1, 5 B 10),.000,.777,.777,Pass  
Pass HVD-1 ch 3 in (HVD-1, 5 A 9), acc 24 (HVD-1, 5 D 36),.000,.769,.769,Pass  
Pass HVD-1 ch 20 in (HVD-1, 5 A 15), acc 6 (HVD-1, 5 B 22),.000,.630,.630,Pass  
Pass HVD-1 ch 9 in (HVD-1, 5 D 1), ch 8 out (HVD-1, 5 A 30),.000,.623,.623,Pass  
Pass HVD-1 ch 9 in (HVD-1, 5 D 1), acc 16 (HVD-1, 5 C 30),.000,.626,.626,Pass  
Pass HVD-1 ch 9 in (HVD-1, 5 D 1), acc 18 (HVD-1, 5 B 34),.000,.625,.625,Pass  
Pass HVD-1 ch 9 in (HVD-1, 5 D 1), acc 20 (HVD-1, 5 D 34),.000,.622,.622,Pass  
Pass HVD-1 ch 16 in (HVD-1, 5 D 29), ch 9 out (HVD-1, 5 D 2),.000,.587,.587,Pass  
Pass HVD-1 ch 16 in (HVD-1, 5 D 29), acc 1 (HVD-1, 5 B 2),.000,.585,.585,Pass  
Pass HVD-1 ch 16 in (HVD-1, 5 D 29), ch 24 out (HVD-1, 5 A 32),.000,.589,.589,Pass  
Info HVD-1 Test finished on all slots  
Info VHFAC Beginning VHFAC continuity test  
Pass VHFAC src2a+ (9 B 2) pin 1 DUT relay stuck closed  
Pass VHFAC src2b+ (9 D 2) pin 2 DUT relay stuck closed  
Pass VHFAC src2a+ (VHFAC, 9 B 2), src2b+ (VHFAC, 9 D 2),.000,1.048,1.048,Pass  
Pass VHFAC src1a+ (9 A 3) pin 1 DUT relay stuck closed  
Pass VHFAC src1b+ (9 C 3) pin 2 DUT relay stuck closed  
Pass VHFAC src1a+ (VHFAC, 9 A 3), src1b+ (VHFAC, 9 C 3),.000,1.744,1.744,Pass  
Pass VHFAC src2a- (9 B 6) pin 1 DUT relay stuck closed  
Pass VHFAC src2b- (9 D 6) pin 2 DUT relay stuck closed  
Pass VHFAC src2a- (VHFAC, 9 B 6), src2b- (VHFAC, 9 D 6),.000,.740,.740,Pass  
Pass VHFAC src1a- (9 A 7) pin 1 DUT relay stuck closed  
Pass VHFAC src1b- (9 C 7) pin 2 DUT relay stuck closed  
Pass VHFAC src1a- (VHFAC, 9 A 7), src1b- (VHFAC, 9 C 7),.000,1.046,1.046,Pass  
Pass VHFAC cap2a+ (9 B 10) pin 1 DUT relay stuck closed  
Pass VHFAC cap2b+ (9 D 10) pin 2 DUT relay stuck closed  
Pass VHFAC cap2a+ (VHFAC, 9 B 10), cap2b+ (VHFAC, 9 D 10),.000,1.044,1.044,Pass  
Pass VHFAC cap1a+ (9 A 11) pin 1 DUT relay stuck closed  
Pass VHFAC cap1b+ (9 C 11) pin 2 DUT relay stuck closed  
Pass VHFAC cap1a+ (VHFAC, 9 A 11), cap1b+ (VHFAC, 9 C 11),.000,1.044,1.044,Pass  
Pass VHFAC cap2a- (9 B 14) pin 1 DUT relay stuck closed

Pass VHFAC cap2b- (9 D 14) pin 2 DUT relay stuck closed  
Pass VHFAC cap2a- (VHFAC, 9 B 14), cap2b- (VHFAC, 9 D 14),.000,1.042,1.042,Pass  
Pass VHFAC cap1a- (9 A 15) pin 1 DUT relay stuck closed  
Pass VHFAC cap1b- (9 C 15) pin 2 DUT relay stuck closed  
Pass VHFAC cap1a- (VHFAC, 9 A 15), cap1b- (VHFAC, 9 C 15),.000,1.047,1.047,Pass  
Info VHFAC Voltage measurement with forcing pin not connected = 8.19674357771873E-02  
Info VHFAC Voltage measurement before forcing voltage = 0.042142029851675  
Pass VHFAC dibtrig1 (9 A 37) pin 2 DUT relay stuck closed  
Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), dibtrig1 (VHFAC, 9 A 37),.000,1.040,1.040,Pass  
Pass VHFAC dibtrig2 (9 B 36) pin 2 DUT relay stuck closed  
Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), dibtrig2 (VHFAC, 9 B 36),.000,.627,.627,Pass  
Pass VHFAC dibtrig3 (9 C 37) pin 2 DUT relay stuck closed  
Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), dibtrig3 (VHFAC, 9 C 37),.000,1.046,1.046,Pass  
Pass VHFAC dibtrig4 (9 D 36) pin 2 DUT relay stuck closed  
Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), dibtrig4 (VHFAC, 9 D 36),.000,.626,.626,Pass  
Pass VHFAC accsrc1+ (9 A 17) pin 2 DUT relay stuck closed  
Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), accsrc1+ (VHFAC, 9 A 17),.000,.797,.797,Pass  
Pass VHFAC accsrc1+ (9 A 17) pin 2 DUT relay stuck closed  
Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), accsrc1+ (VHFAC, 9 A 17),.000,.797,.797,Pass  
Pass VHFAC accsrc1- (9 B 16) pin 2 DUT relay stuck closed  
Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), accsrc1- (VHFAC, 9 B 16),.000,.797,.797,Pass  
Pass VHFAC accsrc1- (9 B 16) pin 2 DUT relay stuck closed  
Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), accsrc1- (VHFAC, 9 B 16),.000,.803,.803,Pass  
Pass VHFAC accsrc2+ (9 A 21) pin 2 DUT relay stuck closed  
Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), accsrc2+ (VHFAC, 9 A 21),.000,.798,.798,Pass  
Pass VHFAC accsrc2+ (9 A 21) pin 2 DUT relay stuck closed  
Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), accsrc2+ (VHFAC, 9 A 21),.000,.799,.799,Pass  
Pass VHFAC accsrc2- (9 B 20) pin 2 DUT relay stuck closed  
Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), accsrc2- (VHFAC, 9 B 20),.000,.793,.793,Pass  
Pass VHFAC accsrc2- (9 B 20) pin 2 DUT relay stuck closed  
Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), accsrc2- (VHFAC, 9 B 20),.000,.781,.781,Pass  
Pass VHFAC acccap1+ (9 A 23) pin 2 DUT relay stuck closed  
Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), acccap1+ (VHFAC, 9 A 23),.000,.795,.795,Pass  
Pass VHFAC acccap1+ (9 A 23) pin 2 DUT relay stuck closed  
Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), acccap1+ (VHFAC, 9 A 23),.000,.795,.795,Pass  
Pass VHFAC acccap1- (9 B 22) pin 2 DUT relay stuck closed  
Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), acccap1- (VHFAC, 9 B 22),.000,.797,.797,Pass  
Pass VHFAC acccap1- (9 B 22) pin 2 DUT relay stuck closed  
Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), acccap1- (VHFAC, 9 B 22),.000,.797,.797,Pass  
Pass VHFAC acccap2+ (9 A 25) pin 2 DUT relay stuck closed  
Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), acccap2+ (VHFAC, 9 A 25),.000,.797,.797,Pass  
Pass VHFAC acccap2+ (9 A 25) pin 2 DUT relay stuck closed  
Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), acccap2+ (VHFAC, 9 A 25),.000,.790,.790,Pass  
Pass VHFAC acccap2- (9 B 24) pin 2 DUT relay stuck closed  
Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), acccap2- (VHFAC, 9 B 24),.000,.795,.795,Pass  
Pass VHFAC acccap2- (9 B 24) pin 2 DUT relay stuck closed  
Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), acccap2- (VHFAC, 9 B 24),.000,.781,.781,Pass  
Pass VHFAC acctmu+ (9 A 33) pin 2 DUT relay stuck closed



Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), acctmu+ (VHFAC, 9 A 33),.000,.800,.800,Pass  
 Pass VHFAC acctmu- (9 B 32) pin 2 DUT relay stuck closed  
 Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), acctmu- (VHFAC, 9 B 32),.000,.559,.559,Pass  
 Pass VHFAC sb\_io1 (9 C 33) pin 2 DUT relay stuck closed  
 Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), sb\_io1 (VHFAC, 9 C 33),.000,.997,.997,Pass  
 Pass VHFAC sb\_io2 (9 D 32) pin 2 DUT relay stuck closed  
 Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), sb\_io2 (VHFAC, 9 D 32),.000,.713,.713,Pass  
 Pass VHFAC sb\_clk (9 D 34) pin 2 DUT relay stuck closed  
 Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), sb\_clk (VHFAC, 9 D 34),.000,.997,.997,Pass  
 Pass VHFAC tmu\_ch2+ (9 A 29) pin 2 DUT relay stuck closed  
 Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), tmu\_ch2+ (VHFAC, 9 A 29),.000,.997,.997,Pass  
 Pass VHFAC tmu\_ch2- (9 B 28) pin 2 DUT relay stuck closed  
 Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), tmu\_ch2- (VHFAC, 9 B 28),.000,1.000,1.000,Pass  
 Pass VHFAC tmu\_ch1+ (9 A 27) pin 2 DUT relay stuck closed  
 Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), tmu\_ch1+ (VHFAC, 9 A 27),.000,1.000,1.000,Pass  
 Pass VHFAC tmu\_ch1- (9 B 26) pin 2 DUT relay stuck closed  
 Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), tmu\_ch1- (VHFAC, 9 B 26),.000,.997,.997,Pass  
 Pass VHFAC event0\_0 (9 C 17) pin 2 DUT relay stuck closed  
 Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), event0\_0 (VHFAC, 9 C 17),.000,.643,.643,Pass  
 Pass VHFAC event0\_1 (9 C 19) pin 2 DUT relay stuck closed  
 Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), event0\_1 (VHFAC, 9 C 19),.000,.522,.522,Pass  
 Pass VHFAC event0\_2 (9 C 21) pin 2 DUT relay stuck closed  
 Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), event0\_2 (VHFAC, 9 C 21),.000,.525,.525,Pass  
 Pass VHFAC event0\_3 (9 C 23) pin 2 DUT relay stuck closed  
 Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), event0\_3 (VHFAC, 9 C 23),.000,.527,.527,Pass  
 Pass VHFAC event0\_4 (9 C 25) pin 2 DUT relay stuck closed  
 Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), event0\_4 (VHFAC, 9 C 25),.000,.525,.525,Pass  
 Pass VHFAC event0\_5 (9 C 27) pin 2 DUT relay stuck closed  
 Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), event0\_5 (VHFAC, 9 C 27),.000,.526,.526,Pass  
 Pass VHFAC event0\_6 (9 C 29) pin 2 DUT relay stuck closed  
 Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), event0\_6 (VHFAC, 9 C 29),.000,.525,.525,Pass  
 Pass VHFAC event0\_7 (9 C 31) pin 2 DUT relay stuck closed  
 Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), event0\_7 (VHFAC, 9 C 31),.000,.443,.443,Pass  
 Pass VHFAC event1\_0 (9 D 16) pin 2 DUT relay stuck closed  
 Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), event1\_0 (VHFAC, 9 D 16),.000,.643,.643,Pass  
 Pass VHFAC event1\_1 (9 D 18) pin 2 DUT relay stuck closed  
 Fail VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), event1\_1 (VHFAC, 9 D 18),.000,.004,.004,Fail  
 Fail VHFAC DC\_CAL\_HS Neighboring Pins: DC\_CAL\_HS (A 13), DC\_CAL\_LG4 (C 13), DIGITAL\_SYNC1 (B 15), TIP (D 15), Thru1 (A 16), Thru3 (C 16), IDDATAOUT (B 18), ID+3.3V (D 18)  
 Fail VHFAC event1\_1 Neighboring Pins: dgsc1 (A 13), nc (C 13), Gnd (B 15), Gnd (D 15), Gnd (A 16), Gnd (C 16), nc (B 18), event1\_1 (D 18)  
 Pass VHFAC event1\_2 (9 D 20) pin 2 DUT relay stuck closed  
 Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), event1\_2 (VHFAC, 9 D 20),.000,.526,.526,Pass  
 Pass VHFAC event1\_3 (9 D 22) pin 2 DUT relay stuck closed  
 Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), event1\_3 (VHFAC, 9 D 22),.000,.517,.517,Pass  
 Pass VHFAC event1\_4 (9 D 24) pin 2 DUT relay stuck closed  
 Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), event1\_4 (VHFAC, 9 D 24),.000,.521,.521,Pass  
 Pass VHFAC event1\_5 (9 D 26) pin 2 DUT relay stuck closed

Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), event1\_5 (VHFAC, 9 D 26),.000,.528,.528,Pass  
 Pass VHFAC event1\_6 (9 D 28) pin 2 DUT relay stuck closed  
 Pass VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), event1\_6 (VHFAC, 9 D 28),.000,.527,.527,Pass  
 Pass VHFAC event1\_7 (9 D 30) pin 2 DUT relay stuck closed  
 Fail VHFAC DC\_CAL\_HS (SupportBoard, 20 A 13), event1\_7 (VHFAC, 9 D 30),.000,.001,.001,Fail  
 Fail VHFAC DC\_CAL\_HS Neighboring Pins: DC\_CAL\_HS (A 13), DC\_CAL\_LG4 (C 13), DIGITAL\_SYNC1 (B 15), TIP (D 15), Thru1 (A 16), Thru3 (C 16), IDDATAOUT (B 18), ID+3.3V (D 18)  
 Fail VHFAC event1\_7 Neighboring Pins: acccap2+ (A 25), event0\_4 (C 25), Gnd (B 27), Gnd (D 27), Gnd (A 28), Gnd (C 28), nc (B 30), event1\_7 (D 30)  
 Info VHFAC Test finished

09/08/2022;12:40:41;Performance Check;1;By System;Verbosity = ALL

1; Continuity; SupportBoard-0; Utility Bits; Pass; ; ; 09/08/2022  
 12:41:04  
 1; Continuity; SupportBoard-0; User Power; Pass; ; ; 09/08/2022  
 12:41:04  
 1; Continuity; SupportBoard-USM; Utility Bits; Pass; ; ; 09/08/2022  
 12:41:27  
 1; Continuity; SupportBoard-USM; User Power; Pass; ; ; 09/08/2022  
 12:41:27  
 1; Continuity; BBAC-15; Slot 1; Pass; ; ; 09/08/2022 12:41:52  
 1; Continuity; BBAC-15; Slot 13; Pass; ; ; 09/08/2022 12:41:52  
 1; Continuity; DC-30; Slot 3; Pass; ; ; 09/08/2022 12:44:46  
 1; Continuity; DC-30; Slot 5; Pass; ; ; 09/08/2022 12:44:46  
 1; Continuity; DC-30; Slot 9; Pass; ; ; 09/08/2022 12:44:46  
 1; Continuity; DC-30; Slot 12; Pass; ; ; 09/08/2022 12:44:46  
 1; Continuity; DC-90; Slot 2; Pass; ; ; 09/08/2022 12:49:56  
 1; Continuity; DC-90; Slot 10; Pass; ; ; 09/08/2022 12:49:56  
 1; Continuity; DC-90; Slot 11; Pass; ; ; 09/08/2022 12:49:56  
 1; Continuity; HSD-200; Slot 8; Pass; ; ; 09/08/2022 12:50:03  
 1; Continuity; HVD-1; Slot 4; Pass; ; ; 09/08/2022 12:51:23  
 1; Continuity; VHFAC; Slot 6; Fail; ; Check VHFAC Cables or Pins or Instrument or  
 TesterConfig.txt file; 09/08/2022 12:52:45

DATE: 09/08/2022

TIME: 12:40:41

TESTER: AGRMFLE

DRIVE C TOTAL SPACE: 14,998 MB DRIVE C UNUSED SPACE: 3,305 MB

IG-XL VERSION: 5.10.20\_fix (P16) IG-XL BUILD: 02.09.07.17.21

TEST MODE: Performance Check LOOP: By System

\*\*\*\*\* SYSTEM CHECKS \*\*\*\*\*

Name	S/N	Part/All	Total	Pass	Fail	Invalid
Continuity		All	1 0	1	0	
SignalDeliveryTwr			0 0	0	0	

\*\*\*\*\* OPTION CHECKS \*\*\*\*\*

Slot	Option_Name	S/N	Part/All	Total	Pass	Fail	Invalid
1	BBAC-15	0305B7F4		0	0	0	0
13	BBAC-15	03028AE8		0	0	0	0

\*\*\*\*\* CONFIGURATION \*\*\*\*\*

Slot	Option_Name	Assembly	Part_Number	S/N	Rev_Date
1.0	BBAC-15	High-Level	805-004-50	0305B7F4	1041-E
	BBAC-15	Primary	949-977-03	0305B142	0819-E
	BBAC-15Rider	Primary	949-987-02	03058D76	0739-D
	PowerBoard	Primary	949-984-04	0305B05A	0949-E
2.0	DC-90	High-Level	805-229-50	0C156CAE	1225-C
	DC-90	Primary	939-229-06	0C156AC2	1150-C
	PowerBoard	Primary	939-228-00	0C17D17D	1225-D
	Sub		949-972-25	0C17DF4F	1206-E
2.1	DC-90PowerSupply	High-Level	805-230-30	002B4735	0439-B
	DC-90PowerSupply	Primary	939-230-03	002AF137	0446-B
	Sub		939-231-03	002AF186	0436-B
3.0	DC-30	High-Level	805-002-60	0600ECB3	1133-B
	DC-30	Primary	949-901-60	0600EB86	0808-B
	DC-30Rider	Primary	949-904-60	0600EBE5	0808-B
	PowerBoard	Primary	949-955-02	0600D208	0531-E
	Sub		949-972-00	0600FFEF	0313-C
4.0	HVD-1	High-Level	805-740-50	0C33DE1A	1735-C
	HVD-1	Primary	939-273-00	0C34AEBB	1042-C
4.1	HVDModuleBoard-0	High-Level	805-740-50	0C33DE1A	1735-C
	HVDModuleBoard-0	Primary	939-274-10	0C34E2A2	1735-C
4.2	HVDModuleBoard-1	High-Level	805-740-50	0C33DE1A	1735-C
	HVDModuleBoard-1	Primary	939-274-10	0C34E2AA	1735-C
4.3	HVDModuleBoard-2	High-Level	805-740-50	0C33DE1A	1735-C
	HVDModuleBoard-2	Primary	939-274-10	0C34E294	1735-C
5.0	DC-30	High-Level	805-002-60	0C0178E8	1206-E
	DC-30	Primary	949-901-60	0C0178A3	1205-E
	DC-30Rider	Primary	949-904-60	0C01B89F	1205-E
	PowerBoard	Primary	949-955-02	0C0177E3	0644-E
	Sub		949-972-25	0C01B6B6	1206-E
6.0	VHFAC	High-Level	805-245-50	03012B8E	0707-E
	VHFAC	Primary	939-245-10	03012826	0707-E
	VHFACCapture0	Primary	939-244-00	030127F8	0552-C
	VHFACCapture1	Primary	939-244-00	030127F8	0552-C
	VHFACSource4	Primary	939-247-10	0301BE08	0613-C
	VHFACSource5	Primary	939-247-10	0301BE08	0613-C
	VHFACTime	Primary	939-245-10	03012826	0707-E
7.0	SupportBoard	High-Level	805-003-50	0C0FA9AF	1223-B

	SupportBoard	Primary	949-909-00	OC0F999F	1223-B
	DCCalModule	Primary	949-938-12	OC0F97CA	0912-C
	PowerBoard	Primary	949-910-30	OC0F96AA	1121-C
		Sub	949-972-25	OC11351A	0544-E
7.1	DSP-1	High-Level	810-503-00	03031F22	0423-B
	DSP-1	Primary	949-939-00	03031F22	0423-B
8.0	HSD-200	High-Level	805-251-50	OC006192	1231-D
	HSD-200	Primary	939-251-30	OC00458C	1231-D
	PowerBoard	Primary	939-253-01	OC0048F7	1037-C
		Sub	949-972-25	OC0046B2	1206-E
9.0	DC-30	High-Level	805-002-60	03014654	1206-B
	DC-30	Primary	949-901-60	0301AD3A	0808-B
	PowerBoard	Primary	949-955-02	0301AD58	0531-E
		Sub	949-972-25	03014153	1206-E
	DC-30Rider	Primary	949-904-60	03018EE6	0808-B
10.0	DC-90	High-Level	805-229-50	OC311FAE	1233-C
	DC-90	Primary	939-229-06	OC311EDE	1150-C
	PowerBoard	Primary	939-228-00	OC310B72	1225-D
		Sub	609-082-00	OC3109B1	1116-A
10.1	DC-90PowerSupply	High-Level	805-230-06	OC1910C9	1115-B
	DC-90PowerSupply	Primary	939-230-06	OC191B48	1115-B
		Sub	939-231-06	OC191B92	1115-B
11.0	DC-90	High-Level	805-229-50	OC130F47	1225-C
	DC-90	Primary	939-229-06	OC125A72	1127-C
	PowerBoard	Primary	939-228-00	OC12CAF8	1225-D
		Sub	949-972-25	OC13FE9F	1206-E
11.1	DC-90PowerSupply	High-Level	805-230-30	002B4816	0439-B
	DC-90PowerSupply	Primary	939-230-03	002AF13E	0446-B
		Sub	939-231-03	002ADA44	0436-B
12.0	DC-30	High-Level	805-002-60	OC351261	1505-E
	DC-30	Primary	949-901-60	OC35D71B	1418-E
	PowerBoard	Primary	949-955-02	OC35D7DE	1505-E
		Sub	609-082-02	OC35E2CC	1421-A
	DC-30Rider	Primary	949-904-60	OC360002	1418-E
13.0	BBAC-15	High-Level	805-004-50	03028AE8	1041-E
	BBAC-15	Primary	949-977-03	0302695A	0819-E
	BBAC-15Rider	Primary	949-987-02	030289A7	0739-D
	PowerBoard	Primary	949-984-04	03026966	0633-E
		Sub	949-972-25	03024856	0544-E
20.0	SupportBoard-USM	High-Level	939-270-00	002AF110	0549-B
	SupportBoard-USM	Primary	939-270-00	002AF110	0549-B

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