%JOB_START - Beginning Channel_Board Quick Check test on slot 0 at 2:15:03 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Started IdProm Test
- Completed IdProm Test
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On
- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram
- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram
- Starting TG_SVM_Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram
- Completed TG_Tset_LkDwn_Ram
- Starting TG_Edge_Ram
- Completed TG_Edge_Ram
- Starting TG_Format_Ram
- Completed TG_Format_Ram

- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 0 (S/N:802031C) at 2:15:55 PM

%JOB_START - Beginning Channel_Board Quick Check test on slot 1 at 2:16:02 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Started IdProm Test
- Completed IdProm Test
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On
- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram
- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram

- Starting TG_SVM_Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram
- Completed TG_Tset_LkDwn_Ram
- Starting TG_Edge_Ram
- Completed TG_Edge_Ram
- Starting TG_Format_Ram
- Completed TG_Format_Ram
- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 1 (S/N:C004DB5) at 2:16:53 PM

%JOB_START - Beginning Channel_Board Quick Check test on slot 2 at 2:17:00 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Started IdProm Test
- Completed IdProm Test
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On

- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram
- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram
- Starting TG_SVM_Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram
- Completed TG_Tset_LkDwn_Ram
- Starting TG_Edge_Ram
- Completed TG_Edge_Ram
- Starting TG_Format_Ram
- Completed TG_Format_Ram
- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 2 (S/N:5007DC3) at 2:17:52 PM

%JOB_START - Beginning Channel_Board Quick Check test on slot 3 at 2:17:59 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Started IdProm Test
- Completed IdProm Test
- Starting PG_History_Ram
- Completed PG_History_Ram

- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On
- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram
- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram
- Starting TG_SVM_Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram
- Completed TG_Tset_LkDwn_Ram
- Starting TG_Edge_Ram
- Completed TG_Edge_Ram
- Starting TG_Format_Ram
- Completed TG_Format_Ram
- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 3 (S/N:50085B4) at 2:18:50 PM

%JOB_START - Beginning Channel_Board Quick Check test on slot 4 at 2:18:58 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Started IdProm Test
- Completed IdProm Test
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On
- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram
- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram
- Starting TG_SVM_Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram
- Completed TG_Tset_LkDwn_Ram
- Starting TG_Edge_Ram
- Completed TG_Edge_Ram
- Starting TG_Format_Ram
- Completed TG_Format_Ram

- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 4 (S/N:C0B2DC7) at 2:19:49 PM

%JOB_START - Beginning Channel_Board Quick Check test on slot 5 at 2:19:56 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Started IdProm Test
- Completed IdProm Test
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On
- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram
- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram

- Starting TG_SVM_Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram
- Completed TG_Tset_LkDwn_Ram
- Starting TG_Edge_Ram
- Completed TG_Edge_Ram
- Starting TG_Format_Ram
- Completed TG_Format_Ram
- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 5 (S/N:C067944) at 2:20:48 PM

%JOB_START - Beginning Channel_Board Quick Check test on slot 6 at 2:20:55 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Started IdProm Test
- Completed IdProm Test
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On

- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram
- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram
- Starting TG_SVM_Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram
- Completed TG_Tset_LkDwn_Ram
- Starting TG_Edge_Ram
- Completed TG_Edge_Ram
- Starting TG_Format_Ram
- Completed TG_Format_Ram
- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 6 (S/N:5009FD3) at 2:21:46 PM

%JOB_START - Beginning Channel_Board Quick Check test on slot 7 at 2:21:53 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Started IdProm Test
- Completed IdProm Test
- Starting PG_History_Ram
- Completed PG_History_Ram

- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On
- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram
- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram
- Starting TG_SVM_Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram
- Completed TG_Tset_LkDwn_Ram
- Starting TG_Edge_Ram
- Completed TG_Edge_Ram
- Starting TG_Format_Ram
- Completed TG_Format_Ram
- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 7 (S/N:8025A4A) at 2:22:45 PM

%JOB_START - Beginning Relay_Board_Lower Quick Check test on slot 0 at 2:22:52 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 0 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Lower Quick Check of slot 0 at 2:22:53 PM

%JOB_START - Beginning Relay_Board_Lower Quick Check test on slot 1 at 2:23:00 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 1 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Lower Quick Check of slot 1 at 2:23:00 PM

%JOB_START - Beginning Relay_Board_Lower Quick Check test on slot 2 at 2:23:07 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 2 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Lower Quick Check of slot 2 at 2:23:08 PM

%JOB_START - Beginning Relay_Board_Lower Quick Check test on slot 3 at 2:23:15 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------

- Running Relay Checker Ver 1.03 on AG012 in Slot 3 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Lower Quick Check of slot 3 at 2:23:16 PM

%JOB_START - Beginning Relay_Board_Lower Quick Check test on slot 4 at 2:23:23 PM on
8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 4 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Lower Quick Check of slot 4 at 2:23:24 PM

%JOB_START - Beginning Relay_Board_Lower Quick Check test on slot 5 at 2:23:31 PM on
8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 5 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Lower Quick Check of slot 5 at 2:23:31 PM

%JOB_START - Beginning Relay_Board_Lower Quick Check test on slot 6 at 2:23:39 PM on
8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 6 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Lower Quick Check of slot 6 at 2:23:39 PM

%JOB_START - Beginning Relay_Board_Lower Quick Check test on slot 7 at 2:23:46 PM on
8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------

%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 7 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Lower Quick Check of slot 7 at 2:23:47 PM

%JOB_START - Beginning Relay_Board_Upper Quick Check test on slot 0 at 2:23:54 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 0 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Upper Quick Check of slot 0 at 2:23:55 PM

%JOB_START - Beginning Relay_Board_Upper Quick Check test on slot 1 at 2:24:02 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 1 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Upper Quick Check of slot 1 at 2:24:02 PM

%JOB_START - Beginning Relay_Board_Upper Quick Check test on slot 2 at 2:24:10 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 2 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Upper Quick Check of slot 2 at 2:24:10 PM

%JOB_START - Beginning Relay_Board_Upper Quick Check test on slot 3 at 2:24:17 PM on

8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 3 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Upper Quick Check of slot 3 at 2:24:18 PM

%JOB_START - Beginning Relay_Board_Upper Quick Check test on slot 4 at 2:24:25 PM on
8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 4 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Upper Quick Check of slot 4 at 2:24:26 PM

%JOB_START - Beginning Relay_Board_Upper Quick Check test on slot 5 at 2:24:33 PM on
8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 5 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Upper Quick Check of slot 5 at 2:24:33 PM

%JOB_START - Beginning Relay_Board_Upper Quick Check test on slot 6 at 2:24:41 PM on
8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 6 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Upper Quick Check of slot 6 at 2:24:41 PM

%JOB_START - Beginning Relay_Board_Upper Quick Check test on slot 7 at 2:24:48 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 7 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Upper Quick Check of slot 7 at 2:24:49 PM

%JOB_START - Beginning MTO Quick Check test on slot 0 at 2:24:56 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Started IdProm Test
- Completed MTO IdProm Test
- ------ Performing MTO tests board type 23901402 ------
- TCIO 0 Connection
- TCIO 0 First Pass
- TCIO 0 Second Pass
- TCIO 1 Connection
- TCIO 1 First Pass
- TCIO 1 Second Pass
- TCIO 1 Third Pass
- MPG asic registers
- Mem asic registers
- MapMem Bist
- Long Mode
- CapMem Bist
- Starting MTO_CAPTUREMEM long BIST test
- Completed MTO_CAPTUREMEM_BIST test
- MapMem Ram
- MapMem MemTest 1
- MapMem MemTest 2
- CapMem Ram
- CapMem Data1Check
- Walking 1 across data lines (0-47)
- CapMem AddressCheck

- Walking 1 across address lines (22..0)
- XYScramble Ram
- Write scramData1 across address lines
- Walking 1 across address lines
- Walking 0 across address lines
- Walking 1 across data lines
- MTO SVM to Immediate Window & Test
- Data Test (104 bits)
- Address Test (10 bits)
- Mem Mode: Build Fail Image
- Mem Mode: Accumulate Fail Test
- Mem Mode: Build Expect
- Mem Mode: Build Expect in Double
- Mem Mode: Compress test
- MTO_XYSCRAM_CAPTURE test
- MTO_AdbToDut test
- MTO Utility Counters
- MTO Coincidence test
- MTO LFG test
- Mem Mode: Build From Capture
- MTO Map Mem source x16
- MTO Map Mem source x8
- MTO Map Mem source x2
- MTO Map Mem source x1

%JOB_END - ****PASSED**** MTO Quick Check of slot 0 (S/N:C0B8374) at 2:25:18 PM

%JOB_START - Beginning MTO Quick Check test on slot 1 at 2:25:25 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Started IdProm Test
- Completed MTO IdProm Test
- ------ Performing MTO tests board type 23901402 ------
- TCIO 0 Connection
- TCIO 0 First Pass
- TCIO 0 Second Pass
- TCIO 1 Connection
- TCIO 1 First Pass
- TCIO 1 Second Pass
- TCIO 1 Third Pass
- MPG asic registers
- Mem asic registers
- MapMem Bist

- Long Mode
- CapMem Bist
- Starting MTO_CAPTUREMEM long BIST test
- Completed MTO_CAPTUREMEM_BIST test
- MapMem Ram
- MapMem MemTest 1
- MapMem MemTest 2
- CapMem Ram
- CapMem Data1Check
- Walking 1 across data lines (0-47)
- CapMem AddressCheck
- Walking 1 across address lines (22..0)
- XYScramble Ram
- Write scramData1 across address lines
- Walking 1 across address lines
- Walking 0 across address lines
- Walking 1 across data lines
- MTO SVM to Immediate Window & Test
- Data Test (104 bits)
- Address Test (10 bits)
- Mem Mode: Build Fail Image
- Mem Mode: Accumulate Fail Test
- Mem Mode: Build Expect
- Mem Mode: Build Expect in Double
- Mem Mode: Compress test
- MTO_XYSCRAM_CAPTURE test
- MTO_AdbToDut test
- MTO Utility Counters
- MTO Coincidence test
- MTO LFG test
- Mem Mode: Build From Capture
- MTO Map Mem source x16
- MTO Map Mem source x8
- MTO Map Mem source x2
- MTO Map Mem source x1

%JOB_END - ****PASSED**** MTO Quick Check of slot 1 (S/N:C0B8390) at 2:25:47 PM

%JOB_START - Beginning MTO Quick Check test on slot 4 at 2:25:54 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Started IdProm Test
- Completed MTO IdProm Test

- ------ Performing MTO tests board type 23901402 ------
- TCIO 0 Connection
- TCIO 0 First Pass
- TCIO 0 Second Pass
- TCIO 1 Connection
- TCIO 1 First Pass
- TCIO 1 Second Pass
- TCIO 1 Third Pass
- MPG asic registers
- Mem asic registers
- MapMem Bist
- Long Mode
- CapMem Bist
- Starting MTO_CAPTUREMEM long BIST test
- Completed MTO_CAPTUREMEM_BIST test
- MapMem Ram
- MapMem MemTest 1
- MapMem MemTest 2
- CapMem Ram
- CapMem Data1Check
- Walking 1 across data lines (0-47)
- CapMem AddressCheck
- Walking 1 across address lines (22..0)
- XYScramble Ram
- Write scramData1 across address lines
- Walking 1 across address lines
- Walking 0 across address lines
- Walking 1 across data lines
- MTO SVM to Immediate Window & Test
- Data Test (104 bits)
- Address Test (10 bits)
- Mem Mode: Build Fail Image
- Mem Mode: Accumulate Fail Test
- Mem Mode: Build Expect
- Mem Mode: Build Expect in Double
- Mem Mode: Compress test
- MTO_XYSCRAM_CAPTURE test
- MTO_AdbToDut test
- MTO Utility Counters
- MTO Coincidence test
- MTO LFG test
- Mem Mode: Build From Capture
- MTO Map Mem source x16
- MTO Map Mem source x8
- MTO Map Mem source x2
- MTO Map Mem source x1

%JOB_END - ****PASSED**** MTO Quick Check of slot 4 (S/N:C0B838C) at 2:26:15 PM

%JOB_START - Beginning MTO Quick Check test on slot 5 at 2:26:22 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Started IdProm Test
- Completed MTO IdProm Test
- ------ Performing MTO tests board type 23901402 ------
- TCIO 0 Connection
- TCIO 0 First Pass
- TCIO 0 Second Pass
- TCIO 1 Connection
- TCIO 1 First Pass
- TCIO 1 Second Pass
- TCIO 1 Third Pass
- MPG asic registers
- Mem asic registers
- MapMem Bist
- Long Mode
- CapMem Bist
- Starting MTO_CAPTUREMEM long BIST test
- Completed MTO_CAPTUREMEM_BIST test
- MapMem Ram
- MapMem MemTest 1
- MapMem MemTest 2
- CapMem Ram
- CapMem Data1Check
- Walking 1 across data lines (0-47)
- CapMem AddressCheck
- Walking 1 across address lines (22..0)
- XYScramble Ram
- Write scramData1 across address lines
- Walking 1 across address lines
- Walking 0 across address lines
- Walking 1 across data lines
- MTO SVM to Immediate Window & Test
- Data Test (104 bits)
- Address Test (10 bits)
- Mem Mode: Build Fail Image
- Mem Mode: Accumulate Fail Test
- Mem Mode: Build Expect
- Mem Mode: Build Expect in Double
- Mem Mode: Compress test
- MTO_XYSCRAM_CAPTURE test

- MTO_AdbToDut test
- MTO Utility Counters
- MTO Coincidence test
- MTO LFG test
- Mem Mode: Build From Capture
- MTO Map Mem source x16
- MTO Map Mem source x8
- MTO Map Mem source x2
- MTO Map Mem source x1

%JOB_END - ****PASSED**** MTO Quick Check of slot 5 (S/N:23C8FC) at 2:26:44 PM

%JOB_START - Beginning CTO Quick Check test on slot 17 at 2:26:51 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Performing register test...
- Completed register test.
- Performing idprom and temperature test...
- Completed idprom and temperature test.
- Performing PG test...
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- Completed PG test.
- Performing internal loopback test...
- Completed internal loopback test.
- Performing local reference test...
- Completed local reference test.
- Performing internal Capture burst test...
- Completed internal Capture burst test.
- Performing internal loopback burst test...
- Completed internal loopback burst test.

%JOB_END - ****PASSED**** CTO Quick Check of slot 17 (S/N:5008EA3) at 2:27:15 PM

%JOB_START - Beginning DPS Quick Check test on slot 21 at 2:27:23 PM on 8/26/2022

Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Verifying DPS Current Leakage
- Channel 0
- Channel 1
- Channel 2
- Channel 3
- Channel 4
- Channel 5
- Channel 6
- Channel 7
- Finished Verifying DPS Current Leakage

%JOB_END - ****PASSED**** DPS Quick Check of slot 21 (S/N:5008BEC) at 2:27:25 PM

%JOB_START - Beginning DPS Quick Check test on slot 22 at 2:27:32 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Verifying DPS Current Leakage
- Channel 0
- Channel 1
- Channel 2
- Channel 3
- Channel 4

%FAIL - DPS Current Leakage Test (50uA Range (16 averages) Using 9.5v): Slot 22, Chan 4, FullScale
Measured: -5.518E-06 low limit: -0.0000005 high limit: 0.0000005

- Channel 5
- Channel 6
- Channel 7
- Finished Verifying DPS Current Leakage

%JOB_END - ****FAILED**** DPS Quick Check of slot 22 (S/N:8011BD8) at 2:27:35 PM

%JOB_START - Beginning DPS Quick Check test on slot 23 at 2:27:42 PM on 8/26/2022

Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Verifying DPS Current Leakage
- Channel 0
- Channel 1
- Channel 2
- Channel 3
- Channel 4
- Channel 5
- Channel 6
- Channel 7
- Finished Verifying DPS Current Leakage

%JOB_END - ****PASSED**** DPS Quick Check of slot 23 (S/N:802F900) at 2:27:44 PM

%JOB_START - Beginning DPS Quick Check test on slot 24 at 2:27:51 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Verifying DPS Current Leakage
- Channel 0
- Channel 1
- Channel 2
- Channel 3
- Channel 4
- Channel 5
- Channel 6
- Channel 7
- Finished Verifying DPS Current Leakage

%JOB_END - ****PASSED**** DPS Quick Check of slot 24 (S/N:500268E) at 2:27:54 PM

%JOB_START - Beginning LMF Quick Check test on slot 15 at 2:28:35 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13)
PCIT in DMA mode

%INFO - ----------

%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------

LMF Mother Board Test
Testing JBC
Testing ID EEPROM
- Company Teradyne
- Board Type 23904605
- Revision 834C
- Serial Number 2AEDF8
Testing Channel 0 Capture FPGA
Testing Channel 0 Capture DSP Memory
Testing Channel 0 Capture Memory 0
Testing Channel 0 Capture Memory 1
Testing Channel 0 Source FPGA
Testing Channel 0 Source Memory
Testing Channel 0 Source Seg Memory
Testing Channel 0 Channel Control FPGA
Testing Channel 1 Capture FPGA
Testing Channel 1 Capture DSP Memory
Testing Channel 1 Capture Memory 0
Testing Channel 1 Capture Memory 1
Testing Channel 1 Source FPGA
Testing Channel 1 Source Memory
Testing Channel 1 Source Seg Memory
Testing Channel 1 Channel Control FPGA
Testing Channel 2 Capture FPGA
Testing Channel 2 Capture DSP Memory
Testing Channel 2 Capture Memory 0
Testing Channel 2 Capture Memory 1
Testing Channel 2 Source FPGA
Testing Channel 2 Source Memory
Testing Channel 2 Source Seg Memory
Testing Channel 2 Channel Control FPGA
Testing Channel 3 Capture FPGA
Testing Channel 3 Capture DSP Memory
Testing Channel 3 Capture Memory 0
Testing Channel 3 Capture Memory 1
Testing Channel 3 Source FPGA
Testing Channel 3 Source Memory
Testing Channel 3 Source Seg Memory
Testing Channel 3 Channel Control FPGA
loading DSP programs for RAM tests ...
testing DSP internal RAMs ...
Testing Channel 0 Capture DSP Internal RAM
Testing Channel 1 Capture DSP Internal RAM

Testing Channel 2 Capture DSP Internal RAM
Testing Channel 3 Capture DSP Internal RAM
testing memories with DSP access (45 sec)
in the meantime ...
Testing Board Control FPGA
Testing Pattern Generator
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
Testing LVM
Testing Channel 0 DLUT FPGA
Testing Channel 0 DLUT Capture Mem
Testing Channel 0 DLUT LF SRAM
Testing Channel 0 DLUT MF SRAM
Testing Channel 0 SVM (DMA)
Testing Channel 1 DLUT FPGA
Testing Channel 1 DLUT Capture Mem
Testing Channel 1 DLUT LF SRAM
Testing Channel 1 DLUT MF SRAM
Testing Channel 1 SVM (DMA)
Testing Channel 2 DLUT FPGA
Testing Channel 2 DLUT Capture Mem
Testing Channel 2 DLUT LF SRAM
Testing Channel 2 DLUT MF SRAM
Testing Channel 2 SVM (DMA)
Testing Channel 3 DLUT FPGA
Testing Channel 3 DLUT Capture Mem
Testing Channel 3 DLUT LF SRAM
Testing Channel 3 DLUT MF SRAM
Testing Channel 3 SVM (DMA)
waiting for DSP test end ...
Testing Channel 0 Capture Memories with DSP Access
Testing Channel 1 Capture Memories with DSP Access
Testing Channel 2 Capture Memories with DSP Access
Testing Channel 3 Capture Memories with DSP Access
DSP test done

LMF Mother Board Test done

- ASIO Quick Check Analog Tests - Begin

- %INFO: Reading LMF Serial Numbers

- Mother Board = 2AEDF8
- Relay Board = 2AE7DB
- Cal Rider Board = 2AE6F4
- Source Board CH's 0 and 1 = 2AE912

- Source Board CH's 2 and 3 = 2AE8F3
- Capture Board CH's 0 and 1 = 2AF0AA
- Capture Board CH's 2 and 3 = 2AF0B1

- %INFO: Logging Mother Board Revision
- Mother Board = 23904605

- %INFO: Logging FPGA Revision's

- BOARD CONTROL = 2
- CHANNEL CONTROL = 2
- SOURCE = 7
- CAPTURE = 7
- DLUT = 11
- JBC REV = 5

- Reading Zener Reference value in EEPROM...
- Continuity Test...

- LF 20K SRC Filter CH 0 to LF 45K CAP Filter CH 0
- LF 20K SRC Filter CH 0 to LF 80K CAP Filter CH 0
- LF 20K SRC Filter CH 0 to LF 6M CAP Filter CH 0
- LF 100K SRC Filter CH 0 to LF 45K CAP Filter CH 0
- LF 100K SRC Filter CH 0 to LF 80K CAP Filter CH 0
- LF 100K SRC Filter CH 0 to LF 6M CAP Filter CH 0

- LF 20K SRC Filter CH 0 to MF LO 45K CAP Filter CH 0
- LF 20K SRC Filter CH 0 to MF LO 80K CAP Filter CH 0
- LF 20K SRC Filter CH 0 to MF LO 6M CAP Filter CH 0
- LF 100K SRC Filter CH 0 to MF LO 45K CAP Filter CH 0
- LF 100K SRC Filter CH 0 to MF LO 80K CAP Filter CH 0
- LF 100K SRC Filter CH 0 to MF LO 6M CAP Filter CH 0

- MF LO 20K SRC Filter CH 0 to MF LO 45K CAP Filter CH 0
- MF LO 20K SRC Filter CH 0 to MF LO 80K CAP Filter CH 0
- MF LO 20K SRC Filter CH 0 to MF LO 6M CAP Filter CH 0
- MF LO 100K SRC Filter CH 0 to MF LO 45K CAP Filter CH 0
- MF LO 100K SRC Filter CH 0 to MF LO 80K CAP Filter CH 0
- MF LO 100K SRC Filter CH 0 to MF LO 6M CAP Filter CH 0

- MF HI 6M SRC Filter CH 0 to MF LO 45K CAP Filter CH 0
- MF HI 6M SRC Filter CH 0 to MF LO 80K CAP Filter CH 0
- MF HI 6M SRC Filter CH 0 to MF LO 6M CAP Filter CH 0
- MF HI 20M SRC Filter CH 0 to MF LO 45K CAP Filter CH 0
- MF HI 20M SRC Filter CH 0 to MF LO 80k CAP Filter CH 0
- MF HI 20M SRC Filter CH 0 to MF LO 6M CAP Filter CH 0

- LF 20K SRC Filter CH 1 to LF 45K CAP Filter CH 1

- LF 20K SRC Filter CH 1 to LF 80K CAP Filter CH 1
- LF 20K SRC Filter CH 1 to LF 6M CAP Filter CH 1
- LF 100K SRC Filter CH 1 to LF 45K CAP Filter CH 1
- LF 100K SRC Filter CH 1 to LF 80K CAP Filter CH 1
- LF 100K SRC Filter CH 1 to LF 6M CAP Filter CH 1

- LF 20K SRC Filter CH 1 to MF LO 45K CAP Filter CH 1
- LF 20K SRC Filter CH 1 to MF LO 80K CAP Filter CH 1
- LF 20K SRC Filter CH 1 to MF LO 6M CAP Filter CH 1
- LF 100K SRC Filter CH 1 to MF LO 45K CAP Filter CH 1
- LF 100K SRC Filter CH 1 to MF LO 80K CAP Filter CH 1
- LF 100K SRC Filter CH 1 to MF LO 6M CAP Filter CH 1

- MF LO 20K SRC Filter CH 1 to MF LO 45K CAP Filter CH 1
- MF LO 20K SRC Filter CH 1 to MF LO 80K CAP Filter CH 1
- MF LO 20K SRC Filter CH 1 to MF LO 6M CAP Filter CH 1
- MF LO 100K SRC Filter CH 1 to MF LO 45K CAP Filter CH 1
- MF LO 100K SRC Filter CH 1 to MF LO 80K CAP Filter CH 1
- MF LO 100K SRC Filter CH 1 to MF LO 6M CAP Filter CH 1

- MF HI 6M SRC Filter CH 1 to MF LO 45K CAP Filter CH 1
- MF HI 6M SRC Filter CH 1 to MF LO 80K CAP Filter CH 1
- MF HI 6M SRC Filter CH 1 to MF LO 6M CAP Filter CH 1
- MF HI 20M SRC Filter CH 1 to MF LO 45K CAP Filter CH 1
- MF HI 20M SRC Filter CH 1 to MF LO 80k CAP Filter CH 1
- MF HI 20M SRC Filter CH 1 to MF LO 6M CAP Filter CH 1

- LF 20K SRC Filter CH 2 to LF 45K CAP Filter CH 2
- LF 20K SRC Filter CH 2 to LF 80K CAP Filter CH 2
- LF 20K SRC Filter CH 2 to LF 6M CAP Filter CH 2
- LF 100K SRC Filter CH 2 to LF 45K CAP Filter CH 2
- LF 100K SRC Filter CH 2 to LF 80K CAP Filter CH 2
- LF 100K SRC Filter CH 2 to LF 6M CAP Filter CH 2

- LF 20K SRC Filter CH 2 to MF LO 45K CAP Filter CH 2
- LF 20K SRC Filter CH 2 to MF LO 80K CAP Filter CH 2
- LF 20K SRC Filter CH 2 to MF LO 6M CAP Filter CH 2
- LF 100K SRC Filter CH 2 to MF LO 45K CAP Filter CH 2
- LF 100K SRC Filter CH 2 to MF LO 80K CAP Filter CH 2
- LF 100K SRC Filter CH 2 to MF LO 6M CAP Filter CH 2

- MF LO 20K SRC Filter CH 2 to MF LO 45K CAP Filter CH 2
- MF LO 20K SRC Filter CH 2 to MF LO 80K CAP Filter CH 2
- MF LO 20K SRC Filter CH 2 to MF LO 6M CAP Filter CH 2
- MF LO 100K SRC Filter CH 2 to MF LO 45K CAP Filter CH 2
- MF LO 100K SRC Filter CH 2 to MF LO 80K CAP Filter CH 2
- MF LO 100K SRC Filter CH 2 to MF LO 6M CAP Filter CH 2

- MF HI 6M SRC Filter CH 2 to MF LO 45K CAP Filter CH 2
- MF HI 6M SRC Filter CH 2 to MF LO 80K CAP Filter CH 2
- MF HI 6M SRC Filter CH 2 to MF LO 6M CAP Filter CH 2
- MF HI 20M SRC Filter CH 2 to MF LO 45K CAP Filter CH 2
- MF HI 20M SRC Filter CH 2 to MF LO 80k CAP Filter CH 2
- MF HI 20M SRC Filter CH 2 to MF LO 6M CAP Filter CH 2

- LF 20K SRC Filter CH 3 to LF 45K CAP Filter CH 3
- LF 20K SRC Filter CH 3 to LF 80K CAP Filter CH 3
- LF 20K SRC Filter CH 3 to LF 6M CAP Filter CH 3
- LF 100K SRC Filter CH 3 to LF 45K CAP Filter CH 3
- LF 100K SRC Filter CH 3 to LF 80K CAP Filter CH 3
- LF 100K SRC Filter CH 3 to LF 6M CAP Filter CH 3

- LF 20K SRC Filter CH 3 to MF LO 45K CAP Filter CH 3
- LF 20K SRC Filter CH 3 to MF LO 80K CAP Filter CH 3
- LF 20K SRC Filter CH 3 to MF LO 6M CAP Filter CH 3
- LF 100K SRC Filter CH 3 to MF LO 45K CAP Filter CH 3
- LF 100K SRC Filter CH 3 to MF LO 80K CAP Filter CH 3
- LF 100K SRC Filter CH 3 to MF LO 6M CAP Filter CH 3

- MF LO 20K SRC Filter CH 3 to MF LO 45K CAP Filter CH 3
- MF LO 20K SRC Filter CH 3 to MF LO 80K CAP Filter CH 3
- MF LO 20K SRC Filter CH 3 to MF LO 6M CAP Filter CH 3
- MF LO 100K SRC Filter CH 3 to MF LO 45K CAP Filter CH 3
- MF LO 100K SRC Filter CH 3 to MF LO 80K CAP Filter CH 3
- MF LO 100K SRC Filter CH 3 to MF LO 6M CAP Filter CH 3

- MF HI 6M SRC Filter CH 3 to MF LO 45K CAP Filter CH 3
- MF HI 6M SRC Filter CH 3 to MF LO 80K CAP Filter CH 3
- MF HI 6M SRC Filter CH 3 to MF LO 6M CAP Filter CH 3
- MF HI 20M SRC Filter CH 3 to MF LO 45K CAP Filter CH 3
- MF HI 20M SRC Filter CH 3 to MF LO 80k CAP Filter CH 3
- MF HI 20M SRC Filter CH 3 to MF LO 6M CAP Filter CH 3
- LF Loopback Test...
- LF Loopback Test Source Channel 0 to LF Capture Channel 0
- LF Loopback Test Source Channel 1 to LF Capture Channel 1
- LF Loopback Test Source Channel 2 to LF Capture Channel 2
- LF Loopback Test Source Channel 3 to LF Capture Channel 3
- MF Loopback Test...
- MF Loopback Test Source Channel 0 to MF Capture Channel 0
- MF Loopback Test Source Channel 1 to MF Capture Channel 1
- MF Loopback Test Source Channel 2 to MF Capture Channel 2
- MF Loopback Test Source Channel 3 to MF Capture Channel 3
- Clock Pull Test...
- Testing LF Source Channel 0 to Capture Channel 0
- Testing LF Source Channel 1 to Capture Channel 1
- Testing LF Source Channel 2 to Capture Channel 2

- Testing LF Source Channel 3 to Capture Channel 3

- ASIO Quick Check Analog Tests - End

%JOB_END - ****PASSED**** LMF Quick Check of slot 15 (S/N:2AEDF8) at 2:29:55 PM

%JOB_START - Beginning systemwide tests at 3:26:05 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Systemwide functionality and continuity to slot 0
- Systemwide functionality and continuity to slot 1
- Systemwide functionality and continuity to slot 2
%FAIL - Slot 2 channel 33 loaded BPMU channel relay test at 0.5V
Measured: 10.447ohms high limit: 6.5ohms

%FAIL - Slot 2 channel 33 loaded PPMU channel relay test at 0.5V
Measured: 97.252ohms high limit: 12ohms

%FAIL - Slot 2 channel 33 loaded PE channel relay test at 0.5V
Measured: 191.943ohms high limit: 61ohms

%INFO - Note:
%INFO - Slot 2 channel 33 diagnosis: High Pogo to DIB resistance. Extern relays stuck open or open at Extern Force or Sense pogos (contained on AG012 relay boards).Bad DIB to Pogo Pin Contact can cause this failure. Reseating of DIB multiple times may fix this problem, If not then Inspect Pogo pins for damage or dirt. If necessary, clean DIB using Isopropyl Alcohol and lint free cloth. Do NOT use any solvent or cleaner on the pogo pins replace them as necessary to provide sufficient contact.
- Systemwide functionality and continuity to slot 3
- Systemwide functionality and continuity to slot 4
- Systemwide functionality and continuity to slot 5
%FAIL - Slot 5 channel 30 loaded BPMU channel relay test at 0.5V
Measured: 22.713ohms high limit: 6.5ohms

%FAIL - Slot 5 channel 30 unloaded PPMU channel relay test at 0.5V
Measured: 1.492E-02V low limit: 0.28V high limit: 0.72V

%FAIL - Slot 5 channel 30 unloaded PE channel relay test at 0.5V
Measured: 1.377E-02V low limit: 0.28V high limit: 0.72V

%FAIL - Slot 5 channel 30 PE relay closed TDR test
Measured: 2.109nS low limit: 4nS high limit: 5.95nS

%FAIL - Slot 5 channel 30 DIB relay closed TDR test
Measured: 0nS low limit: 11.05nS high limit: 13.55nS

%INFO - Note:
%INFO - Slot 5 channel 30 diagnosis: High pogo to DIB resistance or BPMU channel relay resistance. Extern relays stuck open or open at Extern Force or Sense pogos (contained on AG012 relay boards).Bad DIB to Pogo Pin Contact can cause this failure. Reseating of DIB multiple times may fix this problem, If not then Inspect Pogo pins for damage or dirt. If necessary, clean DIB using Isopropyl Alcohol and lint free cloth. Do NOT use any solvent or cleaner on the pogo pins replace them as necessary to provide sufficient contact.
- Systemwide functionality and continuity to slot 6
- Systemwide functionality and continuity to slot 7
- Starting BackPlane Fail Bus test
- Completed BackPlane Fail Bus test
- Completed Extra

%JOB_END - ****FAILED**** Systemwide tests at 3:27:24 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 2 at 3:29:04 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 2 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 2 at 3:29:04 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 5 at 3:29:12 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 5 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 5 at 3:29:12 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 2 at 3:29:20 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 2 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 2 at 3:29:20 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 5 at 3:29:27 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 5 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 5 at 3:29:28 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 2 at 3:29:35 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 2 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 2 at 3:29:36 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 5 at 3:29:43 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 5 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 5 at 3:29:44 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 2 at 3:29:51 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 2 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 2 at 3:29:52 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 5 at 3:29:59 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 5 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 5 at 3:29:59 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 2 at 3:30:07 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------

- Running Relay Checker Ver 1.03 on AG012 in Slot 2 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 2 at 3:30:07 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 5 at 3:30:14 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 5 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 5 at 3:30:15 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 2 at 3:30:22 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 2 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 2 at 3:30:23 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 5 at 3:30:30 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 5 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 5 at 3:30:31 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 2 at 3:30:38 PM on 8/26/2022

Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 2 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 2 at 3:30:39 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 5 at 3:30:46 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 5 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 5 at 3:30:46 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 2 at 3:30:54 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 2 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 2 at 3:30:54 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 5 at 3:31:02 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO -
---------- %
INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40

%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 5 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 5 at 3:31:02 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 2 at 3:31:09 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 2 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 2 at 3:31:10 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 5 at 3:31:17 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 5 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 5 at 3:31:18 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 2 at 3:31:25 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 2 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 2 at 3:31:26 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 5 at 3:31:33 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 5 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 5 at 3:31:33 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 2 at 3:31:41 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 2 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 2 at 3:31:41 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 5 at 3:31:49 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 5 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 5 at 3:31:49 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 2 at 3:31:57 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------

%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 2 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 2 at 3:31:57 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 5 at 3:32:04 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 5 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 5 at 3:32:05 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 2 at 3:32:12 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 2 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 2 at 3:32:13 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 5 at 3:32:20 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 5 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 5 at 3:32:21 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 2 at 3:32:28 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 2 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 2 at 3:32:28 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 5 at 3:32:36 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 5 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 5 at 3:32:36 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 2 at 3:32:44 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 2 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 2 at 3:32:44 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 5 at 3:32:51 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 5 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 5 at 3:32:52 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 2 at 3:32:59 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 2 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 2 at 3:33:00 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 5 at 3:33:07 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 5 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 5 at 3:33:08 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 2 at 3:33:15 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------

- Running Relay Checker Ver 1.03 on AG012 in Slot 2 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 2 at 3:33:16 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 5 at 3:33:23 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 5 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 5 at 3:33:23 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 2 at 3:33:31 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 2 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 2 at 3:33:31 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 5 at 3:33:38 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 5 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 5 at 3:33:39 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 2 at 3:33:46 PM on 8/26/2022

Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 2 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 2 at 3:33:47 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 5 at 3:33:54 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 5 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 5 at 3:33:55 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 2 at 3:34:02 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 2 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 2 at 3:34:03 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 5 at 3:34:10 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO -
---------- %
INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40

%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 5 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 5 at 3:34:10 PM

%JOB_START - Beginning Channel_Board_DIB Module Check test on slot 0 at 3:35:29 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Completed Commpare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.

- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus : Checking STB lines : Normal mode, 30MHz
- Statebus : Checking STB lines : Normal mode, 50MHz
- Statebus : Checking STB lines : Normal mode, 80MHz
- Statebus : Checking STB lines : Normal mode, 100MHz
- Statebus : Checking State number lines : Extended mode, 25MHz
- Statebus : Checking State number lines : Extended mode, 30MHz
- Statebus : Checking State number lines : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 25MHz

- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 30MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 80MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 0

%JOB_END - ****PASSED**** Channel_Board_DIB Module Check of slot 0 (S/N:802031C) at 3:39:53 PM

%JOB_START - Beginning Channel_Board_DIB Module Check test on slot 1 at 3:40:00 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests...

- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Completed Commpare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz

- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus : Checking STB lines : Normal mode, 30MHz
- Statebus : Checking STB lines : Normal mode, 50MHz
- Statebus : Checking STB lines : Normal mode, 80MHz
- Statebus : Checking STB lines : Normal mode, 100MHz
- Statebus : Checking State number lines : Extended mode, 25MHz
- Statebus : Checking State number lines : Extended mode, 30MHz
- Statebus : Checking State number lines : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 25MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 30MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 80MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 1

%JOB_END - ****PASSED**** Channel_Board_DIB Module Check of slot 1 (S/N:C004DB5) at 3:44:24 PM

%JOB_START - Beginning Channel_Board_DIB Module Check test on slot 2 at 3:44:31 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Completed Commpare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.

- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus : Checking STB lines : Normal mode, 30MHz
- Statebus : Checking STB lines : Normal mode, 50MHz
- Statebus : Checking STB lines : Normal mode, 80MHz
- Statebus : Checking STB lines : Normal mode, 100MHz
- Statebus : Checking State number lines : Extended mode, 25MHz
- Statebus : Checking State number lines : Extended mode, 30MHz
- Statebus : Checking State number lines : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 25MHz

- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 30MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 80MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test

%FAIL - Testing MultiClock Chan 161 [2:33], Period 1.28usec, Clocks Per Period 32
Measured: 93 expected: 96

%FAIL - Testing MultiClock Chan 161 [2:33], Period 1.28usec, Clocks Per Period 64
Measured: 186 expected: 192

%FAIL - Testing MultiClock Chan 161 [2:33], Period 12.8usec, Clocks Per Period 512
Measured: 0 expected: 1536

%FAIL - Testing MultiClock Chan 161 [2:33], Period 12.8usec, Clocks Per Period 576
Measured: 1722 expected: 1728

%FAIL - Testing MultiClock Chan 161 [2:33], Period 12.8usec, Clocks Per Period 640
Measured: 1914 expected: 1920

%FAIL - Testing MultiClock Chan 161 [2:33], Period 20usec, Clocks Per Period 768
Measured: 0 expected: 2304

%FAIL - Testing MultiClock Chan 161 [2:33], Period 20usec, Clocks Per Period 832
Measured: 2113 expected: 2496

%FAIL - Testing MultiClock Chan 161 [2:33], Period 20usec, Clocks Per Period 896
Measured: 2682 expected: 2688

%FAIL - Testing MultiClock Chan 161 [2:33], Period 20usec, Clocks Per Period 1000
Measured: 2994 expected: 3000

- Completed MultiClock test.
- Starting SCIO test

- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 2

%JOB_END - ****FAILED**** Channel_Board_DIB Module Check of slot 2 (S/N:5007DC3) at 3:49:03 PM

%JOB_START - Beginning Channel_Board_DIB Module Check test on slot 3 at 3:49:10 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Completed Commpare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode

- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus : Checking STB lines : Normal mode, 30MHz
- Statebus : Checking STB lines : Normal mode, 50MHz
- Statebus : Checking STB lines : Normal mode, 80MHz
- Statebus : Checking STB lines : Normal mode, 100MHz
- Statebus : Checking State number lines : Extended mode, 25MHz
- Statebus : Checking State number lines : Extended mode, 30MHz

- Statebus : Checking State number lines : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 25MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 30MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 80MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 3

%JOB_END - ****PASSED**** Channel_Board_DIB Module Check of slot 3 (S/N:50085B4) at 3:53:33 PM

%JOB_START - Beginning Channel_Board_DIB Module Check test on slot 4 at 3:53:40 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...

- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Completed Commpare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz

- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus : Checking STB lines : Normal mode, 30MHz
- Statebus : Checking STB lines : Normal mode, 50MHz
- Statebus : Checking STB lines : Normal mode, 80MHz
- Statebus : Checking STB lines : Normal mode, 100MHz
- Statebus : Checking State number lines : Extended mode, 25MHz
- Statebus : Checking State number lines : Extended mode, 30MHz
- Statebus : Checking State number lines : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 25MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 30MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 80MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.

- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 4

%JOB_END - ****PASSED**** Channel_Board_DIB Module Check of slot 4 (S/N:C0B2DC7) at 3:58:04 PM

%JOB_START - Beginning Channel_Board_DIB Module Check test on slot 5 at 3:58:11 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Completed Commpare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode

- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test

%FAIL - Testing Frequency Counter Chan 350 [5:30]
Measured: 0xFFFA expected: 0xFFFF

- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus : Checking STB lines : Normal mode, 30MHz
- Statebus : Checking STB lines : Normal mode, 50MHz
- Statebus : Checking STB lines : Normal mode, 80MHz

- Statebus : Checking STB lines : Normal mode, 100MHz
- Statebus : Checking State number lines : Extended mode, 25MHz
- Statebus : Checking State number lines : Extended mode, 30MHz
- Statebus : Checking State number lines : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 25MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 30MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 80MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test

%FAIL - Testing MultiClock Chan 350 [5:30], Period 1.28usec, Clocks Per Period 64
Measured: 189 expected: 192

%FAIL - Testing MultiClock Chan 350 [5:30], Period 12.8usec, Clocks Per Period 512
Measured: 1533 expected: 1536

%FAIL - Testing MultiClock Chan 350 [5:30], Period 12.8usec, Clocks Per Period 576
Measured: 1725 expected: 1728

%FAIL - Testing MultiClock Chan 350 [5:30], Period 12.8usec, Clocks Per Period 640
Measured: 1917 expected: 1920

%FAIL - Testing MultiClock Chan 350 [5:30], Period 20usec, Clocks Per Period 768
Measured: 2301 expected: 2304

%FAIL - Testing MultiClock Chan 350 [5:30], Period 20usec, Clocks Per Period 832
Measured: 2493 expected: 2496

%FAIL - Testing MultiClock Chan 350 [5:30], Period 20usec, Clocks Per Period 896
Measured: 2685 expected: 2688

%FAIL - Testing MultiClock Chan 350 [5:30], Period 20usec, Clocks Per Period 1000
Measured: 2997 expected: 3000

%FAIL - Testing MultiClock Chan 363 [5:43], Period 1.28usec, Clocks Per Period 64
Measured: 189 expected: 192

%FAIL - Testing MultiClock Chan 363 [5:43], Period 12.8usec, Clocks Per Period 576
Measured: 1725 expected: 1728

%FAIL - Testing MultiClock Chan 363 [5:43], Period 12.8usec, Clocks Per Period 640
Measured: 1917 expected: 1920

%FAIL - Testing MultiClock Chan 363 [5:43], Period 20usec, Clocks Per Period 896
Measured: 2685 expected: 2688

%FAIL - Testing MultiClock Chan 363 [5:43], Period 20usec, Clocks Per Period 1000
Measured: 2997 expected: 3000

- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 5

%JOB_END - ****FAILED**** Channel_Board_DIB Module Check of slot 5 (S/N:C067944) at 4:02:34 PM

%JOB_START - Beginning Channel_Board_DIB Module Check test on slot 6 at 4:02:42 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
- Continuing Drive Level tests

- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Completed Commpare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.

- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus : Checking STB lines : Normal mode, 30MHz
- Statebus : Checking STB lines : Normal mode, 50MHz
- Statebus : Checking STB lines : Normal mode, 80MHz
- Statebus : Checking STB lines : Normal mode, 100MHz
- Statebus : Checking State number lines : Extended mode, 25MHz
- Statebus : Checking State number lines : Extended mode, 30MHz
- Statebus : Checking State number lines : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 25MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 30MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 80MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 6

%JOB_END - ****PASSED**** Channel_Board_DIB Module Check of slot 6 (S/N:5009FD3) at 4:07:05 PM

%JOB_START - Beginning Channel_Board_DIB Module Check test on slot 7 at 4:07:12 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Completed Commpare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode

- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus : Checking STB lines : Normal mode, 30MHz
- Statebus : Checking STB lines : Normal mode, 50MHz
- Statebus : Checking STB lines : Normal mode, 80MHz
- Statebus : Checking STB lines : Normal mode, 100MHz
- Statebus : Checking State number lines : Extended mode, 25MHz
- Statebus : Checking State number lines : Extended mode, 30MHz
- Statebus : Checking State number lines : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 25MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 30MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 50MHz

- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 80MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 7

%JOB_END - ****PASSED**** Channel_Board_DIB Module Check of slot 7 (S/N:8025A4A) at 4:11:36 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 0 at 4:11:43 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 0 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 0 at 4:11:44 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 1 at 4:11:51 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------

- Running Relay Checker Ver 1.03 on AG012 in Slot 1 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 1 at 4:11:51 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 2 at 4:11:59 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 2 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 2 at 4:11:59 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 3 at 4:12:06 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 3 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 3 at 4:12:07 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 4 at 4:12:14 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 4 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 4 at 4:12:15 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 5 at 4:12:22 PM on 8/26/2022

Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 5 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 5 at 4:12:23 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 6 at 4:12:30 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 6 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 6 at 4:12:30 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 7 at 4:12:38 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG012 in Slot 7 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 7 at 4:12:38 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 0 at 4:12:45 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO -
---------- %
INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40

%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 0 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 0 at 4:12:46 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 1 at 4:12:53 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 1 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 1 at 4:12:54 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 2 at 4:13:01 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 2 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 2 at 4:13:02 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 3 at 4:13:09 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 3 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 3 at 4:13:09 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 4 at 4:13:16 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 4 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 4 at 4:13:17 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 5 at 4:13:24 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 5 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 5 at 4:13:25 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 6 at 4:13:32 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 6 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 6 at 4:13:33 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 7 at 4:13:40 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------

%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Running Relay Checker Ver 1.03 on AG009 in Slot 7 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 7 at 4:13:40 PM

%JOB_START - Beginning DPS_DIB Module Check test on slot 21 at 4:13:48 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- The license of DPS_MOUT is not available.
- DPS_MOUT Check Skipped.

%JOB_END - ****PASSED**** DPS_DIB Module Check of slot 21 (S/N:5008BEC) at 4:13:58 PM

%JOB_START - Beginning DPS_DIB Module Check test on slot 22 at 4:14:05 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
%FAIL - Slot 22 channel 4 DPS Current measure test at 25MA on 100MA range
Measured using cal CUB source impedance
Measured: -7.385E-04 low limit: 2.361E-02 high limit: 2.620E-02

%FAIL - Slot 22 channel 4 DPS Current measure test at 5MA on 10MA range
Measured using cal CUB source impedance
Measured: 2.256E-04 low limit: 4.749E-03 high limit: 5.249E-03

%FAIL - Slot 22 channel 4 DPS Current measure test at 250UA on 500UA range
Measured using cal CUB source impedance
Measured: -2.354E-05 low limit: 1.874E-04 high limit: 3.124E-04

- The license of DPS_MOUT is not available.
- DPS_MOUT Check Skipped.

%JOB_END - ****FAILED**** DPS_DIB Module Check of slot 22 (S/N:8011BD8) at 4:14:15 PM

%JOB_START - Beginning DPS_DIB Module Check test on slot 23 at 4:14:22 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
%FAIL - Slot 23 channel 7 external measurement of test current in uA
100mA range used
Measured: -1935.545 low limit: -1200 high limit: -800

- The license of DPS_MOUT is not available.
- DPS_MOUT Check Skipped.

%JOB_END - ****FAILED**** DPS_DIB Module Check of slot 23 (S/N:802F900) at 4:14:32 PM

%JOB_START - Beginning DPS_DIB Module Check test on slot 24 at 4:14:39 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
%FAIL - Slot 24 channel 7 DPS Guard voltage test at 3.3V
Measured: 5.799E-02 low limit: 2.917 high limit: 3.683

%FAIL - Slot 24 channel 7 DPS Guard voltage test at 5V
Measured: 6.623E-02 low limit: 4.43 high limit: 5.57

%FAIL - Slot 24 channel 7 DPS Guard voltage test at 10V
Measured: 0.07264 low limit: 8.88 high limit: 11.12

- The license of DPS_MOUT is not available.
- DPS_MOUT Check Skipped.

%JOB_END - ****FAILED**** DPS_DIB Module Check of slot 24 (S/N:500268E) at 4:14:49 PM

%JOB_START - Beginning CTO Module Check test on slot 17 at 4:14:56 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO -
---------- %
INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40

%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Performing CTO calibration test...
- Performing RAM test...

%JOB_END - ****PASSED**** CTO Module Check of slot 17 (S/N:5008EA3) at 4:15:00 PM

%JOB_START - Beginning CTO_DIB Module Check test on slot 17 at 4:15:07 PM on 8/26/2022
Workbook Rev V7.60.42 IG-XL Version: 3.50.40 (P13) DIB: P/N 23905203 S/N 2C4D7C Rev 1135B

%INFO - ----------
%INFO - System (J750)
%INFO - IG-XL 3.50.40 (P13), Build: 04.20.12.23.40
%INFO - Maintenance 7.60.42, Build: 10.04.10.11.04
%INFO - ----------
- Performing internal reference test...
- Performing VREF voltage test...
- Performing PPMU force V test...
- Performing PPMU force I test...
- Performing PPMU measure V test...
- Performing PPMU measure I test...
- Performing Source voltage test...
- Performing Capture voltage test...
- Performing Source/Capture loopback test...
- Performing Source burst test...
- Performing Capture burst test...
- Performing Source/Capture loopback burst test...

%JOB_END - ****PASSED**** CTO_DIB Module Check of slot 17 (S/N:5008EA3) at 4:17:11 PM